



Article

# Synthesis, Characterization and Power Factor Estimation of SnSe Thin Film for Energy Harvesting Applications

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Abstract: In this work, a simple, cost-effective successive ionic layer adsorption and reaction (SILAR) deposition technique has been used to deposit a high-quality tin selenide (SnSe) thin film onto a glass substrate. Structural, morphologic, and thermoelectric properties have been characterized for the prepared thin film. X-ray diffraction (XRD) results of the SnSe thin film reveal an orthorhombic structure phase. The morphological properties of the prepared thin films have been studied using field emission scanning electron microscopy (FESEM). The stoichiometric composition of the deposited thin film and the elemental binding energies of the Sn and Se elements have been investigated with energy-dispersive spectroscopy (EDS) and X-ray photoelectron spectroscopy (XPS). The Fourier transformation infrared (FTIR) spectrum of the SnSe thin film displays vibrational modes of chalcogenides bonds. These results suggest that the developed thin film is crystalline, uniform, and without impurities and is appropriate for energy harvesting applications. The prepared thin film's Seebeck coefficient and electrical resistivity were estimated through ZEM-3 from room temperature to 600 K. The power factor was evaluated. A substantially high electrical conductivity is observed, which decreases somewhat with temperature, suggesting a semimetal conducting transport—the absolute values of the Seebeck coefficient increase with temperature. The resulting power factor showed the highest values near room temperature and a somewhat decreasing trend as the temperature increased. Despite lower values of the Seebeck coefficient, the substantially enhanced power factor is due to the higher electrical conductivity of the thin film, superior to that reported previously. This precursor study demonstrates promising results for developing high-performance flexible thermoelectric devices via a simple and facile SILAR strategy.

Keywords: SILAR; SnSe thin film; thermoelectric; power factor; energy harvesting



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# 1. Introduction

Thermoelectric energy harvesting technology provides solutions for sustainable energy challenges, which recycle low-grade waste heat into electricity with the added benefit of reducing greenhouse gas emissions [1,2]. The development of low-cost, high-performance thermoelectric devices is essential to the widespread adoption of this technology [3]. Recently, SnSe came into the limelight in research communities based on its exceptional thermoelectric properties, reported by Zhao and co-workers [2,4]. They proposed alternative strategies to nanostructuring for achieving an exceptionally high figure of merit [4]. In contrast to traditional thermoelectric materials, SnSe is composed of Earth-abundant nontoxic elements and is a simple and stable compound [5]. The crystal structure is layered

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orthorhombic (Pnma group) and highly anisotropic. At 773 K, the crystal experiences a structural transition from Pnma to Cmcm [4,6]. The maximum ZT~2.6 was observed in the thermoelectric characteristics of SnSe crystals at 923 K along the b-axis, which was attributed to ultralow thermal conductivity induced by gigantic phonon anharmonicity [4,6]. Apparently, this finding is tantamount to earlier innovations in superlattice or nanowire structures that resulted in higher values of ZT. However, industrial applications of SnSe through economically scaled-up processing are still considered a big milestone. Despite the inherent advantage (e.g., low thermal conductivity) of SnSe, which is accounted for by an exceptionally high ZT, SnSe still faces several challenges and requires substantial research activity for its early commercial deployment in the industry.

Several studies have been performed to improve the energy conversion efficiency of polycrystalline materials [7–12]. However, the reported ZT values in polycrystalline SnSe are substantially lower than the values observed in single crystals [13]. Some improvements were observed in polycrystalline SnSe by adopting different strategies such as alloying [14], doping [15], and microstructure modulation [16], but most of the ZT values are not larger than  $\sim$ 1 [5].

There are continued efforts for the fabrication of a polycrystalline SnSe thin film through facile and economical routes for applications in flexible thermoelectric materials and the Internet of Things (IoT). Moreover, the strong anisotropy in single-crystal SnSe diverted research toward fabricating textured SnSe thin film to achieve high-performance SnSe materials [17]. Less work has been reported for the development of a thin film based on SnSe materials. For instance, Feng et al. synthesized a SnSe thin film via the rapid microwave plasma chemical vapor deposition method [18]. The thermoelectric characterization showed an improved power factor at 600 K [18]. Similarly, Zhong et al. fabricated highly textured SnSe thin film through a one-step chemical vapor deposition technique and showed an improved power factor at higher temperatures [17]. Most recently, the thermoelectric characteristics of an epitaxially oriented SnSe thin film were investigated by Marjin et al., exhibiting an in-plane Seebeck coefficient similar to single-crystal values, thus leading to an increased power factor [19].

At present, numerous techniques have been used to synthesize SnSe thin films, such as chemical bath deposition [20], sputtering [21], thermal coating [22], hydrothermal deposition [23], pulse laser deposition [24], atomic layer electrochemical epitaxy [24], and electrochemical deposition [25]. Successive ionic layer adsorption and reaction (SILAR) is unique among these deposition methods since it has many advantages [26]. Primarily, it permits the preparation of thin films on a large scale. In addition, the thickness and stoichiometry of the thin films can be precise. Lastly, SILAR is generally an inexpensive technique in terms of application necessities. Klochko et al. [27] reported the deposition of Li-doped NiO films by the SILAR technique and revealed the effects on the structure's optical, electrical, and thermoelectric characteristics. Kumar et al. synthesized SnSe-based quantum dots through the SILAR method for high-performance solar cells [28]. Farhad et al. [29] described the deposition of cuprous oxide (Cu<sub>2</sub>O) thin films by a modified SILAR method on both glass and FTO substrates by controlling the pH level of the cationic precursor and the concentration of the anionic precursor to explain their influence on the physical properties of the deposited films. In order to obtain good-quality films, Deshpande et al. [30] reported on the fabrication of a tin disulfide (SnS<sub>2</sub>) thin film utilizing the SILAR process by varying the cycles, immersion time, rinse time, and deposition temperature. In this study, the deposition, characterization, and power factor estimation of a SILAR-deposited SnSe thin film has been analytically examined. The primary motivation of this research is to deposit nanostructural, uniform, and crystalline SnSe thin films and investigate their thermoelectric properties for energy harvesting applications.

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## 2. Experimental Details

## 2.1. Chemicals

 $SnCl_2 \cdot 2H_2O$  (Sigma Aldrich, St. Louis, MO, USA, 99.99%), NaOH (Sigma Aldrich, 99.99%), Se powder (Sigma Aldrich, 99.99%), and  $Na_2SO_3$  (Sigma Aldrich, 99.99%) were commercially procured.

# 2.2. Preparation of SnSe Thin Film

To deposit the tin selenide (SnSe) thin film, 0.5 M tin chloride (SnCl<sub>2</sub>·2H<sub>2</sub>O) was prepared in deionized water. The pH of the solution was adjusted to approximately equal to 11 by the addition of 1 M NaOH, and the prepared solution was used as the cation (Sn<sup>2+</sup>) source. The other solution, used as the anion (Se<sup>2-</sup>) source, was sodium selenosulfate (Na<sub>2</sub>SeSO<sub>3</sub>), prepared by mixing 2.77 g of sodium sulfite (Na<sub>2</sub>SO<sub>3</sub>) with 0.107 g of selenium powder in 50 mL of deionized water (DIW) for 6 h at 80 °C. The clean soda–lime glass substrates were dipped in the cation source solution for 30 s to adsorb Sn<sup>2+</sup> ions on the substrate surface. After that, the substrate was washed with deionized water for 10 s to eliminate loosely bound Sn<sup>2+</sup> ions. Then, the pre-adsorbed substrate was immersed in the Se<sup>2-</sup> source solution for 30 s for the reaction of Se<sup>2-</sup> ions with Sn<sup>2+</sup> ions to deposit the SnSe thin film. The unreacted ions were removed by rinsing in DIW for 10 s. Consequently, a SILAR progression cycle of SnSe deposition was accomplished. This deposition cycle was repeated to grow the SnSe thin film to the preferred thickness as shown in Figure 1.



Figure 1. The scheme of SILAR technique for the deposition of SnSe thin film.

The thickness of the prepared SnSe thin film was measured using the gravimetric method.

$$t = \frac{M}{\rho A}$$

where 'M' is the deposited mass of SnSe in grams, 'A' is the area of the thin film in cm<sup>2</sup>, and ' $\rho$ ' is the mass density of SnSe (6.18 g/cm<sup>3</sup>). The measured thickness of the film is about 161.8 nm [31].

## 2.3. Thin Film Characterization

The structural phase identification of the deposited SnSe thin film was performed by monochromatic PANalytical X'Pert Pro diffractometer with a CuK $\alpha$  X-ray source (wavelength  $\lambda$  = 1.5418 Å). The XRD data were analyzed with highscore pluse software version

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3.0e. The vibration modes of the prepared thin film were assessed by FTIR spectroscopy (Perkin Elmer 3 FT-NIR, Waltham, MA, USA). The morphology of the thin film was studied by the FESEM (JEOL JSM-7800F) microscope. The grain size of the thin film was estimated with Image–J software version 1.54. The composition of the prepared thin film was measured using the Oxford Instruments energy-dispersive X-ray analysis (EDAX) facility (Oxford xMax, Abingdon, Oxfordshire, UK) attached to the FE-SEM. The binding energy of the deposited materials was measured using X-ray photoelectron spectroscopy (JPS-9030).

#### 2.4. Thermoelectric Characterization

ZEM-3 from ULVAC Rikco, Yokohama, Japan was employed to evaluate the Seebeck coefficient and electrical conductivity of the thin film using a thin film adapter provided by the company. The thin film samples were mounted on an alumina pillar and connected at both ends with the electrodes through nickel foil caps. Using the static DC method (ULVAC, ZEM-3) in a He environment, the Seebeck coefficient and electrical resistivity were measured from room temperature to  $\sim\!600~\rm K$  and at temperature gradients of 20, 30, and 40. All measurements were taken carefully after ensuring the thin film samples were chemically and thermally stable at higher temperatures. The thermoelectric parameters were evaluated using the ZEM series analysis software version 1.01.

#### 3. Results and Discussion

### 3.1. Structural Analysis

The XRD results of the deposited SnSe thin film and the powder spectra of SnSe described with analogous plans (hkl) are depicted in Figure 2. The indexed diffraction peaks in the XRD pattern correspond to the orthorhombic SnSe structural phase with space group Pnma (JCPDS No.: 00-032-1382) represented by the red lines in Figure 2. Similar structure phase studies by Zainal et al. using the SILAR technique have been reported [32]. The spectrum reveals that the primary dominant peak diffraction is (111), demonstrating a clear association with the consistency of the SnSe thin film.

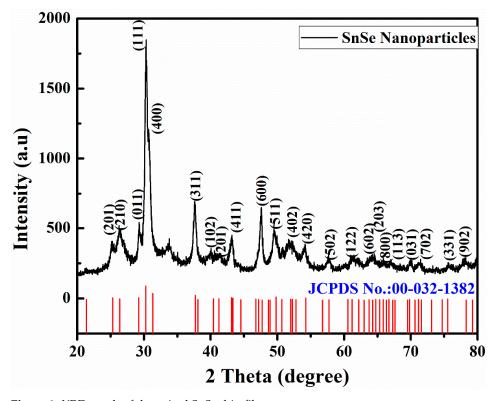


Figure 2. XRD result of deposited SnSe thin film.

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The structural parameters of the deposited SnSe have been computed using the following equations [33,34]:

$$D = \frac{0.94\lambda}{\beta \cos \theta} \tag{1}$$

$$\frac{1}{d^2} = \frac{h^2}{a^2} + \frac{k^2}{b^2} + \frac{l^2}{c^2} \tag{2}$$

$$\varepsilon = \frac{\beta}{4\tan\theta} \tag{3}$$

$$\delta = \frac{1}{D^2} \tag{4}$$

where 'D' is the crystallite size, ' $\lambda$ ' is the wavelength of Cu-K $\alpha$  = 1.5406 Å' X-rays, '20' is the peak position in radians, 'd' is the spacing between the plans, ' $\beta$ ' is full width at half maximum of the dominant peak, (h, k, l) are the indices of the planes, and (a, b, c) is the lattice constant. Table 1 provides the estimated nanostructure parameters obtained from XRD.

**Table 1.** Nanostructural parameters of deposited SnSe thin film.

Sample	Crystallite Dimension (nm)	$\begin{array}{c} \text{Microstrain} \\ (\epsilon \times 10^{-2}) \end{array}$	Deformation Density ( $\delta$ ) (m <sup>-2</sup> $\times$ 10 <sup>15</sup> )	<b>Lattice Constants</b>		
				a (Å)	b (Å)	c (Å)
SnSe	108.54	11.83	8.48	4.173	4.786	11.239

The prepared SnSe thin film was also examined by FTIR spectroscopy to support the pureness of the deposited SnSe and endorse the absorption modes. Figure 3 depicts the FTIR spectra of the deposited thin film at ambient temperature, which were obtained in the  $500-4000~\rm cm^{-1}$  range. The occurrence of an extensive peak at  $3401.50~\rm cm^{-1}$  is related to OH $^-$  [35], and the absorption modes' presence in the range of  $818-1525~\rm cm^{-1}$  corresponds to chalcogenide bonding [36]. The intense vibration peak at  $604.75~\rm cm^{-1}$  might be attributed to the Sn-Se bonding [37]. The water that was absorbed from the air may be the cause of the absorption peak at  $1667.88~\rm cm^{-1}$  [38].

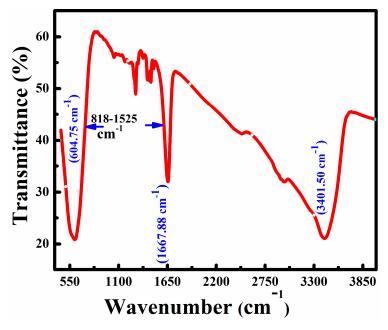


Figure 3. FTIR spectra of SnSe thin film.

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XPS analysis has been accomplished to investigate the chemical stoichiometry of the SnSe thin film. The XPS spectra (Figure 4a) disclose the presence of both Sn and Se elements in the deposited thin film. The groups present at 54.3 and 56.5 eV are analogous to Se-3d $_{3/2}$  and Se-3d $_{5/2}$ , corresponding to the occurrence of  $2^-$  valency of selenium, as depicted in Figure 4b. The existence of Sn-3d is revealed in Figure 4c, which can be allocated at about 485.7 eV and 494.4 eV, ascribed to Sn-3d $_{5/2}$  and Sn-3d $_{3/2}$ , respectively. It describes that the tin has  $2^+$  valency. According to the XPS data, Sn and Se showed  $2^+$  and  $2^-$  valence locations, respectively.

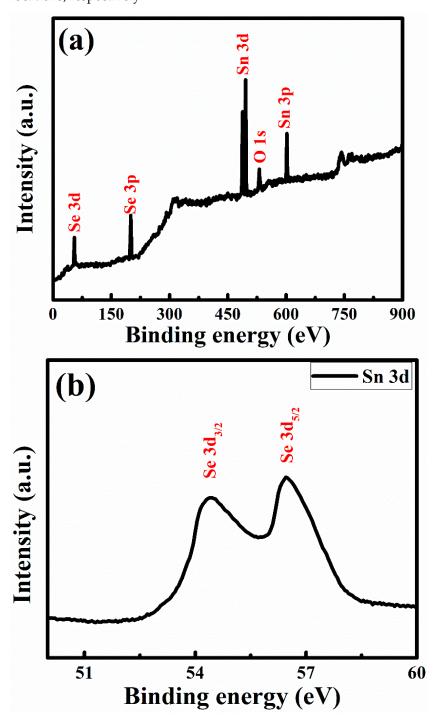


Figure 4. Cont.

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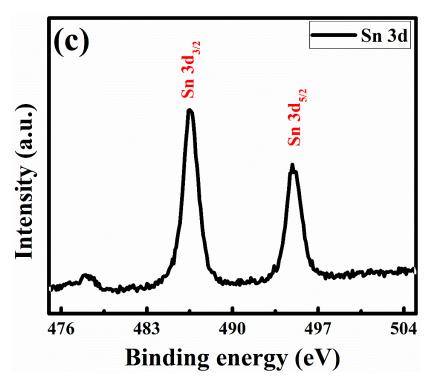


Figure 4. The XPS results: (a) full-range spectrum, (b) Se-3d, and (c) Sn-3d.

# 3.2. Surface and Composition Investigations

The peak centered at 495 eV is attributed to Sn-O bonding. It can be assumed that the O 1 s peak appeared because the grain surface of the SnSe thin film was exposed to oxygen in laboratory conditions. The XPS results agree with the conclusions reported for high-performance n-type SnSe synthesized by the arc-melting technique [39].

The morphological investigations of the prepared SnSe thin film were carried out using FESEM. From the FESEM image in Figure 5a, it is detected that the deposited thin film is uniformly deposited on the whole surface and is smooth without cracks. Furthermore, the micrographs of the thin films exposed almost regular nano-sized granular shape grains closely packed together to form bigger spherical structures. The grain size dispersion exposed that the average size is 132 nm, as measured with ImageJ software and shown in Figure 5b.

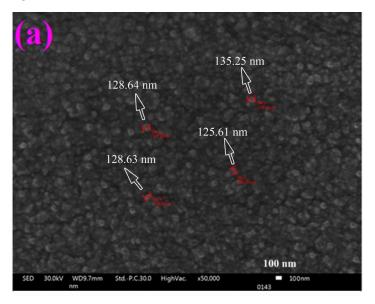


Figure 5. Cont.

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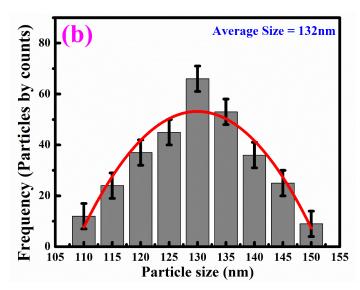
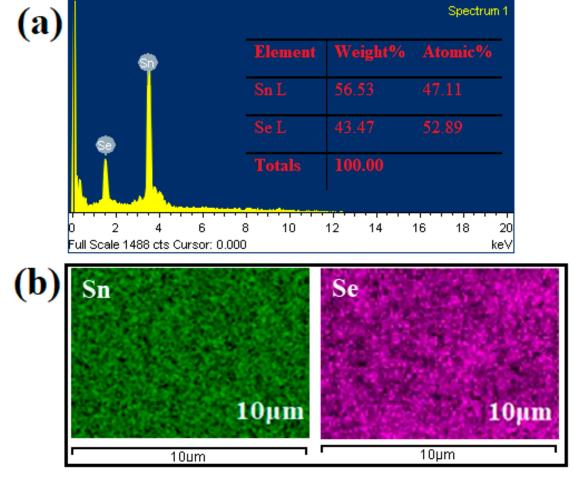


Figure 5. FESEM micrograph: (a) SnSe thin film and (b) particle distribution of the SnSe thin film.

The elemental composition of the SnSe thin film was measured by EDX analysis, as shown in Figure 6a. Figure 6a establishes that the SnSe consisted of weight fractions of Sn and Se of 56.43 and 43.47%, respectively. The EDX result shows no impurities in the thin film deposited. The EDX mapping investigation (Figure 6b) also discloses a uniform distribution of Sn and Se.

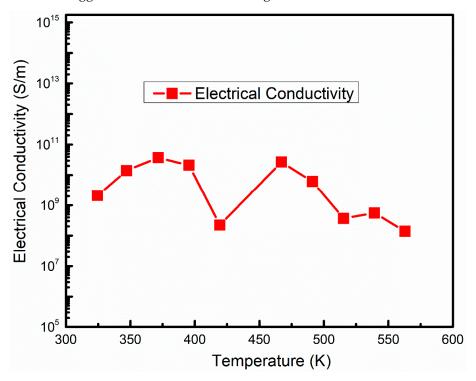


**Figure 6.** EDX analysis of (a) SnSe thin film and (b) elemental mappings of SnSe thin film with elements of Sn and Se.

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#### 3.3. Thermoelectric Properties

The electrical resistivity and Seebeck coefficient were measured through ZEM-3 simultaneously from room temperature to  $\sim\!600$  K. The thin film demonstrates decreasing electrical conductivity values with the increase in temperature as shown in Figure 7. This behavior suggests the semimetal conducting behavior of the thin film.



**Figure 7.** Electrical conductivities of the thin film with temperature.

The drop in electrical conductivity and subsequent rise with temperature has been reportedly previously in SnSe single crystals [40]. The decreasing and increasing electrical conductivity behavior with temperature is more significant in polycrystalline materials than in single crystals. It is highlighted that SnSe polycrystals exhibit this kind of behavior similar to hysteresis during heating and cooling due to some reversible processes, as observed in several studies [40]. This implies that there could be competing factors between semiconducting and metallic phases in the different temperature ranges, as described by Kuma et al. [41]. The electrical conductivity increases with temperature up to ~400 K, followed by a dip in the value at ~425, suggesting the dominance of semiconducting and metallic characteristics over different temperature regimes. This conductivity trend agrees well with several studies of SnSe-based materials exhibiting similar electrical conductivity behavior with temperature [40–42].

The electrical conductivity values are substantially higher than usually reported in the literature. The increase in electrical conductivity values is believed to be partly due to higher values of carrier concentration and partly due to the higher mobility of the charge carriers.

It is well established that electrical conductivity is closely related to carrier concentration and mobility through the relation  $\sigma = \mu^* e^* n$ , where  $\mu$  is the mobility of the charge carriers, q is the charge, and 'n' is the carrier concentration. It can be deduced that an increase in the value of n and/or mobility is the only way to impact the electrical conductivity values in the thin film directly. Duon et al. [43] conducted a similar study by preparing an n-type  $SnSe_2$  thin film on an alumina substrate. They attributed the higher electrical conductivity values to the higher carrier concentration. Moreover, Gu et al. [44] related the higher values of electrical conductivity to the higher values of carrier concentration in n-type SnSe polycrystals, which agrees with our results for electrical conductivity. It can be

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deduced that the higher electrical conductivity values could be primarily attributed to the higher carrier concentration values in this study in accordance with the literature [43,44].

A similar result of a dramatic increase in the electrical conductivity of an n-type SnSe polycrystalline material was reported by Guo et al. [44]. They attributed the substantial increase in the electrical conductivity values to higher concentrations of the charge carriers [44]. The substantial increase in the electrical conductivity values is similar to a previous report [45].

The temperature-dependent Seebeck coefficient of the thin film is shown in Figure 8. The negative sign of the values suggests that most of the charge carriers are composed of electrons. The absolute values of the Seebeck coefficient increase with temperature. The highest value of the Seebeck coefficient is ~30  $\mu V/K$ . Our values of the Seebeck coefficients are substantially lower than the reported values in the literature for this thin film [46].

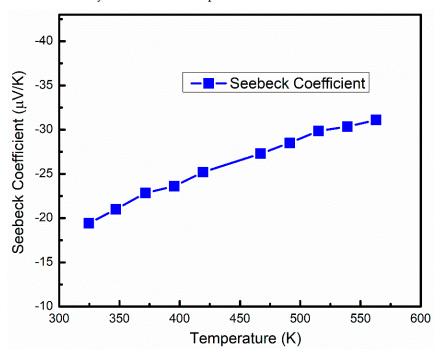


Figure 8. Seebeck coefficient of the thin film with temperature.

The Seebeck coefficient mainly depends on the carrier concentration, carrier effective mass, and temperature. The higher electrical conductivity values are generally considered to be the effect of an increase in carrier concentration, which is consistent with the lower values of the Seebeck coefficient in this study [19]. This suggests that the substantially lower values of Seebeck coefficients agree with the higher electrical conductivity values of the thin film, as both thermoelectric parameters have an inverse relationship with carrier concentration [47].

In a similar study for low-temperature applications, Urmila et al. fabricated a SnSe thin film on a glass substrate using the reactive evaporation method [48]. The group evaluated the Seebeck coefficient and electrical conductivity at lower temperatures up to 300 K. A dramatic increase in the Seebeck coefficient was observed below 84 K. This behavior was attributed to the effect of phonon drags on the charge carriers. In addition, this enhancement resulted from phonon interactions with the mobile charge carriers, and both carriers were combinedly responsible for the unprecedented increase in the Seebeck coefficient [48]. Consequently, an enhanced power factor of the SnSe thin film was achieved.

The resulting values of the power factors as a function of temperature are shown in Figure 9. The power factor has been improved substantially due to the huge electrical conductivity. Compared to the present study, Song et al. reported lower power factor values for a SnSe thin film fabricated by single-target magnetron sputtering [46]. The highest values of power factors are achieved at ~375 and ~470 K. The result suggests that

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the higher electrical conductivity values could result from the SILAR fabrication method, which formed a completely smooth surface of thin film without cracks, as shown in the SEM image in Figure 5a. Furthermore, the almost regular nano-sized granular-shaped grains closely packed together to form bigger spherical structures are also favorable for smooth carrier transport in the thin film, which could be responsible for the higher mobility of the charge carriers.

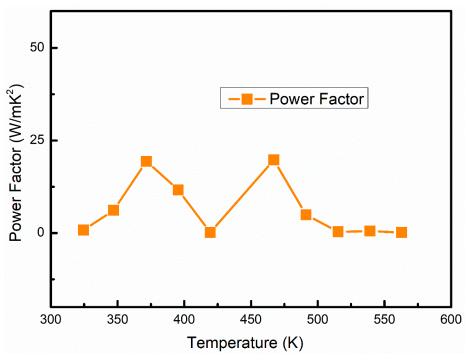


Figure 9. Power factor of the thin film with temperature.

The carrier mobility and concentration in SnSe polycrystalline thin film are believed to be considerably higher than the reported results. In general, the counterpart studies show lower values of electrical conductivity due to the strong anisotropy and large number of defects generally observed in polycrystalline SnSe caused by the segregation of Sn [4,5,13]. In contrast, our study shows a considerably improved power factor due to the excellent formation of the thin film using the SILAR method.

#### 4. Conclusions

A SnSe thin film was deposited by the SILAR method on glass substrates for evaluations of structural, morphological, and thermoelectric properties. Several investigative tools were used to evaluate the prepared thin film's structural phase, chemical bonding, morphological details, and thermoelectric properties. XRD investigation reveals that the SnSe thin film shows an orthorhombic structure, and the crystallite size of the prepared film is about 108.54 nm. The scanning electron microscope image reveals that the SnSe is regularly dispersed on the glass substrate. The prepared materials have deliberately closely packed nanostructures of an average grain size of 132 nm. The elemental investigation and the structural bonding of the film were determined from XPS, EDS, and FTIR analysis. From the FTIR spectra, a broad peak at 818–1525 cm<sup>-1</sup> is related to the absorption modes corresponding to chalcogenide bonding. The thermoelectric properties of the thin film were estimated using ZEM-3 from room temperature to ~600 K. The electrical conductivity, in general, decreases with temperature, suggesting a semimetallic conducting behavior. The substantially higher conductivity values suggest an increased concentration and mobility of charge carriers in the thin film.

In contrast, lower values of the Seebeck coefficient are observed in agreement with the literature, and its values increase with temperature increase. The results suggest that Processes **2024**, 12, 665

substantially improved carrier concentration and mobility could be the prime factors for a higher electrical conductivity of the thin film. Consequently, superior values of the power factor were observed.

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