



Article Generalized Normal Distribution Algorithm-Based Control of 3-Phase 4-Wire Grid-Tied PV-Hybrid Energy Storage System

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Abstract: The presented work employs the multiple random feature kernel mean p-power algorithm (MRFKMP) for the voltage source converter (VSC) control of a three-phase four-wire grid-tied dualstage photovoltaic-hybrid energy storage system (HESS) to achieve multiple objectives during various induced dynamic conditions. The proposed control enables the VSC to accomplish manifold goals, i.e., reactive power compensation, power quality enhancement, load, power balancing at common coupling point and grid voltage balancing during unity power factor mode of operation. The proposed system is scrutinized under steady-state and numerous dynamic states such as irradiation variation, specified power mode, abnormal grid voltage, load, and grid voltage unbalancing. The seamless control facilitates the swift resynchronization of the grid as well as maintaining stability during islanding and re-synchronization operations while satisfying the necessary load requirements. The associated HESS consisting of battery and ultra-capacitor is competent enough in managing the interruptions occurring on the grid, load and photovoltaic side. The DC bus voltage is controlled by the PI controller, which is tuned by the generalized normal distribution algorithm and kept at the desired level during diverse operating conditions. The optimized DC bus generates an accurate loss component of current and further enhances the VSC performance. The proposed system is investigated by simulation and found acceptable as per IEEE 519 standards.

Keywords: PV; power electronics; power quality; adaptive control; hybrid energy storage system; optimization

1. Introduction

Renewable energy systems (RES), such as photovoltaic (PV) and wind energy systems (WES), have established themselves as a dependable and sustainable green energy source that is also adaptable to existing grids [1]. Countries including the US, China, India and Japan are investing heavily in the field of grid integrated as well as isolated RES at utility-scale and distributed generator (DG) scale [2,3]. The cost of PV energy production has been reduced by 90% in the last decade with the help of favorable government policies worldwide and the latest technological advancements in the power electronics field, resulting in appreciable consumer acceptability of PV as a reliable source. The Solar Energy Corporation of India (SECI) recorded the lowest cost of a PV tariff in 2020 at 0.027 US cents per kWh. Moreover, India also records a 27% lower installation cost per kWh as compared to the rest of the world [4,5]. The Western world aims to reduce carbon footprints to 55% by 2030 and achieve net-zero emission by 2050 with RES [6].

PV power quality and control are heavily reliant on its power electronics counterpart, i.e., voltage source converters (VSC), DC-DC converters, etc. The VSC control fulfils multiple goals, i.e., harmonics elimination, reactive power compensation of the load, power



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Copyright: © 2021 by the authors. Licensee MDPI, Basel, Switzerland. This article is an open access article distributed under the terms and conditions of the Creative Commons Attribution (CC BY) license (https:// creativecommons.org/licenses/by/ 4.0/). balancing at point of common coupling (PCC) managing grid voltage sag and swell, power quality enhancements, maintaining the unity power factor (UPF), while ensuring the stability of the system [7,8]. The VSC bridges the DC and AC sides of the system by converting the DC power to AC power, providing an adequate switching sequence [9]. Numerous VSC controls have been investigated and implemented by researchers, which may be classified as time-domain controls (conventional controls), frequency domain control, adaptive controls, predictive controls, artificial intelligence (AI)-based controls, etc. [10]. Because of their reduced computational complexity and ease of use, conventional controls such as synchronous reference frame (SRF) theory, power balance theory (PBT), etc. [7] were widely used in early research. The frequency-domain controls, e.g., Fourier series theory-based controls, Kalman filter-based control (KFC) [11], Stockwell transformation (ST) [12], Hilbert transformation theory (HT) [12], etc., did not gain wide acceptance as time-domain controls due to their high computational complexity and convergence time. The adaptive controls, such as least mean square (LMS) [13], least mean fourth (LMF) [14], and maximum correntropy criteria (MCC) [15], are among the many widely employed algorithms due to their better noise rejection capability and lower computational burden. Many derivatives of the above-mentioned adaptive algorithm have been proposed, e.g., admittance-based LMS (ALMS) [16], hyperbolic cosine LMS (HCLMS) [17], variable step-size LMS (VSSLMS) [18], combined LMS-LMF [19], reweighted zero attracting MCC (RWZMCC) [20], variable parameters zero attracting LMS (VPZALMS) [21], adaptive kernel width MCC (AKWMCC) [22] method, multiple random feature kernel p-power mean (MRFKPM) [23], etc., and predictive controls, e.g., dead beat control, model predictive control (MPC), trajectory-based controls [24,25], etc. The MPC is the most important predictive control for VSC control. It requires a keenly developed cost function to represent the system, whereas dead beat control requires a modulator to generate a proper switching sequence. The AI-based controls, i.e., fuzzy, and neural network (NN), have been implemented in DC bus stabilization, and the PI controller gains tuning, but the whole VSC control by AI technique brings a huge computational burden [26]. The optimization techniques of conventional and meta-heuristic (swarm and computational intelligence) techniques have been around for a while and are continuously evolving [27]. Nature-inspired optimization techniques, e.g., genetic algorithms (GA), particle swarm optimization (PSO) [28], salp swarm optimization (SSO) [29], mantra ray foraging optimization (MRFO) [30], Jaya algorithm [31], generalized normal distributed optimization (GNDO) [32], etc., have found applications in DC link control, DC–DC converter control and optimal parameter estimation of the VSC control.

The seamless control facilitates the isolation and re-synchronization of the grid with the system [33]. The isolation may be intentional or unintentional, resulting in huge transients, diminishing the stability and putting stress on the VSC switches. The transition from grid-connected mode to isolated mode and back to grid-connected mode is done swiftly and smoothly by proper seamless control. The isolated and grid-tied control of VSC may be based on current control or voltage control strategies depending on the system configuration. In references [34], voltage amplitude regulation-based novel seamless control transfer strategy as the VSC control transferred from current control mode to voltage control is implemented for grid-tied mode and switched to voltage control for isolated mode. In [36], the adaptive droop control strategy is implemented for voltage and frequency matching, where VSC control is provided by voltage control in both grid-tied and isolated mode.

The hybrid energy storage system (HESS) can overcome the shortcomings of the PV system and improve its reliability during bad weather conditions and at night. The elements of the energy storage system are selected based on load requirements, availability of storage type, and system configurations. The vastly employed energy storage elements are the battery (lead-acid, Li-ion, vanadium battery), proton exchange membrane fuel cell, ultra-capacitor (UC), superconducting magnetic energy storage, flywheel, hydrogen gas storage, compressed air storage, etc. [37–39]. The application of batteries, UC and fuel cells

is very extensive due to their availability, scalability and reduced reaction time, and they may be configured to deliver the necessary energy and power density.

In the presented work, a multiple random feature kernel mean p-power (MRFKMP)based adaptive algorithm is proposed for the VSC control of a three-phase four-wire grid-tied dual-stage PV-HESS system. Generally, the Gaussian kernel-based adaptive filter is widely used to counter Gaussian and non-Gaussian noises. The multi-kernel adaptive filter (MKAF) is proposed to select a particular kernel depending on the noise pattern. The MKAF demands a large memory space that keeps on increasing with every iteration, and the network matrix approximation methods further increase the computational burden. Therefore, to reduce the computation complexity and increase the convergence speed, the minimum kernel mean p-power-based MKAF is proposed as the MRFKMP algorithm. The presented system fulfils multiple goals during various dynamic conditions, such as harmonics suppressions, irradiation variation, load unbalancing, specified power delivery restraints, grid voltage unbalancing, and voltage sag and swell. The seamless control smoothly transfers (without major transients on the grid and load side) the VSC control from grid current control (GCC) in grid-integrated mode to voltage control in grid isolated mode and back to GCC in grid re-synchronization mode. The presented system is supported by HESS, consisting of a lead-acid battery and a UC. The UC with a very fast reaction time and a battery with a moderate reaction time can manage any type of irregularities occurring in the system, such as faults, insolation change and voltage sag, swell, etc. The DC bus voltage (V_{DC}) control is provided by the PI regulator, which is optimally tuned by GNDO to maintain the DC link voltage at the desired level during all dynamic conditions. The stable DC link further improves the power transfer to the grid while reducing the size of the associated coupling capacitor and stress on solid-state switches. The proposed system performs satisfactorily as per IEEE519 standards [40]. The main contributions to the paper are:

- a. The GNDO algorithm is utilized for the optimal gain tuning of the PI controller to secure the V_{DC} during various induced dynamic conditions in the system and generate the accurate loss component of current for enhanced VSC performance.
- b. MRFKMP-based adaptive control of VSC is employed, that accurately extracts the fundamental load current component, delivers a better convergence rate with less computational complexity and performs better than other adaptive controls during both steady-state and dynamic state operations.

The rest of the paper is organized as follows: Section 2 describes the proposed topology and its description; Section 3 presents the implemented control strategies—VSC control, Seamless control, DC-DC bi-directional converter control and GNDO optimized DC link voltage control; Section 4 describes the results and discussions of the presented system during various induced dynamic conditions; Section 5 shows the main conclusions of the research work.

2. System Description

The topology of the presented system is shown in Figure 1. The PV array of 32 kW is simulated by keeping 'Kyocera GT200' module parameters as standard values. The incremental conductance (InC) algorithm is implemented to retain maximum power out of the PV system and provide a duty cycle for DC–DC boost converter control. The boost converter stabilizes the PV voltage (V_{PV}) during irradiation variations and delivers the power to the DC bus. The battery and UC are also connected to the DC bus via bi-directional buck-boost converters to facilitate the two-way power communication between the energy HESS and the DC bus. Each buck-boost converter is provided with an individual current control mechanism for the battery and the UC. The three-phase four-wire VSC bridges the DC bus coupling capacitor to the rest of the AC system. The RC ripple filter and interfacing inductor are connected at the PCC to reduce the voltage and current ripples of the system and maintain the power quality of the grid. The non-linear load of 17 kW and the three-phase four-wire grid of 415 V (r.m.s.) at 50 Hz (via a master control switch)



are also connected to the PCC. The master control switch allows the bidirectional flow of current to and from the grid.

Figure 1. Block diagram.

3. System Controlling Strategies

The proposed system is governed by several controls: MRFKMP-based VSC control, seamless control for islanding and re-synchronization operation, bi-directional buck-boost converter control of HESS, DC bus voltage control by GNDO tuned PI and InC-based maximum power extraction.

3.1. Optimization-Based V_{DC} Control

The V_{DC} is controlled by a conventional PI controller. To improve the DC bus stability during dynamic conditions, the gains of the PI controller are tuned by GA, GA with pattern search and the GNDO algorithm. The DC bus voltage error (V_{err}) is converted into the integral square error (ISE), and provided to the optimization algorithm as a cost function to be minimized with tuned gains of the PI controller.

The GNDO algorithm is inspired by the Gaussian distribution, being simple to implement as no special controlling parameters are required other than design variable D, upper and lower bounds (u_j and l_j), population size N and termination criteria. The GNDO function is implemented in two steps, local exploration and global exploitation, as shown in Figure 2.



Figure 2. GNDO flow chart.

The GNDO algorithm is initiated by selecting an appropriate N value and calculating the fitness function as the distance between the mean and the position of the *i*th individual x_i^t as per Equation (1). The best individual x_{best} is selected based on the fitness function.

In our problem, two design variables are considered as PI controller gains (k_p and k_i) to be tuned by GNDO to reduce variation in V_{DC}; N = 30, D = 2, λ_1 is the random number between 0 and 1; and $u_i = [10,10]$ and $l_i = [0,0]$ are chosen in the presented problem.

$$x_{i,j}^{t} = l_{j} + (u_{j} - l_{j}) * \lambda_{1}$$
 where $i = 1, 2, 3, ..., N, j = 1, 2, 3, ..., D$ (1)

The total number of the iterations was selected as t = 50 and updated with every iteration. The local exploitation or global exploration step is selected depending on the randomly generated number ($0 < \alpha < 1$). During local exploitation, mean position M, generalized mean position μ_i , standard variance δ_i and penalty factor η are calculated as per Equations (2)–(5). The relation between a normal distribution and an individual's distribution is represented by the generalized normal distribution model as per Equation (6):

$$M = \frac{\sum_{i=1}^{N} x_i^t}{N} \tag{2}$$

$$\mu_i = \frac{1}{3}(x_i^t + x_{Best}^t + M))$$
(3)

$$\delta_i = \sqrt{\frac{1}{3} \left[\left(x_i^t - \mu_i \right)^2 + \left(x_{Best}^t - \mu_i \right)^2 + \left(M - \mu_i \right)^2 \right]} \tag{4}$$

$$\eta = \left\{ \begin{array}{c} \sqrt{-\log(\lambda_1)} * \cos(2\pi\lambda_2), \text{ if } a \le b\\ \sqrt{-\log(\lambda_1)} * \cos(2\pi\lambda_2 + \pi), \text{ otherwise} \end{array} \right\}$$
(5)

The *a*, *b*, λ_1 and λ_2 are selected as a random variable between 0 and 1, and x_{Best}^t is the best position in the current iteration. The *i*th individual x_i^t is pushed towards the x_{Best}^t . The local exploitation is completed with a screening step that avoids the chances of getting trapped in local minima and pushes the best individual in the next-generation population as per Equations (6) and (7).

$$v_i^t = \mu_i + \delta_i * \eta, \text{ where } i = 1, 2, 3 \dots N$$
(6)

$$x_i^{t+1} = \left\{ \begin{array}{c} v_i^t, \text{ if } f(v_i^t) \le f(x_i^t) \\ x_i^t, \text{ otherwise} \end{array} \right\}$$
(7)

The global exploration is carried out based on local and global information sharing as per Equation (8), in which λ_3 and λ_4 are a random number concerning the standard normal distribution, and β is the adjusted parameter between 0 and 1. The v_1 and v_2 are calculated as per Equations (9) and (10), where *p*1, *p*2, and *p*3 are the random integers between 1 to N, but they cannot be $p1 \neq p2 \neq p3 \neq i$.

$$v_i^t = x_i^t + \underbrace{\beta(|\lambda_3| * v_1)}_{\text{Local information}} + \underbrace{(1 - \beta)(|\lambda_4| * v_2)}_{\text{slobal information}}$$
(8)

$$v_{1} = \left\{ \begin{array}{c} x_{i}^{t} - x_{p1}^{t}, \text{ if } f(x_{i}^{t}) \leq f\left(x_{p1}^{t}\right) \\ x_{p1}^{t} - x_{i}^{t}, \text{ otherwise} \end{array} \right\}$$
(9)

$$v_{2} = \left\{ \begin{array}{c} x_{p2}^{t} - x_{p3}^{t}, \ if \ f\left(x_{p2}^{t}\right) \leq f\left(x_{p3}^{t}\right) \\ x_{p3}^{t} - x_{p2}^{t}, \ otherwise \end{array} \right\}$$
(10)

The global exploration ends with the screening step as per Equation (7), and the optimization follows the same steps for all iterations until the termination criteria are reached.

3.2. VSC Control

The MRFKMP-based VSC with random Fourier feature approximated network structure reduces calculation burden and storage cost, and ensures freedom to choose kernel parameters for multiple kernels, as shown in Figure 3. The Gaussian kernel is an obvious choice for kernel adaptive filters (KAF) due to its universal approximation capability. The choice of kernel and its parameters is specific to the application, which can be selected by cross-validation, penalizing function, etc. Due to the large computational burden, it is difficult to apply KAF methods and achieve desirable online learning efficiency. The Multiple KAF (MKAF) solves the problem of kernel parameter selection, but it suffers from a linearly growing network and a large memory requirement. The random Fourier feature approximation delivers the fixed size network matrix in MRFKMP, which is unrestrained from the input signal. The excess mean square error (EMSE) as a loss function can handle Gaussian noise. For both Gaussian and non-Gaussian noises, the mean p-power criteria-based loss function is utilized in kernel space.



Figure 3. VSC Control.

The error is e_{nx} , where x = a,b,c, for each phase is generated as per Equation (11) as a function of the load current i_{La} and the in-phase component μ_{pa} . μ_{px} , where x = a,b,c, is

calculated as per Equation (12). The V_d is voltage magnitude and v_{sx} , where x = a,b,c, is the phase to ground source voltage [15].

$$e_{nx} = i_{Lx}(n) - \mu_{px} * W_{px}(n)$$
, where x = a, b, c (11)

$$\mu_{px} = \frac{v_{sx}}{V_d} , \text{ where } x = a, b, c$$
(12)

The S(e(n)) is calculated as a function of the error signal and further utilized in evaluating each phase weight signal W_{Px} , where x = a,b,c, as per Equations (13)–(16).

$$S(e(n)) = exp\left(-\frac{e_{nx}^2}{2\epsilon^2}\right) \text{ where } \mathbf{x} = \mathbf{a}, \mathbf{b}, \mathbf{c}$$
(13)

$$W_{pa}(n+1) = W_{pa}(n) + \eta * (1 - S(e(n)))^{(p-2)/2} * S(e(n))\mu_{pa}e_{na}$$
(14)

$$W_{pb}(n+1) = W_{pb}(n) + \eta * (1 - S(e(n)))^{(p-2)/2} * S(e(n))\mu_{pb}e_{nb}$$
(15)

$$W_{pc}(n+1) = W_{pc}(n) + \eta * (1 - S(e(n)))^{(p-2)/2} * S(e(n))\mu_{pc}e_{nc}$$
(16)

The feed-forward term (W_{PV}) is utilized to balance the grid current and reduces the voltage oscillations on the DC bus as per Equation (17). The overall weight (W_{SP}) is calculated as a function of average weight W_P and W_{PV} as per Equations (18) and (19), and further utilized in generating the reference source currents i_{sx}^* , where x = a,b,c, as per Equation (20) [15].

$$W_{PV} = \frac{2}{3} \left(\frac{P_{PV}}{\sqrt{\frac{2}{3} \left(v_{sa}^2 + v_{sb}^2 + v_{sc}^2 \right)}} \right) = \frac{2}{3} \frac{P_{PV}}{V_d}$$
(17)

$$W_p = \frac{1}{3} \Big(W_{pa} + W_{pb} + W_{pc} \Big) \tag{18}$$

$$W_{sp} = W_p - W_{PV} \tag{19}$$

$$i_{sx}^* = W_{sp} * \mu_{px} \text{ where } x = a, b, c$$
(20)

During specified power mode operation, the W_{SP} is replaced by $W_{sp_{fix}}$ to supply a fixed amount of power to the grid, and $i^*_{Sx_{fix}}$ is generated with $W_{sp_{fix}}$ as per Equations (21) and (22).

$$W_{sp_{fix}} = \frac{2}{3} \left(\frac{P_{fix}}{\sqrt{\frac{2}{3} \left(v_{sa}^2 + v_{sb}^2 + v_{sc}^2 \right)}} \right) = \frac{2}{3} \frac{P_{fix}}{V_d}$$
(21)

$$i_{sx_{fix}}^* = W_{sp_{fix}} * \mu_{px} \text{ where } x = a, b, c$$
(22)

The i_{sx}^* , where x = a,b,c, or $i_{Sx_{fix}}^*$ are further provided to the hysteresis current controller (HCC) to generate the proper switching sequence for VSC.

3.3. Seamless Control

The seamless control facilitates the smooth transition of the VSC control from GCC during grid-tied operation to isolated voltage control during isolated operation, and back to GCC during grid re-synchronization operation of the system. With the initiation of the grid isolation, the grid V_d and frequency (*f*) are reduced to zero, and the reference signal selector switch chooses the re-synchronization reference current (i_{ref}^{ReSyn}) over the GCC reference currents (i_{ref}^{GCC}) . During islanded operation, reference voltage (v_{abc}^*) is multiplied with V_d and compared with the load voltage (v_{Labc}) , provided to the PI controller and further compared with the load currents (i_{Labcn}) to generate (i_{ref}^{ReSyn}) . The isolated operation of the system will be considered stable only when v_{Labc} maintains its frequency and amplitude.

When the grid is about to be synchronized, the VSC control shifts from islanded control to the re-synchronization control of VSC. The re-synchronization period continues up to 5–10 cycles at 50 Hz, until grid synchronization is confirmed, in which the frequency matching of load and source voltages (v_{Labc} , and v_{Sabc}) is done. If the comparison of V_d, f, and change in angular frequency ($\Delta\omega$) of the incoming grid with the reference values remain within desirable limits, that ensures the stability of the grid. The weak grid resynchronization may result in huge transients on grid voltage, current and frequency. Once the stability of the incoming grid is verified, the VSC control shifts from re-synchronization control to GCC, after checking the V_d and *f* again, as shown in Figure 3.

3.4. HESS Control

The HESS, consisting of lead-acid battery and UC, is employed to tackle the system interruptions of sudden and sustained nature, occurring on the grid, load and the PV side. The DC bus voltage (V_{DC}) control is provided with the conventional PI converter tuned with the GNDO algorithm. The loss component of current (I_{d1}) is considered as reference current for battery (I_{BAT}) and UC current (I_{UC}) control. The battery requires overcurrent protection to maintain the life cycle of the lead-acid battery, whereas the UC does not require overcurrent protection. The duty cycles D1 and D2 are further utilized to generate the switching sequence for battery and UC connected buck-boost bi-directional converter control by pulse width modulation (PWM) technique at 10 kHz, as shown in Figure 4. The battery over current protection is provided by limiting the I_{BAT} during the charging and discharging phase as $I_{BAT} \leq I_{BAT}^{max}$, where $I_{BAT}^{max} = 30$ A.



Figure 4. HESS Control.

4. Results and Discussions

The system is scrutinized under steady-state, irradiation variation mode, specified power mode, unbalanced grid voltage, abnormal grid voltage, unbalanced load, islanding and re-synchronization mode, with optimized V_{DC}.

4.1. Steady-State Analysis

During steady-state operation, the irradiation level is kept at 1000 W/m^2 , at 250 standard temperatures. The total harmonics distortion (THD) is defined as the ratio of root mean square of the harmonics content, including the harmonics components, up to 50th order. The latest IEEE Standards 519–2014 provide the distortion limit of the v_{Sabc} and i_{Sabc} for designing an electrical system with both linear and non-linear loads, which must

be maintained <5% for healthy operation. The THD of source voltage and current of phase 'a' (v_{Sa} and i_{Sa}), and load voltage and current (v_{La} and i_{La}) are analysed and found satisfactory as per IEEE519 standards, as shown in Figure 5a–d.



Figure 5. THD analysis of v_{Sa} , i_{Sa} , v_{La} and i_{La} .

The comparison of the THD of selected source, load, and compensator signals with various adaptive VSC controls, e.g., MCC, AKWMCC and proposed control, are also presented in Table 1.

Table 1. THD comparison of *v*_{Sa}, *i*_{Sa}, *v*_{La}, *i*_{LA} with MCC, AKWMCC and MRFKMP-based VSC controls.

| Quantity | МСС | AKWMCC | MRFKMP |
|-----------------|--------|--------|--------|
| v_{Sa} | 0.56% | 0.60% | 0.62% |
| i_{Sa} | 1.48% | 1.34% | 1.30% |
| i _{La} | 39.61% | 39.61% | 39.61% |

4.2. Irradiation Variation Analysis

The irradiation level is reduced from 1000 W/m^2 to 600 W/m^2 during 0.45 to 0.55 s of simulation time. As the active power (P_g) is supplied to the grid, the source voltage (v_{Sabc}) and current (i_{Sabc}) keep the UPF in phase opposition to each other. The amplitude of i_{Sabc} and P_g reduces with the insolation level. The i_{Sabc} is compared with reference current $(i_{s_{obc}}^*)$ generated by the VSC control and found to be following its reference signals. The load and compensator currents (i_{La} and i_{Ca}) of phase 'a' are in phase with each other. The i_{Sn} , i_{Ln} , and i_{Cn} are the source, load and compensator neutral currents, respectively. The i_{Ln} and i_{Cn} are exactly out of phase with each other, cancelling each other out and bringing i_{Sn} to zero, which is also its reference value $(i_{Sn}^* = 0)$. The VSC supplies the required reactive power (Q_g) to the load, and Q_g exchange with the grid is zero, which is its reference value ($Q_q^* = 0$). The V_d is maintained at 340 V during irradiation variation, as shown in Figure 6. On the DC side, the PV voltage, current and power (V_{PV} , I_{PV} and P_{PV}) change with the insolation change in the same manner as their reference values (V_{PV}^*, I_{PV}^*) and P_{PV}^* . The battery and UC currents (I_{BAT} , I_{UC}) are slightly negative, with reduced insolation level, and battery start discharging, which is verified by the dip in battery and UC voltage (V_{BAT}, V_{UC}) . The I_{BAT} and I_{UC} are the battery and UC currents, respectively. The V_{DC} is maintained at the desired level of 700 V during irradiation variation, as shown in Figure 7.



Figure 6. Irradiation variation analysis of v_{Sabc} , i_{Sabc} , i_{La} , i_{Ca} , V_d , i_{Sn} , i_{Cn} , i_{Ln} , P_g and Q_g .



Figure 7. Irradiation variation analysis of VPV, IPV, PPV, VDC, VBAT, IBAT, VUC, and IUC.

4.3. Unbalanced Grid Voltage Analysis

The grid voltage unbalancing is created by decreasing the phase 'a' amplitude to 0.8p.u., keeping it as it is for the remaining two phases. The v_{Sabc} and i_{Sabc} maintain UPF in phase opposition. The i_{Sabc} and i_{Sabc}^* are the same without any distortion due to grid voltage unbalancing. The i_{La} and i_{Ca} remain in phase with each other, and V_d reduces with the voltage unbalancing. The i_{Ln} and i_{Cn} are revolving at thrice the fundamental frequency in phase opposition. The P_g delivered to the grid shows variations due to the grid voltage unbalancing but tries to settle down on P_g^* . The i_{Sn} and Q_g are kept around zero as i_{Sn}^* and Q_g^* , as shown in Figure 8.



Figure 8. Grid voltage unbalancing analysis of v_{Sabc} , i_{Sabc} , i_{La} , i_{Ca} , $\mathbf{V}_{\mathbf{d}}$, i_{Sn} , i_{Cn} , i_{Ln} , P_g and Q_g .

On the DC side, V_{PV} , I_{PV} and P_{PV} show no change and keep following their references as irradiation level is kept fixed at 1000 W/m². The I_{BAT} and I_{UC} variations change the HESS from charging to discharging mode as grid voltage is unbalanced. The DC bus stability is preserved, and V_{DC} is maintained at 700 V while following the V_{DC}^* , as shown in Figure 9.



Figure 9. Grid voltage unbalancing analysis of VPV, IPV, PPV, Vdc, VBAT, IBAT, VUC, and IUC.

4.4. Abnormal Grid Voltage Analysis

The grid voltage sag and swell are created by varying the three-phase voltage magnitude to 0.8 p.u. during voltage sag from 0.2 s to 0.25 s, and 1.2 p.u. during voltage swell from 0.25 s to 0.3 s of simulation time. The variation in i_{Sabc} and its reference i^*_{Sabc} with the abnormal grid voltage are shown in Figure 10. With voltage sag for the same amount of P_g delivered to the grid, i_{Sabc} will increase and subsequently decrease for the voltage swell. The variation in V_d clearly shows the voltage sag and swell. The i_{Ln} and i_{Cn} maintain the in-phase relation, and i_{Ln} and i_{Cn} remain in phase opposition with each other. The i_{Sn} and Q_g is maintained around zero with negligible reactive power exchange with the grid. The P_g shows variations with grid voltage abnormality but settles down to reference value. On the DC side, HESS changes its mode from charging during voltage sag to discharging during voltage swell to maintain the balance of power. The V_{DC} is kept at the desired value of 700 V, as shown in Figure 11.



Figure 10. Abnormal grid voltage analysis of v_{Sabc} , i_{Sabc} , i_{La} , i_{Ca} , V_d , i_{Sn} , i_{Cn} , i_{Ln} , P_g and Q_g .



Figure 11. Abnormal grid voltage analysis of VPV, IPV, PPV, VDC, VBAT, IBAT, VUC, and IUC.

4.5. Specified Power Mode Analysis

During the specified power mode, the system pre-decides the amount of power that is extracted from the system. The fixed power mode is simulated from 0.95 to 1.15 s of simulation time. The P_g delivered to the grid decreases to 17 kW from 0.95 s to 1.05 s, and further increases to 25.5 kW from 1.05 s to 1.15 s of simulation time, which shows the ability of VSC to provide the pre-decided amount of power for peak shaving. With the varying P_g , the i_{Sabc} also varies at the constant v_{Sabc} . The v_{Sabc} and i_{Sabc} maintain the UPF, i_{La} and i_{Ca} remain in phase with each other, i_{Ln} and i_{Cn} remain in phase opposition at thrice the fundamental frequency that results in i_{Sn} matching its reference, i.e., zero, and Q_g is also maintained at zero, as shown in Figure 12. On the DC side, V_{PV} , I_{PV} , and P_{PV} remain the same as in steady-state. The HESS changes from charging to discharging mode as P_g delivered to grid decreases and then increases, which can be verified from V_{BAT} , I_{BAT} and V_{UC} , I_{UC} . The V_{DC} is maintained at 700 V and keeps following V_{DC}^* , as shown in Figure 13.



Figure 12. Specified power mode analysis of v_{Sabc}, i_{Sabc}, i_{La}, i_{Ca}, V_d, i_{Sn}, i_{Cn}, i_{Ln}, P_g and Q_g.

4.6. Unbalanced Load Analysis

The load unbalancing is simulated by disconnecting the phase 'a' of the load from the rest of the system from 0.7 s to 0.8 s of simulation time. With phase 'a' of load disconnected, the power requirement of load also reduces, which can be verified by the reduced magnitude of i_{Sabc} and increased magnitude of P_g . The i_{Ln} and i_{Cn} start revolving with twice the fundamental frequency and maintain the i_{Sn} around i_{Sn}^* . The Q_g also kept around zero. The i_{Sabc} , i_{Sn} , P_g , and Q_g follow their reference signals and maintain the stability of the system, as shown in Figure 14. On the DC side, V_{PV} , I_{PV} and P_{PV} show no variations and keep following their respective reference signals. The I_{BAT} and I_{UC} become slightly negative and start charging with a reduced load. The V_{DC} remains stable at 700 V, ensuring the stability of the system, as shown in Figure 15.



Figure 13. Specified power mode analysis of V_{PV}, I_{PV}, P_{PV}, V_{DC}, V_{BAT}, I_{BAT}, V_{UC}, and I_{UC}.



Figure 14. Unbalanced load analysis of v_{Sabc} , i_{Sabc} , i_{La} , i_{Ca} , V_d , i_{Sn} , i_{Cn} , i_{Ln} , P_g and Q_g .

4.7. Internal Signal Analysis

The μ_{pa} , W_{pa} , W_{avg} , W_{PV} , and W_{sp} are considered as the internal signals of the proposed system. Their behavior during various dynamic conditions have been analyzed under abnormal grid voltage conditions, insolation variation, load unbalancing and specified power mode, as shown in Figure 16. The W_{avg} , W_{PV} , and W_{sp} show maximum variations in their weights during abnormal grid voltage conditions. During insolation variation, only W_{PV} and W_{SP} were changing to maintain the power balance between the system and the grid. During the load unbalancing, the W_{pa} reduces to zero and its effect can be seen on W_{avg} . During fixed power mode, the power balancing at PCC is also achieved with W_{PV} and W_{SP} . The μ_{pa} remains sinusoidal with unity amplitude throughout the dynamic states. The comparison of the W_{avg} with different adaptive controls, i.e., LMS,

LMF, MCC, AKWMCC and MRFKMP, is shown in Figure 17. The W_{avg} shows variations during abnormal grid voltage and load unbalancing operation.



Figure 15. Unbalanced load analysis of VPV, IPV, PPV, VDC, VBAT, IBAT, VUC, and IUC.



Figure 16. Internal signals analysis (during dynamic conditions) of μ_{pa} , W_{pa} , W_{avg} , W_{PV} , W_{sp} .



Figure 17. Comparison of w_{avg} with LMS, LMF, MCC, AKWMCC and MRFKMP-based VSC control.

The LMS delivers high steady-state error as compared to LMF. The MCC and AK-WMCC present almost similar weights; AKWMCC shows fewer variations as compared to MCC during steady-state and dynamic conditions. The MRFKMP-based VSC control delivers the more accurate weight signals with reduced variations during abnormal grid voltage and load unbalancing, and it results in reduced overall losses in the system and provides better efficiency. The comparative analysis of the proposed control with LMS, LMF, MCC, AKWMCC and MRFKMP is shown in Table 2.

| Parameters | LMS | LMF | MCC | AKWMCC | MRFKMP |
|--|----------|----------|----------|----------|----------|
| Steady-state error | 1.428% | 1.257% | 0.857% | 0.5% | 0.114% |
| Convergence Speed | 13.5 ms | 17.7 ms | 16.5 ms | 23 ms | 32 ms |
| Computational Burden | Less | Less | Moderate | Moderate | High |
| Dynamic State Transients (Abnormal Grid Voltage) | 4.117% | 3.957% | 2.362% | 1.785% | 0.928% |
| Dynamic State Transients (Irradiation change) | 4.1% | 3.857% | 2.184% | 1.428% | 0.714% |
| Dynamic State Transients (Unbalanced Load) | 1.814% | 1.643% | 0.727% | 0.585% | 0.257% |
| Dynamic State Transients (Abnormal Grid Voltage) | 4.285% | 3.741% | 1.071% | 0.857% | 0.285% |
| Seamless transition of VSC control duration | 4 cycles | 4 cycles | 3 cycles | 2 cycles | 2 cycles |

Table 2. Comparative performance analysis of the VSC Controls.

4.8. Islanding and Re-Synchronization Analysis

The islanding is induced by the master control switch from 0.2 s to 0.4 s, and the grid re-synchronization is initiated at 0.4 s of simulation time. During islanding v_{Sabc} , and i_{Sabc} reduce to zero; v_{Labc} is maintained at the desired amplitude and frequency during islanding operation. With islanding i_{Ca} , amplitude reduces as P_g is supplied to the grid, and VSC must satisfy load only. The V_d during the islanded operation is maintained around the V_d^{*} = 340. The i_{Ln} and i_{Cn} cancel each other out, which results in $i_{Sn} = 0$. The P_g and Q_g exactly follow their reference values P_g^* and Q_g^* , as shown in Figure 18. The re-synchronization is initiated at 0.4 s, and the system is kept under intentional islanding for a few cycles to ensure the grid stability, in which the f, ω and V_d of load are matched with the incoming grid. The re-synchronization is completed within four cycles without any major transients in the grid. On the DC side, IPV, VPV, and PPV remain on the desired levels as their reference signals with minor variations in isolated mode. The I_{BAT} and I_{UC} become negative and start charging as is P_g delivered to the grid until they are zero, and an excess amount of power is utilized by HESS charging. The increase in the rate of charging of HESS can be verified from increasing amplitude of V_{BAT} and V_{UC} . With the reconnected grid, the HESS charging rate is reduced, and the system continues with the P_g supply to the grid. The V_{DC} is maintained at the desired level of 700 V during the whole operation, as shown in Figure 19. With the islanding of the grid, the angular frequency (ω) of v_{Labc} deviates from v_{Sabc} . As the grid re-synchronization is initiated at 0.4 s, it becomes essential to match the ω of load with the incoming grid, which is done by resynchronization control. The system took 2–3 cycles at 50 Hz to re-synchronize the incoming grid with the system, as shown in Figure 20.



Figure 18. Islanding and re-synchronization analysis of v_{Sabc} , i_{Sabc} , v_{Labc} , i_{La} , i_{Ca} , V_d , i_{Sn} , i_{Cn} , i_{Ln} , P_g and Q_g .



Figure 19. Islanding and re-synchronization analysis of V_{PV} , I_{PV} , P_{PV} , V_{DC} , V_{BAT} , I_{BAT} , V_{UC} , and I_{UC} .



Figure 20. Angular frequency variations of v_{Sabc} and v_{Labc} during islanding and re-synchronization.

4.9. Optimized DC Bus Analysis

The V_{DC} variations with the induced dynamic conditions should be minimum to maintain the stability of the system, and it reduces the need for a larger coupling capacitor. The DC bus controlled by PI controller gains is tuned with GA, GA with pattern search and GNDO algorithm. The V_{DC} regulated by the GNDO algorithm generates the accurate I_{d1}, which eventually enhances the operational capability of VSC control, as shown in Figure 21. The I_{d1} variations during diverse dynamic conditions are minimum with GNDO-regulated V_{DC}. The I_{d1} generated by initial gain tuned and GA tuned V_{DC} show large variations. I_{d1} performance of GA with pattern search tuned V_{DC} is comparable with GNDO tuned V_{DC}, but as GNDO tuned V_{DC} provides I_{d1} closer to zero with reduced variations. The accurate I_{d1} helps the MRFKMP-based VSC to achieve faster convergence, precise generation of the fundamental component of load current and reduced steady-state losses. The comparison of V_{DC} during initial transients, abnormal grid voltage, insolation variation, unbalanced load and specified power mode is shown in Figure 22. The gains of the tuned PI controller with various optimization techniques are presented in Table 3.



Figure 21. Loss component of current with GA, GA with pattern search and GNDO tuned PI controller gains.



Figure 22. V_{DC} variations with GA, GA with pattern search and GNDO tuned PI controller gains.

| Table | 3. PI | gains | tuned by | GA, GA | A with | pattern | search | and G | NDO a | algorithm. |
|-------|--------------|-------|----------|--------|--------|---------|--------|-------|-------|------------|
|-------|--------------|-------|----------|--------|--------|---------|--------|-------|-------|------------|

| Optimization Technique | Proportional Gain (k_p) | Integral Gain (k _i) | | |
|---------------------------------------|-----------------------------|---------------------------------|--|--|
| Initial gains | 1 | 0 | | |
| GA tuned PI gains | 0.68274 | 0.26343 | | |
| GA with pattern search tuned PI gains | 0.184228 | 0.066836 | | |
| GNDO tuned PI gains | 0.162588 | 0.144438 | | |

5. Conclusions

This paper presents a GNDO MRFKMP-based VSC control of a three-phase four-wire grid-tied dual-stage PV system. The DC bus has been regulated by the gain optimized conventional PI controller that generates a precise loss component of current, improves power quality, and enhances the VSC performance. The optimization is done on an offline basis, so it did not increase the complexity of the VSC control. The MRFKMP-based VSC control extracts an accurate fundamental loss current component with a reduced computational burden due to random Fourier feature approximation. The MRFKMP delivers the reduced steady-state error but its convergence speed has been comparable with LMS and LMF algorithms though they are simple algorithms. The VSC conducts multifunctional operations, i.e., power quality improvement, reactive power compensation, load balancing, power balancing at the point of common coupling, etc., during diverse dynamic operating conditions. The GNDO-based MRFKMP allows a seamless transition of VSC control from grid current control mode to isolated voltage control mode during grid islanding and re-synchronization. The seamless transfer of VSC control is achieved without any large transients on the load and grid side while maintaining the desired frequency and load voltage during islanded operation. The grid re-synchronization has been achieved in four cycles at 50 Hz. The hybrid energy storage system enhances the sustainability and reliability of the PV system by satisfying load during the nighttime and in poor weather. The proposed system operates satisfactorily under various dynamic conditions, e.g., sudden irradiation change, unbalanced load, abnormal and unbalanced grid voltage mode. The total harmonic distortion of the grid voltage and current remains below 5% as per IEEE519 standards throughout the operation. A trade-off has to be made between steady-state error and convergence speed while utilizing the proposed VSC control. The complex VSC control reduces the steady-state error at the cost of convergence speed, which is also a drawback of the proposed control.

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