


## Article

# Monolithic Perovskite-Carrier Selective Contact Silicon Tandem Solar Cells Using Molybdenum Oxide as a Hole Selective Layer

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**Abstract:** Monolithic perovskite–silicon tandem solar cells with MoO<sub>x</sub> hole selective contact silicon bottom solar cells show a power conversion efficiency of 8%. A thin 15 nm-thick MoO<sub>x</sub> contact to n-type Si was used instead of a standard p<sup>+</sup> emitter to collect holes and the SiO<sub>x</sub>/n<sup>+</sup> poly-Si structure was deposited on the other side of the device for direct tunneling of electrons and this silicon bottom cell structure shows ~15% of power conversion efficiency. With this bottom carrier selective silicon cell, tin oxide, and subsequent perovskite structure were deposited to fabricate monolithic tandem solar cells. Monolithic tandem structure without ITO interlayer was also compared to confirm the role of MoO<sub>x</sub> in tandem cells and this tandem structure shows the power conversion efficiency of 3.3%. This research has confirmed that the MoO<sub>x</sub> layer simultaneously acts as a passivation layer and a hole collecting layer in this tandem structure.

**Keywords:** perovskite–silicon tandem solar cell; monolithic tandem cell; carrier selective contact; molybdenum oxide; hole selective layer

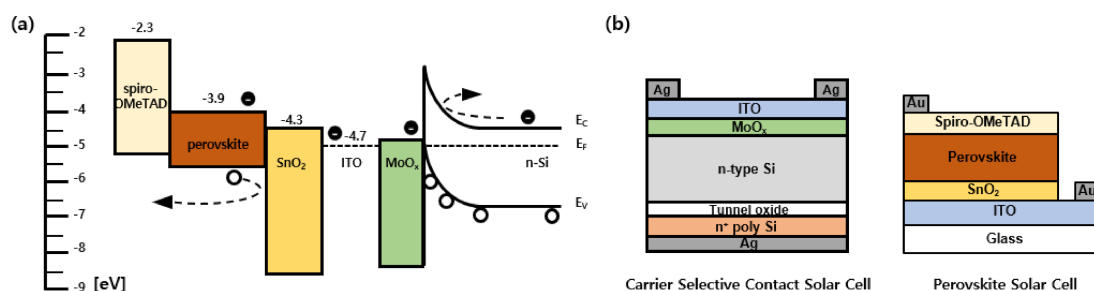
## 1. Introduction

The conversion efficiency of silicon solar cells, which occupy the largest share of the photovoltaic market [1], is approaching the maximum theoretical efficiency [2]. Currently, the Si champion cell is 26.7% [3], due to its low radiation rate, the efficiency of Si solar cells is limited to 29.4%, which is ~4% below the Shockley–Queisser limit [4]. Hence, several research groups are involved in the development of perovskite–silicon tandem solar cells with enhanced optical absorption in the short wavelength band [5]; a group of Young Investigator Group Perovskite Tandem Solar Cells at the Helmholtz-Zentrum Berlin reports a high efficiency of 29.15% for perovskite–silicon solar cells [6]. Various silicon bottom cell concepts are being evaluated to further improve the conversion efficiency of perovskite–silicon tandem solar cells [7]. In this study, tandem cells are fabricated using carrier selective contact silicon solar cells as bottom structures. Molybdenum oxide is used as a carrier selective contact material in the bottom cells, which can play the role of a hole transport layer without a conventional p-n junction.

The concept of carrier selective contact in silicon solar cell research has attracted considerable attention in recent years. The highest efficiency of monocrystalline silicon solar cells is achieved using the heterojunction with intrinsic thin layer (HIT) cell structure,

in which the solar cell is fabricated using an intrinsic amorphous silicon passivated contact layer [3,8]. In addition, devices using passivating contacts of polysilicon called tunnel oxide passivated contact (TOPCon) [9] and polycrystalline silicon on oxide (POLO) [10] show high efficiency. Research on carrier selective contact solar cells, which effectively collect photogenerated carriers by depositing other materials on silicon without a substrate doping process, has been extensively carried out since the 1970s [11–15]. Recently, a high value of carrier lifetime can be obtained by improving the silicon interface passivation characteristics [16]. Hence, studies on carrier selective contacts using carbon-based materials [17,18] and metal oxides such as  $\text{TiO}_2$ ,  $\text{ZnO}$ , and  $\text{MoO}_x$ , which can be easily deposited, have attracted renewed attention [19–21]. Unlike conventional p-n junction cells with built-in electric fields formed by a doping process to separate photogenerated carriers, the carrier selective contact structure separates the carriers by band bending generated between the material being contacted and the silicon. The carrier selectivity of these layers depends on the electronic structures of the materials deposited on silicon [22,23].

In this study,  $\text{MoO}_x$  was deposited on a silicon substrate through evaporation to form a hole selective contact, and the passivation characteristics of  $\text{MoO}_x$  were studied.  $\text{MoO}_x$  has a wide bandgap and a considerably large work function compared to other metal oxides [24]. The schematic diagram of the energy band of the  $\text{MoO}_x/\text{n-Si}$  interface on contact is shown in Figure 1a.  $\text{MoO}_x$  selective contact has been interpreted as two possible transport mechanisms in the contact area [25]: (1) band-to-band tunneling, in which the carriers directly tunnel the bands and (2) trap-assisted tunneling, in which the carriers tunnel through the trap sites within the bandgap of the  $\text{MoO}_x$  [26]. The value of the work function of  $\text{MoO}_x$  changes depending on the presence of oxygen vacancy of the layer [27], and the work function value influences the interface inversion for carrier separation [28]. As shown in Figure 1b, the hole selective contact silicon cell using the molybdenum oxide layer worked without homojunction and additional passivation layers. When a layer of  $\text{MoO}_x$  with a high work function is deposited on n-type silicon, an inversion layer similar to the Schottky barrier is formed, separating electron-hole pairs [29]. The electrons photogenerated from the silicon bulk are blocked by the barrier at the  $\text{MoO}_x/\text{n-Si}$  interface and flow down to reach the opposite electrode and the holes recombine at the  $\text{MoO}_x/\text{n-Si}$  interface. Since the inversion layer at the silicon interface is affected by the electronic properties of the deposited material, various metal oxides with high work functions are deposited on silicon, which are being studied as carrier selective contact materials [30].



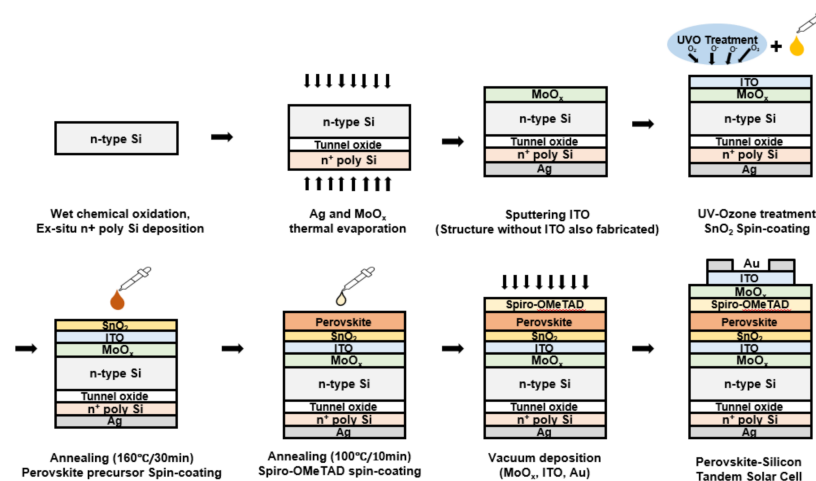
**Figure 1.** (a) Energy band diagram of the monolithic perovskite-carrier selective contact silicon tandem solar cell using  $\text{MoO}_x$ . (b) Schematic diagrams of the carrier selective contact solar cell and the perovskite solar cell.

$\text{MoO}_x$  with various structures have been studied to create hole selective contact in silicon cells to achieve high efficiency [31,32], and an efficiency of more than 20% has recently been reported [33]. However, monolithic perovskite–silicon tandem structures using carrier selective contact silicon cells have not yet been reported. In this study, monolithic perovskite-carrier selective contact silicon tandem cells without a conventional p-n junction were fabricated using two types of solar cells, as shown in Figure 1b. Since the structure can also be fabricated without an ITO layer in terms of band alignment, structures both with and without the ITO layer were studied.

## 2. Materials and Methods

### 2.1. Fabrication of Monolithic Perovskite-Carrier Selective Contact Silicon Tandem Solar Cell

A hole selective contact silicon solar cell was prepared using an n-type float-zone (FZ) silicon wafer (100) with a thickness of  $\sim 280 \mu\text{m}$  and a resistivity of  $\sim 1 \Omega\text{cm}$ . The wafers were subjected to the conventional cleaning process of the Radio Corporation of America (RCA) using deionized water,  $\text{NH}_4\text{OH}$ ,  $\text{HCl}$ , and  $\text{H}_2\text{O}_2$ . After RCA cleaning, as shown in Figure 2, a silicon oxide layer with a thickness of  $\sim 1.2 \text{ nm}$  was grown on the wafer surface via wet chemical oxidation using  $\text{H}_2\text{O}_2$ . Thickness of the silicon oxide thin film was confirmed by ellipsometer measurement with a reflective index  $N$  of 1.45. Subsequently,  $\sim 300 \text{ nm}$  of intrinsic polysilicon was deposited on the silicon oxide surface using low-pressure chemical vapor deposition (LPCVD) equipment at  $600 \text{ }^\circ\text{C}$ . The intrinsic polysilicon layer was doped using  $\text{POCl}_3$  diffusion at  $925 \text{ }^\circ\text{C}$  after 45 min of ramp-up time from  $500 \text{ }^\circ\text{C}$  in a furnace. The front side  $n^+$  poly Si layer was etched by dipping  $80 \text{ }^\circ\text{C}$  Tetramethylammonium hydroxide (TMAH, 20 wt. % in  $\text{H}_2\text{O}$ , Aldrich, St. Louis, MO, USA) solution and the front side tunnel oxide layer was etched by dilute HF solution. Next,  $\text{MoO}_x$  layers were deposited by thermal evaporation using molybdenum (VI) oxide powder (99.99% purity, Sigma Aldrich), and ITO films were prepared by plasma sputtering. The samples were subjected to UV–ozone treatment for improving the wettability and removing contamination. Then, a compact  $\text{SnO}_2$  layer was deposited by a solution process using a spin coater, and the spin-coated film was annealed at  $160 \text{ }^\circ\text{C}$  for 30 min. The perovskite precursor solution was spin-coated onto the  $\text{SnO}_2$  layer while anhydrous diethyl ether, to treat the layer during the anti-solvent process [34], was slowly dripped on top of the substrate. Subsequently, the film was annealed at  $100 \text{ }^\circ\text{C}$  for 10 min to obtain a dense  $\text{CH}_3\text{NH}_3\text{PbI}_3$  film. The spiro-OMeTAD layer was spin-coated by dissolving spiro-OMeTAD (Merck, Kenilworth, NJ, USA) in a mixed solution of chlorobenzene (anhydrous, 99.8%, Sigma-Aldrich), 4-tert-butylpyridine (96%, Aldrich), and Li-TFSL (99.95%, Aldrich). Next, the  $\text{MoO}_x$  buffer layer was deposited by thermal evaporation, and the ITO transparent electrode was deposited by DC magnetron sputtering. The front gold electrode was deposited through a thermal evaporator using a pattern-shaped mask.



**Figure 2.** Schematic illustration for the fabrication of monolithic perovskite-carrier selective contact silicon tandem solar cell.

### 2.2. Hole Selective Contact Silicon Solar Cell and Perovskite Solar Cell

The fabrication of the  $\text{MoO}_x$  hole selective contact silicon cell was carried out using the same steps shown in Figure 2 up to the sputtering ITO process. A silver electrode was deposited on the front side of the sample by thermal evaporation to complete the silicon solar cell. An ITO patterned glass substrate was used to fabricate the perovskite solar cell, and the sequence of steps from the UV–ozone treatment, as depicted in Figure 2, was followed.

### 2.3. Sample Measurement

To evaluate the passivation quality of the symmetric MoO<sub>x</sub> and tunnel oxide/P doped polysilicon passivated layers, the implied Voc of the samples under 1-sun illumination (xenon flash lamp) was determined using a WCT-120 Sinton Instruments tool in the QSS mode according to the following equation proposed by Sinton [35]:

$$V_{oc,implied} = \frac{kT}{q} \ln \left( \frac{(N_D + \Delta n)\Delta n}{n_i^2} \right) \quad (1)$$

where  $kT/q$  is the thermal voltage,  $N_D$  is the doping concentration,  $\Delta n$  is the excess carrier concentration related to the effective minority carrier lifetime, and  $n_i$  is the intrinsic carrier concentration. The J-V tests were carried out using a Keithley 2400 Source meter under the illumination of an AM 1.5 G solar simulator (100 mW/cm<sup>2</sup>). An EQE testing system consisting of a monochromator was used to scan the UV-Vis-Infrared spectrum.

## 3. Results

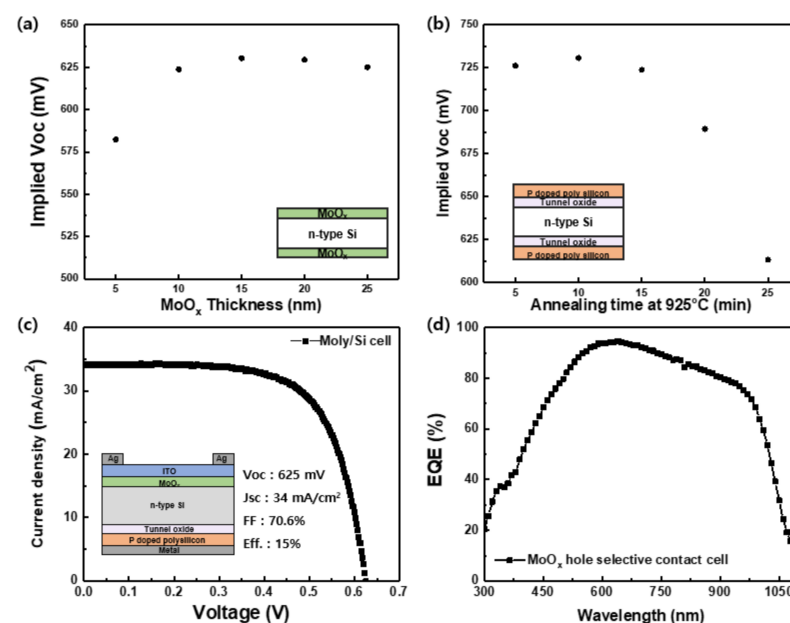
### 3.1. Molybdenum Oxide Hole Selective Contact Si Solar Cell

MoO<sub>x</sub> film and phosphorus(n<sup>+</sup>) doped polysilicon layers were optimized to fabricate a carrier selective contact silicon solar cell for use as a bottom cell in tandem solar cells. It is important to optimize the thickness of the MoO<sub>x</sub> layer. This is because the layer induces silicon band bending and at the same time, acting as a passivation layer without any interlayer. Figure 3a shows the implied open-circuit voltage (Voc) values of the MoO<sub>x</sub>/Si/MoO<sub>x</sub> symmetrical lifetime samples. The MoO<sub>x</sub> layers were deposited with thicknesses in the range of 5 to 25 nm on both sides of an n-type silicon wafer without any passivation layers, such as an amorphous silicon or a thin oxide layer. The implied Voc value increases as the thickness of the MoO<sub>x</sub> layer increases up to 15 nm, which reaches a maximum value of 630.3 mV. With further increase in the film thickness, the implied Voc value slightly decreases while maintaining a certain degree of passivation. The MoO<sub>x</sub> dielectric layer deposited on the silicon wafer prevents surface recombination and at the same time shows the effect of increasing the carrier lifetime and implied Voc due to the band bending according to the thickness of a hole selective material [36].

To improve the bottom silicon cell characteristics, the TOPCon structure, which is widely studied as a carrier selective passivated contact structure showing high-quality passivation properties [37–39], was optimized. We fabricated n<sup>+</sup> poly-Si/SiO<sub>x</sub>/Si/SiO<sub>x</sub>/n<sup>+</sup> poly-Si symmetrical lifetime samples. The sample structure is shown in the inset of Figure 3b. After depositing intrinsic polysilicon on the chemical tunnel oxide passivated n-type silicon wafer, phosphorus doping was performed in a tube furnace at temperatures in the range of 875–1000 °C using POCl<sub>3</sub> as the source. Implied Voc values were measured after an additional hydrogenation process, which can largely enhance the passivation quality of the TOPCon structure [40]. The lowest value of recombination current density was obtained after phosphorus diffusion at 925 °C, with the time varying from 5 to 25 min, as shown in Figure 3b, and a maximum implied Voc of 730.6 mV was obtained after 10 min diffusion at 925 °C. These high properties of the rear passivated contact structure improve the qualities of the entire bottom cell, even if the front MoO<sub>x</sub> passivation property is quite low.

After applying the above two optimized carrier selective contacts to Si substrates, a transparent ITO electrode and a patterned Ag electrode were deposited on the front MoO<sub>x</sub> hole selective contact layer and a full area Ag electrode was deposited on the rear n<sup>+</sup> poly-Si/SiO<sub>x</sub> electron selective contact layer, as shown in the inset of Figure 3c to fabricate a 1 × 1 cm<sup>2</sup> carrier selective contact solar cell. For these solar cells without a p-n junction, the best conversion efficiency of 15% was achieved with a Voc of 625 mV, current density of 34 mA/cm<sup>2</sup>, and fill factor (FF) of 70.6%. Various hole selective contact silicon solar cells using an MoO<sub>x</sub> layer showed high conversion efficiencies (Table S1). The open-circuit voltage value of our cell using MoO<sub>x</sub> as an induced junction without any interlayer, such

as a-Si, silicon oxide on a flat surface, is comparable to other groups. The fabricated Si solar cells have planar surfaces for stacking a perovskite solar cell. Therefore, the fabricated Si solar cells may have the benefit of higher Voc values but the disadvantage of lower Jsc values compared to Si solar cells with pyramid textured surfaces [41,42]. This is due to the higher light absorption but a higher number of dangling bonds of pyramid textured surfaces. Figure 3d shows the external quantum efficiency (EQE) of the fabricated Si solar cell in the wavelength range of 300 to 1100 nm. The EQE obtained in the wavelength range of about 600–800 nm is more than 80%. In the longer wavelength region, the EQE value tends to decrease due to the parasitic absorption of a 300 nm thick doped polysilicon layer. The EQE can be enhanced by reducing the thickness of a polysilicon layer and improving light reflection by a rear metal electrode [43]. The fabricated carrier selective contact silicon solar cell using MoO<sub>x</sub> in this study requires additional heat treatment to recover surface damage from the sputtering process [44] and to improve resistivity of the top ITO layer. However, the annealing process above 200 °C reduces the oxygen/Mo ratio and work function of the thermal evaporated MoO<sub>x</sub> [45], which can affect the voltage drop by reducing the band bending at the silicon interface. This structure still has some room to improve voltage and FF through the optimized annealing condition. It is expected that a high-efficiency cell can be fabricated by inserting a dielectric interlayer.



**Figure 3.** Implied Voc data of (a) the MoO<sub>x</sub>/Si stack and (b) the tunnel oxide/P doped polysilicon stack at different layer thicknesses and annealing durations, respectively, at 925 °C. (c) J–V curve and (d) EQE data of the MoO<sub>x</sub> hole selective contact silicon solar cell.

### 3.2. Tandem Solar Cells

The cell characteristics of the perovskite solar cells (PSCs) are shown in Figure 4. Figure 4a shows the J–V curve, and Figure 4b shows the EQE data of the fabricated perovskite cell in the wavelength range of 300–1100 nm. The PSC showed a conversion efficiency of 15.96% with an open-circuit voltage of 1.01 V, current density of 21.7 mA/cm<sup>2</sup>, and 72.4% FF. The EQE data show that the perovskite cell absorbs light only in the wavelength range of 300–750 nm, and the EQE value is more than 70% in the wavelength range of approximately 350–650 nm. To further increase the light absorption at shorter wavelengths, the bandgap of the perovskite was optimized and then applied to a tandem cell. The bandgap was increased from ~1.55 to ~1.65 eV by changing the composition of perovskite (Figure S2).

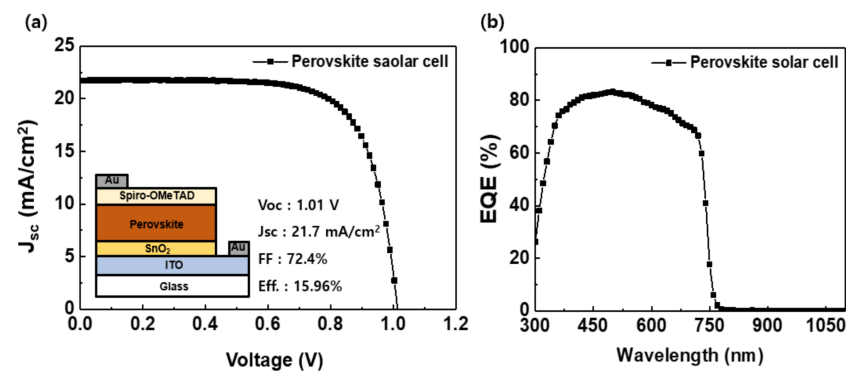


Figure 4. (a) J-V curve and (b) EQE data of the PSC on ITO patterned glass.

Figure 5a shows the light-induced current-voltage (LIV) characteristics of monolithic perovskite-carrier selective contact silicon tandem solar cells using  $\text{MoO}_x$  as a hole selective contact. The solar cell without an ITO interlayer (#1) exhibited a PCE of 3.3%, and by using 80 and 20 nm-thick ITO interlayers (#2), the efficiency increased to 8.0 and 7.8%, respectively. The specific structures and cross-section SEM data of the fabricated tandem cells are shown in Figure 5b. In the structure without the ITO interlayer (#1), the  $\text{MoO}_x$  film appears to be degraded, as UV-ozone treatment was performed before carrying out the  $\text{SnO}_2$  solution process [46]. In the case of samples with an ITO interlayer, structure #2 shows higher conversion efficiency as it prevents  $\text{MoO}_x$  from degradation during UV-ozone treatment. Meanwhile, sample #2-1 and #2-2 have ITO interlayer thicknesses of 80 and 20 nm, respectively. Sample #2-1, with an 80-nm-thick ITO interlayer, showed lower Voc and higher FF compared to the sample #2-2 with a 20-nm-thick ITO interlayer. (Table S2) This may be due to higher sputtering damages during longer sputtering times. To increase the current of these tandem solar cells, current matching between the perovskite and the carrier selective contact silicon cell needs to be further studied.

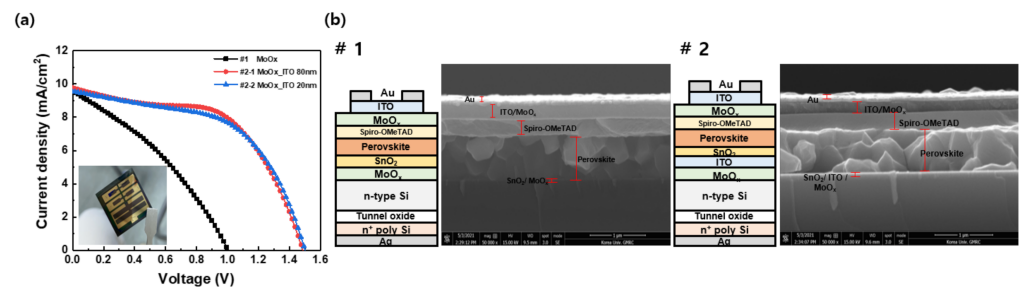


Figure 5. (a) J-V curves and (b) schematic of the structures of the monolithic perovskite-carrier selective contact silicon tandem solar cells.

The monolithic perovskite-carrier selective contact silicon tandem solar cell showed the highest efficiency of 8%, with a Voc of 1.485 V, Jsc of 9.77  $\text{mA}/\text{cm}^2$ , and 55.05% FF when 15 nm-thick  $\text{MoO}_x$  and 80 nm-thick ITO layers were deposited as in cell structure #2. Both cell structures (#1 and #2) were confirmed to be capable of being driven by tandem cells. The Voc, Jsc, and FF are expected to improve with dry processing, current matching, and heat treatment optimization.

#### 4. Conclusions

In this study, a carrier selective contact silicon cell using  $\text{MoO}_x$ , instead of a p-n homojunction structure, was introduced to a monolithic perovskite-silicon tandem cell for the first time. Through the monolithic perovskite-silicon tandem solar cells, it was shown that the proposed new structure worked as a tandem solar cell. In the case of the bottom silicon solar cell, the carrier selective contact structure was optimized on the silicon cell using  $\text{MoO}_x$  to the rear tunnel oxide passivated contact. The maximum implied Voc values

were 630.3 and 730.6 mV, respectively, in the (15 nm-thick) MoO<sub>x</sub>/Si and poly-Si/SiO<sub>x</sub>/Si symmetric structures. For subsequent processes such as spin-coating, a polished surface was used to fabricate tandem solar cells, and a 15% carrier selective contact silicon solar cell was obtained. The bottom solar cell is expected to improve J<sub>sc</sub> and FF through optimization of the rear structure and annealing process after metallization. The perovskite solar cell stacked on the bottom silicon cell showed 15.96% efficiency as a single cell. Structures both with and without an ITO interlayer were found to operate as tandem solar cells, and the highest efficiency of 8% with a Voc of 1.485 V, J<sub>sc</sub> of 9.77 mA/cm<sup>2</sup>, and 55.05% FF for an area of 1 cm<sup>2</sup> was obtained in the presence of the ITO layer. The proposed monolithic perovskite-carrier selective contact silicon tandem solar cell using MoO<sub>x</sub> as a hole selective layer can be improved further by current matching, interlayer optimization, dry processing instead of wet processing and optimization of heat treatment processes. It was found that MoO<sub>x</sub> can play the roles of a passivation layer, induced junction at the same time in this tandem structure. Recently, perovskite–silicon tandem solar cells have been manufactured using various bottom silicon cells. It is expected that the perovskite–silicon tandem solar cells with simple carrier selective contact structures introduced in this study could be developed using commercially valuable materials such as metal oxides, carbon-based materials, and organic polymers.

**Supplementary Materials:** The following are available online at <https://www.mdpi.com/article/10.3390/en14113108/s1>, Figure S1:  $n^+$  recombination current density of n<sup>+</sup> poly Si/tunnel oxide/Si symmetric structures according to annealing temperature, Table S1: Efficiency table of MoO<sub>x</sub> hole selective contact silicon solar cells, Table S2: Photovoltaic parameters of the monolithic perovskite-carrier selective contact silicon tandem solar cells, Figure S2: (a) UV–vis absorption spectra and (b) bandgap of perovskite layer estimated by tauc plot.

**Author Contributions:** H.S. and C.L. contributed equally to this work. Conceptualization, C.L. and H.S.; methodology, J.H.; validation, H.-S.L.; formal analysis, S.-W.L., D.C., D.P., J.N., S.-H.J. and J.K.; investigation, S.B.; resources, S.B. and H.L.; data curation, Y.K.; writing—original draft preparation, C.L.; writing—review and editing, Y.K. and D.K.; visualization, H.S.; supervision, H.-S.L.; project administration, D.K.; funding acquisition, H.-S.L. All authors have read and agreed to the published version of the manuscript.

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