



Article Integrated Magnetics Design for a Full-Bridge Phase-Shifted Converter

Yu-Chen Liu^{1,*}, Cheng-You Xiao², Chien-Chun Huang², Pei-Chin Chi² and Huang-Jen Chiu²

¹ Department of Electronical Engineering, National Ilan University, Ilan 260007, Taiwan

² Department of Electronic and Computer Engineering, National Taiwan University of Science and Technology, Taipei 10607, Taiwan; m10302221@mail.ntust.edu.tw (C.-Y.X.); u8910659@gmail.com (C.-C.H.); D10002205@mail.ntust.edu.tw (P.-C.C.); hjchiu@mail.ntust.edu.tw (H.-J.C.)

* Correspondence: ycliu@niu.edu.tw; Tel.: +886-39-317-389

Abstract: In this study, an optimization procedure was proposed for the magnetic component of an integrated transformer applied in a center-tap phase-shifted full-bridge converter. To accommodate high power-density 0demand, a transformer and an output inductor were integrated into a magnetic component to reduce the volume of the magnetic material and the primary and secondary windings of the transformer were wound on the magnetic legs to reduce conduction loss attributable to the alternating-current resistor. With a focus on the integrated transformer applied in a phase-shifted full-bridge converter, circuit operation in each time interval was analyzed, and a design procedure was established for the integrated magnetic component. In addition, the manner in which output inductance was affected by the mutual inductance between the transformer and the output inductor in the integrated transformer during various operation intervals was discussed and, to minimize circuit loss, a design optimization procedure for the magnetic core was proposed. Finally, the integrated transformer was applied in a phase-shifted full-bridge converter to achieve an input voltage of 400 V, an output voltage of 12 V, output power of 1.7 kW, an output frequency of 80 kHz, and a maximum conversion efficiency of 96.7%.

Keywords: phase shift full-bridge converter; magnetic integration; high power density

1. Introduction

Rapid technological advancements have greatly increased the demand for cloud servers, which calculate and store data, increasing the demand for electricity. To accommodate converter design requirements for reduced loss and volume, improving the efficiency of electric power conversion and increasing power density have become prominent considerations in current technological development. Most power supplies for servers adopt a two-stage circuit topology. The first stage comprises power factor correction equipment for conversion from alternating current (AC) to direct current (DC) and the second stage an isolated DC–DC converter with high-voltage DC as the input and low-voltage DC the output.

The typical types of isolated converters used in server power supplies are phaseshifted full-bridge converters and resonant converters. LLC resonant converters [1–3] achieve wide-range zero-voltage switching (ZVS) on the primary side and ZVS on the secondary side and are thus suitable for application in high-wattage and high-frequency devices. The disadvantage of LLC resonant converters is the absence of output inductors at the output end. Therefore, when used in low-voltage high-current devices, LLC resonant converters require more capacitors connected in series to meet output voltage ripple specifications.

For a phase-shifted full-bridge converter [4–8], the switch on the primary side does not achieve full-range ZVS, and the synchronous rectifier on the secondary side exhibits high switch stress. However, the converter structure features an output inductor, which



Citation: Liu, Y.-C.; Xiao, C.-Y.; Huang, C.-C.; Chi, P.-C.; Chiu, H.-J. Integrated Magnetics Design for a Full-Bridge Phase-Shifted Converter. *Energies* **2021**, *14*, 183. https:// doi.org/10.3390/en14010183

Received: 29 November 2020 Accepted: 27 December 2020 Published: 31 December 2020

Publisher's Note: MDPI stays neutral with regard to jurisdictional claims in published maps and institutional affiliations.



Copyright: © 2020 by the authors. Licensee MDPI, Basel, Switzerland. This article is an open access article distributed under the terms and conditions of the Creative Commons Attribution (CC BY) license (https://creativecommons.org/licenses/by/4.0/). reduces the number of output capacitors. In addition, the ZVS on the primary side of the converter and switching current that exhibits an approximately square wave on the secondary side effectively reduce the primary and secondary-side currents; this reduces the switch conduction loss and thus facilitates high efficiency for applications in high-wattage devices. When this structure is used in devices with a high-current output, a center-tap or current-doubler rectifier is typically adopted for rectification on the secondary side of a transformer. Compared with a center-tap structure, a current-doubler structure reduces the conduction loss in the secondary winding of a transformer by half. In a circuit, a magnetic component comprises a transformer and two output inductors, which increases the volume of the circuit. As the number of magnetic components increases, the number of joints in the circuit design also increases; therefore, when a large current is present, integration between windings and circuit components reduces the conduction loss and contact loss.

Relevant literature features the use of integrated magnetic components to integrate transformers and output inductors into one magnetic component, thereby reducing the number of magnetic components in a circuit and improving circuit power density [9–22]. Figure 1a depicts a structure proposed by [11] (hereafter, "Structure I"), in which the transformer winding is wound on the center leg and the output inductor windings on the two outer legs. The number of windings in the structure is similar to that in the conventional current-doubler structure. As the currents on the two outer legs are the original output inductor currents and possess a DC component, air gaps must be added to the outer legs to prevent magnetic core saturation. In such structures, the transformer and inductor windings are separated, and the inductor design is therefore easier to use and more flexible, which prevents the number of turns of the primary winding from being affected by the turns ratio of the converter. In addition, the current in each winding is half of the output current, contributing to a low conduction loss; however, the small self-inductance in the primary winding results in large primary-side current ripples, in turn increasing loss from primary-side components.



Figure 1. Structures and winding layout of five integrated magnetic components: (**a**) structure I, (**b**) structure II, (**c**) structure III, (**d**) structure IV, and (**e**) structure I–IV.

Figure 1b presents a structure proposed by [12] (hereafter, "Structure II"), in which the primary and secondary windings are wound on different magnetic legs. When conduction

occurs on one single side, the primary-side magnetic flux flows to the other leg and generates inductance; accordingly, these transformer windings result in large leakage inductance and thus increase voltage stress on the components. As the output inductance is associated with the turns ratio of the converter, adjustments in the output inductance may involve the number of turns selected for the primary winding in the structure, rendering such adjustments difficult. Moreover, Structure II, in which the self-inductance of the primary winding is the same as that in Structure I, also exhibits large current ripples in the primary winding and hence high losses from primary-side components.

Figure 1c demonstrates a structure developed by [13] (hereafter, "Structure III"), which is an improved version of Structure II. In this structure, the primary winding is separated from the secondary winding; the magnetic flux generated from the primary winding is cancelled out at the center leg and thus can be coupled completely to the secondary winding. This greatly reduces the leakage inductance problem observed in Structure II. In this winding structure, self-inductance on the primary side is related to magnetic reluctance of the outer legs; therefore, this structure alleviates the problem of excessive current ripples in the primary winding. Similarly, to Structure II, the output inductance of Structure III is associated with the converter turns ratio; accordingly, adjustments in the equivalent inductance may require more turns in the primary winding and thus increase loss on the primary side. The total loss for Structure III is in the middle of the total loss values for the three structures.

Figure 1d depicts a structure proposed by [14] (hereafter, "Structure IV"), which has one winding more than Structure III does, providing a more flexible design with equivalent output inductance; this resolves the difficulty in adjusting output inductance for an integrated transformer. Unlike Structure III, Structure IV does not require the number of primary winding turns to be increased for the equivalent output inductance to be increased, and it thus reduces conduction loss. Its total loss is the lowest among the first four structures.

Figure 1e is a structure developed by [15] (hereafter, "Structure V"), in which an EE core transformer and a UI core inductor are integrated into a four-leg core and applied in a phase-shifted full-bridge converter with a center-tap secondary side. This four-leg core is composed of two center legs, one of which is an ungapped second magnetic leg, or left center leg, used for the primary and secondary windings of the transformer; the other is an air-gapped third magnetic leg, or right center leg, used for inductor winding and for the prevention of core saturation. Compared with the aforementioned three-leg-core integrated transformers and inductors, Structure V provides reduced losses from the primary and secondary windings and an intermediate core loss. It thus has the lowest total loss among the five core structures.

For a power output of 1.2 kW, Figure 2 illustrates the total losses of five transformer structures in the same output inductance condition. Among Structures I-IV, Structures I and II exhibit relatively low self-inductance on the primary side and thus larger current ripples on the primary side. In addition, because interleaved winding cannot be used to reduce the amplitude of the magnetomotive force in winding layers for Structure II, it has the highest conduction loss among the first four structures. The winding method in Structure III alleviates the problem observed in Structure II but requires more turns to achieve high output inductance, which contributes to the additional conduction loss in Structure III. Structure IV increases the number of windings to facilitate a flexible output inductance design. Although it yields lower total loss than the first three structures do, it exhibits a high level of conduction loss. Structure V, compared with the other four structures, has relatively high primary-side self-inductance and, thus, smaller current ripple, reducing the AC conduction loss through the interleaved windings; in addition, Structure V facilitates flexible inductor design because of its additional winding and, due to its low magnetic flux density, has a lower hysteresis loss compared with Structure IV. Analysis of and comparison among the five magnetic components reveals that the lowest loss is in Structure V.



Figure 2. Comparison of the losses from five integrated magnetic components.

In this study, an integrated transformer in a phase-shifted full-bridge converter with a center-tap secondary side was applied. A transformer and an inductor were integrated into one core to reduce volume, but the mutual inductance between them caused the inductance to vary across switching modes. In Section II of this study, the inductance formula for each time interval is derived, a method is proposed for minimizing the variation of current ripples in core inductors to a level approaching that before the integration, and design steps are consolidated for optimizing loss by selecting an appropriate core volume. Section III reveals the experimental results; describes the application of the integrated core in a phase-shifted full-bridge converter with an input voltage of 400 V, output voltage of 12 V, and output wattage of 1.7 kW; provides an analysis of the loss distribution in the circuit; and verifies the theoretical analyses with the experimental results. Finally, Section IV concludes the study.

2. Operating Theory

Figure 3 illustrates the circuit of a phase-shifted full-bridge converter that has a fourleg-core integrated magnetic component applied in its center-tap secondary side. The section analyzes the equivalent inductance in various intervals during the circuit operation.



Figure 3. Application of an integrated magnetic component in a phase-shifted full-bridge converter.

Figure 4a depicts the side view of the magnetic component on which parameters are marked, and Figure 4b presents the equivalent magnetic path model. Each of A_1 – A_5 represents the effective cross-sectional area of a magnetic leg; R_{m1} – R_{m5} represent the reluctance corresponding to each of the respective magnetic legs, and R_{m5} is the reluctance between the transformer and inductor, which affects the coupling effect of the two and thus greatly influences the overall magnetic path analysis. To simplify the production

and design of a magnetic core, the outer legs are usually designed symmetrically, and the effective cross-sectional area is designed to be half the cross-sectional area of the second leg to satisfy $R_{m1} = R_{m4} = R_m$ and $R_{m2} = 0.5R_m$. $\Phi_1 - \Phi_5$ is the magnetic flux in each leg. The transformer and output inductor windings are wound around the second and third legs, which correspond to A_2 and A_3 , respectively; magnetomotive force is represented by voltage source symbols; N_p refers to the number of turns in the converter primary winding P; N_S is the number of turns in each of the secondary windings, S_1 and S_2 ; and N_L is the number of turns in the inductor winding L.



Figure 4. Integrated magnetic component: (a) Schematic and (b) Equivalent reluctance model.

In a duty cycle of the phase-shifted full-bridge converter shown in Figure 3, the switches must be switched in turn to transfer the energy of the input voltage source V_{in} to the output voltage source V_0 ; the converter contains six switching modes, which are marked as modes 1–6 in Figure 5a–f. By marking the corresponding duty interval of each switching mode as intervals 1–6 and the corresponding switching time point as t_0 – t_6 , we produced the voltage and current waveforms of the equivalent circuit (Figure 5) in a duty cycle, as shown in Figure 6. For comparison, the corresponding waveform before the transformer and inductor were integrated into one core represented as a dotted line along the i_L waveform. Subsequently, the corresponding magnetic flux waveforms for the magnetic paths in Figure 4b in a duty cycle are presented in Figure 6. The various slopes of i_L waveforms in time intervals 1–6 reveal that the equivalent inductance differs across intervals, which was attributable to the mutual effects, namely mutual inductance, between the transformer and inductor in the integrated magnetic component. The equivalent inductance of *L* in intervals 1–6, namely switching modes 1–6, was presented using L_{eqx} , with the *x* value in the subscript ranging from 1 to 6.



Figure 5. Cont.



Figure 5. Six switching modes of the phase-shifted full-bridge converter: (**a**) Mode 1, (**b**) Mode 2, (**c**) Mode 3, (**d**) Mode 4, (**e**) Mode 5 and (**f**) Mode 6.



Figure 6. Voltage waveform, current waveform, and magnetic flux waveform of each magnetic path during a duty cycle.

The following sections analyze the circuit operation during the six intervals. Interval 1 ($t_0 \le t < t_1$)

In this interval, the voltage between the two bridge arms on the primary side (V_{AB}) is the input voltage (V_{in}). If the equivalent inductance mapped to the primary side from the output inductance is posited to be much higher than the additional resonant inductance,

the voltage of the primary winding of the transformer can be assumed to be the input voltage. At this time, the magnetic flux in the second leg is increasing; in addition, because the primary and secondary windings are wound on the same leg, satisfactory coupling between the primary and secondary sides can be assumed. On the basis of this relationship

between the primary and secondary sides can be assumed. On the basis of this relationship, the voltage of the winding on the third leg (V_L) is calculated as in Equation (1). As the voltage of the third-leg winding is greater than 0 in this interval, the magnetic flux in the third leg is also increasing.

$$V_{\rm L} = L_{\rm eq1} \frac{di_{\rm L}}{dt} \cong \frac{V_{\rm in}}{n} - V_{\rm o} \tag{1}$$

Interval 2 ($t_3 \leq t < t_4$)

The voltage between the two bridge arms on the primary side is $-V_{in}$. The magnetic flux variation in the third-leg winding during this interval and that in the positive energy transfer interval are both positive. However, the second-leg winding voltage changes from a positive value to a negative one, indicating a slight difference in the equivalent inductance.

Interval 3 ($t_1 \leq t < t_2$)

The voltage between the two bridge arms is short-circuited by the primary-side switches, and the primary winding voltage and the magnetic flux variation in the second leg are therefore both assumed to be zero. Due to the satisfactory coupling between the windings on the second leg, the primary winding voltage is assumed to be zero. According to the loop on the secondary side, the voltage of the third-leg winding is $-V_0$; in this interval, the magnetic flux variation in the third leg is negative.

$$V_{\rm L} = -V_{\rm o} \tag{2}$$

Interval 4 ($t_4 \leq t < t_5$)

Interval 4 is mostly identical to interval 3, except that the current of the third-leg winding is changed from the current of winding S_1 to that of winding 2. Therefore, the equivalent output inductance in this interval is the same as that in the positive energy transfer interval.

Interval 5 ($t_2 \leq t < t_3$)

The current between the two bridge arms on the primary side changes from a shortcircuit state to $-V_{in}$. Deduction shows that the transformer winding voltage approaches zero and that the magnetic flux variation in the second leg is zero. Voltage across the additional resonant inductance and the leakage inductance of the transformer, and the third-leg winding voltage is $-V_o$.

Interval 6 ($t_5 \leq t < t_6$)

Interval 6 is identical to Interval 5 except for the change in the primary winding voltage. In this study, following operation analysis for each interval, the equivalent inductance and magnetic flux corresponding to each interval were investigated. To prevent saturation, the magnetic flux in each magnetic path must be obtained. As a magnetic flux comprises both DC and AC, they can be calculated separately and the total calculated to determine the magnetic flux in each magnetic path. As DC is present only in the third leg, the DC magnetic fluxes in all windings are provided by the third leg. Accordingly, a reluctance model reveals the DC magnetic flux in each magnetic flux in each magnetic path; R₅, particularly, exerts a major influence on the distribution of DC magnetic fluxes.

An AC magnetic flux, due to its characteristics, can be assumed to provide a current source. The size of such a current source is determined by the volt-second balance of the winding, namely the magnetic flux variation of the winding, and the direction of the current source is determined by the voltage and current directions determined by the winding. Figure 7a–d shows four different AC magnetic flux models. Subsequently, the total of the AC and DC components is calculated to obtain the total magnetic flux of each magnetic path.



Figure 7. Definition of AC magnetic flux models.

To analyze the equivalent inductance in each interval, the reluctance model in Figure 4b is first used to obtain the magnetic flux formulae for the second and third legs:

$$\Phi_{2} = \frac{N_{p}i_{p}(t) + N_{s}i_{s2}(t) - N_{s}i_{s1}(t)}{R_{m2} + (R_{m3}/R_{m4} + R_{m5})/R_{m1}} + \frac{N_{L}i_{L}(t)}{R_{m3} + (R_{m1}/R_{m2} + R_{m5})/R_{m4}} \frac{R_{m4}}{R_{m4} + R_{m5} + R_{m1}/R_{m2}} \frac{R_{m1}}{R_{m1} + R_{m2}} (3)$$

$$\Phi_{3} = \frac{N_{p}i_{p}(t) + N_{s}i_{s2}(t) - N_{s}i_{s1}(t)}{R_{m2} + (R_{m3}/R_{m4} + R_{m5})/R_{m1}} \frac{R_{m1}}{R_{m1} + R_{m5} + R_{m3}/R_{m4}} \frac{R_{m4}}{R_{m3} + R_{m4}} + \frac{N_{L}i_{L}(t)}{R_{m3} + (R_{m1}/R_{m2} + R_{m5})/R_{m4}} (4)$$

Subsequently, per Figure 7, a relationship formula between winding voltage and magnetic flux is established:

$$\begin{bmatrix} V_{\rm p} \\ V_{\rm s1} \\ V_{\rm s2} \\ V_{\rm L} \end{bmatrix} = \begin{bmatrix} N_{\rm p} & 0 \\ N_{\rm s} & 0 \\ -N_{\rm s} & 0 \\ 0 & N_{\rm L} \end{bmatrix} \begin{bmatrix} \frac{d\Phi_2}{dt} \\ \frac{d\Phi_3}{dt} \end{bmatrix}$$
(5)

According to the definition of inductance, Equation (5) can then be expressed as a relationship formula between inductance and current:

$$\begin{bmatrix} V_{\rm p} \\ V_{\rm s1} \\ V_{\rm s2} \\ V_{\rm L} \end{bmatrix} = \begin{bmatrix} L_{\rm p,p} & M_{\rm p,s_1} & M_{\rm p,s_2} & M_{\rm p,L} \\ M_{\rm s_1,p} & L_{\rm s_1,s_1} & M_{\rm s_1,s_2} & M_{\rm s_1,L} \\ M_{\rm s_2,p} & M_{\rm s_2,s_1} & L_{\rm s_2,s_2} & M_{\rm s_2,L} \\ M_{\rm L,p} & M_{\rm L,s_1} & M_{\rm L,s_2} & L_{\rm L,L} \end{bmatrix} \begin{bmatrix} \frac{d_{\rm p}}{dt} \\ \frac{d_{\rm is_1}}{dt} \\ \frac{d_{\rm is_2}}{dt} \\ \frac{d_{\rm is_2}}{dt} \\ \frac{d_{\rm is_2}}{dt} \end{bmatrix}$$
(6)

where $L_{x,y}$ and $M_{x,y}$ refer to self-inductance and mutual inductance, respectively, and the *x* and *y* in the subscript is the code for each winding presented in Figure 4.

Through substitution of the voltage condition for each interval into Equation (6) and use of the definitions for transformer turns ratio $(N_p/N_s = n)$ and input voltage ratio $(k = nV_o/V_{in})$, the winding equivalent inductance in each interval is derived.

$$L_{\rm eq1} = \frac{1-k}{1-k-n\frac{N_{\rm L}R_{\rm m}^2}{N_{\rm p}\left(R_{\rm m}^2+2R_{\rm m}R_{\rm m3}+R_{\rm m}R_{\rm m5}+R_{\rm m3}R_{\rm m5}\right)}}\frac{N_{\rm L}^2(2R_{\rm m}+R_{\rm m5})}{R_{\rm m}^2+2R_{\rm m}R_{\rm m3}+R_{\rm m}R_{\rm m5}+R_{\rm m3}R_{\rm m5}}$$
(7)

$$L_{\rm eq3} = \frac{N_{\rm L}^2 (2R_{\rm m} + R_{\rm m5})}{R_{\rm m}^2 + 2R_{\rm m}R_{\rm m3} + R_{\rm m}R_{\rm m5} + R_{\rm m3}R_{\rm m5}}$$
(8)

$$L_{\rm eq5} = \frac{k}{k - n \frac{N_{\rm L} R_{\rm m}^2}{N_{\rm p} \left(R_{\rm m}^2 + 2R_{\rm m} R_{\rm m3} + R_{\rm m} R_{\rm m5} + R_{\rm m3} R_{\rm m5}\right)}} \frac{N_{\rm L}^2 (2R_{\rm m} + R_{\rm m5})}{R_{\rm m}^2 + 2R_{\rm m} R_{\rm m3} + R_{\rm m} R_{\rm m5} + R_{\rm 3} R_{\rm m5}}$$
(9)

$$L_{eq2} = \frac{1-k}{1-k+n\frac{N_L R_m^2}{N_p \left(R_m^2 + 2R_m R_{m3} + R_m R_{m5} + R_{m3} R_{m5}\right)}} \frac{N_L^2 (2R_m + R_{m5})}{R_m^2 + 2R_m R_{m3} + R_m R_{m5} + R_{m3} R_{m5}}$$
(10)

$$L_{\rm eq4} = \frac{N_{\rm L}^2(2R_{\rm m} + R_{\rm m5})}{R_{\rm m}^2 + 2R_{\rm m}R_{\rm m3} + R_{\rm m}R_{\rm m5} + R_{\rm m3}R_{\rm m5}}$$
(11)

$$L_{\rm eq6} = \frac{k}{k + n \frac{N_{\rm L} R_{\rm m}^2}{N_{\rm p} \left(R_{\rm m}^2 + 2R_{\rm m} R_{\rm m3} + R_{\rm m} R_{\rm m5} + R_{\rm m3} R_{\rm m5}\right)}} \frac{N_{\rm L}^2 (2R_{\rm m} + R_{\rm m5})}{R_{\rm m}^2 + 2R_{\rm m} R_{\rm m3} + R_{\rm m} R_{\rm m5} + R_{\rm m3} R_{\rm m5}}$$
(12)

The common term in Equations (7)–(12) is substituted with a single variable to obtain Equation (13).

$$\alpha = n \frac{N_L R_m^2}{N_P (R_m^2 + 2R_m R_{m3} + R_m R_{m5} + R_{m3} R_{m5})}$$
(13)

 L_{eq3} (represented as $L_{eqx,Nor}$) is then normalized and Equations (7)–(12) are reorganized as Equations (14)–(19).

$$L_{\text{eq1,Nor}} = \frac{1-k}{1-k-\alpha} \tag{14}$$

$$L_{\rm eq3,Nor} = 1 \tag{15}$$

$$L_{\rm eq5,Nor} = \frac{k}{k - \alpha} \tag{16}$$

$$L_{\text{eq2,Nor}} = \frac{1-k}{1-k+\alpha} \tag{17}$$

$$L_{\rm eq4,Nor} = 1 \tag{18}$$

$$L_{\rm eq6,Nor} = \frac{k}{k+\alpha} \tag{19}$$

Because *k* and α are both positive, according to Equations (14)–(19), the smaller α is, the closer the inductance values from after integration and before integration are in each interval. When $\alpha = 0$, the inductance in each interval is identical before and after integration. This result is also verified with the sensitivity curves of $L_{eq2,Nor}$ in relation to *k* and α (Figure 8).



Figure 8. Sensitivity curves of $L_{eq2,Nor}$ to *k* and α .

$$L_{o_eff} = \frac{\frac{V_{in}}{n} - V_o}{\Delta I_L 2 f_s} k$$
(20)

$$L_{\rm o_eff} = L_{\rm eq2} \tag{21}$$

where $\Delta I_{\rm L}$ is the inductor ripple current, and $f_{\rm s}$ is the switching frequency of the converter. For design requirements, $L_{\rm eq2}$ is simplified. As an air gap is added to the third leg,

meaning that $R_{m3} >> R_m$ and R_{m5} , Equation (8) can be simplified to Equations (22) and (23):

$$L_{\rm eq3} = \frac{N_{\rm L}^2(2R_{\rm m} + R_{\rm m5})}{R_{\rm m3} \left(\frac{R_{\rm m}^2}{R_{\rm m3}} + 2R_{\rm m}\frac{R_{\rm m}R_{\rm m5}}{R_{\rm m3}} + R_{\rm m5}\right)} \cong \frac{N_{\rm L}^2(2R_{\rm m} + R_{\rm m5})}{R_3(2R_{\rm m} + R_{\rm m5})}$$
(22)

Therefore, Equation (23) can be obtained from Equation (17).

$$L_{\rm eq2} \simeq \frac{1-k}{1-k+\alpha} \frac{N_{\rm L}^2}{R_{\rm m3}}$$
 (23)

3. Design Optimization

According to the theoretical analyses in Section II, in this study, a design optimization procedure (Figure 9) was proposed, in which formulae established by this study were imported to a mathematical software package for calculations. Table 1 lists the output specifications; $V_{\rm in} = 400 V_{\rm DC}$, $V_{\rm o} = 12 V_{\rm DC}$, output current = 141.6 A, and output power = 1.7 kW, and a PC95 ferrite core were used in the design of an integrated magnetic component.

Table 1. Phase shift full-bridge converter specification.

Component	Parameters
Output power	1700 W
Input voltage	400 V _{DC}
Output voltage	12 V _{DC}
Output current	141.6 A
Switching frequency	80 kHz



Figure 9. Design optimization procedure for an integrated magnetic component.

On the basis of the design procedure in Figure 9, the integrated ferrite component design was divided into the following five steps.

Step 1:

First, Equation (21) is used for calculation of the inductance before integration that is required for the current ripple requirement to be satisfied. For example, given the specifications $f_s = 80$ kHz, $\Delta I_L = 26$ A, $N_s = 1$, and $N_p = 24$ in this study, we can calculate that n = 24 and $L_{o_{eff}} = 0.8 \mu$ H.

Step 2:

The equivalent inductance in each interval (Figure 6) is calculated using Equations (7)–(12). To maintain the ripple current (i_L) within the upper limit, because the equivalent output inductance in a duty cycle is L_{eq2} , we let $L_{eq2} = L_{o_eff}$. In addition, the air gap is assumed to be added to the third leg such that $R_3 >> R$ and R_5 , and α can thus be seen to have an extremely small value; therefore, $L_{eq3} \cong L_{eq2} = L_{o_eff}$. Inductance loss caused by the duty cycle must also be prevented from becoming excessively close to α in the design. By the end of Step 2, the equivalent inductance in each interval is obtained.

Step 3:

Reluctance R_{m1} – R_{m5} (Figure 4b) in the respective magnetic paths is calculated. As mentioned in Section II, to simplify the design, we define $R_{m1} = R_{m4} = R_m$, $R_{m2} = 0.5R_m$, and

$$\beta = \frac{R_{\rm m5}}{R_{\rm m}} \tag{24}$$

By substituting these definitions into Equations (8) and (9), we obtain the following Equations.

$$R_{\rm m3} = \frac{N_{\rm L}^2 \left[1 - \frac{\alpha}{N_{\rm L}} (1+\beta) \right]}{L_{\rm eq3}}$$
(25)

$$R_{\rm m5} = \frac{\beta N_{\rm L} \alpha (2+\beta)}{L_{\rm eq3}} \tag{26}$$

Step 4:

The cross-sectional area of each leg is calculated. As the reluctance of each magnetic path is known by this step, the method for calculating AC and DC, separately described in Section II, is used to determine the magnetic flux in each magnetic path of the ferrite core at full load. As the values of DC and voltage across the windings are known, the calculation of the magnetic flux for each path also involves only $N_{\rm L}$ and β . Furthermore, to initiate the design from the third leg and to prevent ferrite core saturation, we obtain Equations (27) and (28).

$$A_{3_\min} = \frac{\Phi_{3_\max}}{B_{\max}}$$
(27)

$$l_{\rm g} = \mu_0 A_3 R_{\rm m3} \tag{28}$$

where A_{3_min} is the minimum value required for A_3 , Φ_{3_max} is the maximum working value for Φ_3 , B_{max} is the saturated magnetic flux density specified in the material specifications of the ferrite core, l_g is the air gap length in the third leg, and μ_0 is the permeability of free space. Substituting different N_L and β values into Equations (27) and (28) reveals that A_3 and l_g are affected mainly by N_L and only slightly by β , as depicted in Figure 10a,b. In this study, in which the feasibility of ferrite core volume and air gap length were considered, $N_L = 2$. Accordingly, β was the only variable whose value was undetermined.



Figure 10. (a) Curve of A_3 in relation to N_L and β (b) Curve of l_g in relation to N_L and β .

For the design of the effective cross-sectional area of each leg, two factors should be considered: preventing saturation and hysteresis loss in the ferrite core. Let the subscript y, ranging from 1 to 5, denote the leg; the minimum cross-sectional area required to prevent ferrite core saturation is obtained using Equation (29).

$$A_{y_\min} = \frac{\Phi_{y_max}}{B_{max}}$$
(29)

$$A_{\rm y_loss} = \frac{\Delta \Phi_{\rm y}}{2B_{\rm y_max}} \tag{30}$$

where $\Delta \Phi_y$ is the magnetic flux variation in each leg; this variation can be calculated according to the magnetic density variation with a given loss, which is determined using the curve for ferrite core loss volume density (Pcv) against magnetic flux density (B_{y_max} ; Figure 11). Whichever of Equations (27) and (28) has the greater value is determined as A_y .



Figure 11. Curve for Pcv against magnetic flux density.

Step 5:

The volume parameters and ferrite core loss values are substituted into the formulae. Figure 12 presents the top and side views of the ferrite core marked with geometric dimension variables. Let the geometric dimension parameters of the core be as depicted in Figure 12: L, W, and H are the length, width, and height of the ferrite core, respectively, including the windings; a and b are the length and width of the outer legs in the ferrite core, respectively; c is the width of the second and third legs; d is the length of the second and third legs; p is the distance between an outer leg and the nearest center leg; and 2W is the distance between the two center legs. These parameters were used to obtain the area occupied by ferrite core. In this study, H = 30 mm, and according to Figure 12, $A_1 = A_4 = A_5 = a \times b$, and $A_2 = A_3 = c \times d$. The area of the ferrite core was calculated using Equation (31).

area = W × L =
$$[d + 2min(w, p)][2(p + c + b + w)]$$
 (31)

where min(w,p) refers to the smaller value of w and p. To calculate the volume parameters and the total ferrite core loss, an auxiliary variable γ was required. We assumed that $\gamma = w/c$, selected a core loss density (Pcv), and substituted β and γ into the calculation to obtain the total size and loss of the ferrite core. Figure 13a presents the results obtained for Pcv = 400 kW/m³ and with the substitution of various β and γ values into the calculation. When the area is constant, each β value corresponds to one γ value, and β is therefore represented on the x-axis. With different total areas, the minimum loss corresponds to different β values. Hence, the β values corresponding to the minimum loss for various Pcv values were plotted in Figure 13b. In this manner, the appropriate total area and the Pcv, β , and γ values for the area were determined. With the total size and loss of the circuit considered, the magnetic component parameters were determined as follows for the experiment in this study: area = 1300 mm² and Pcv = 400 kW/m³.



Figure 12. Geometric dimensions of the ferrite core: (a) top view and (b) side view.



Figure 13. Core loss graph with β and γ substituted into the calculation: (**a**) curve representing loss according to β value for various core areas; (**b**) curve representing loss according to area for various Pcv values.

4. Experimental Verification

This section details the application of Structure V, an integrated four-leg magnetic component, in a center-tap phase-shifted full-bridge converter with an input voltage of $400 V_{DC}$, output voltage of $12 V_{DC}$, and output power of 1.7 kW; an analysis on the loss

of the converter; and a test on parameters, including efficiency. Table 2 illustrates the geometric dimensions of the ferrite core designed in accordance with the aforementioned five steps.

Parameter Names	Specifications
a	25 mm
b	4 mm
С	10 mm
d	13 mm
р	6 mm
2w	12 mm
Н	30 mm
W	25 mm
L	52 mm
Ve	20,520 mm ³

Table 2. Design Parameters of Proposed Transformer Structure.

Figure 14a presents the phase-shifted full-bridge converter in which the ferrite core was used for the experiment, Figure 14b shows the integrated ferrite core for the converter, and Table 3 lists the specifications used in the converter design.



Figure 14. Phase-shifted full-bridge converter with an integrated transformer: (a) Physical circuit; (b) Ferrite core.

Table 3. Design parameters of phase shift full-bridge converter.

Component	Parameters	
Transformer turns ratio	24:1	
Transformer material	PC95	
Primary devices	GS66516T	
Secondary devices	FDMS86300DC	
Equivalent output inductance	0.8 μΗ	
External resonant inductance	20 µH	
Magnetizing inductance	1.7 mH	
Output capacitance	4400 μF	

Figure 15 depicts the experimental efficiency of the novel phase-shifted full-bridge converter with an integrated transformer; the curve peaks at 96.7% in the half-load condition. Although wide band gap components greatly reduced the hard switching loss in the light-load condition, switching loss and ferrite core hysteresis loss remained the main sources of loss in the light-load condition. The integrated ferrite core reduced the distance between the center tap and the inductor contact, which reduced loss along the path in the half-to-full-load condition.



Figure 15. Efficiency curve for the phase-shifted full-bridge converter with an integrated transformer.

Figure 16 depicts the loss distribution in 20%, 50%, and 100% load conditions. A model was used to calculate losses, including the hysteresis loss of the transformer and conduction loss in the copper wire, according to the experimental measurements. According to the calculation results, the secondary winding loss of the transformer accounted for most of the total loss.



Figure 16. Loss distribution.

Figure 17a depicts the switching waveforms of the leading bridge arm switch in various load conditions. When the output load was greater than 45 A, the leading bridge arm switch achieved ZVS, and no Miller plateau was observed in the switch gate voltage. Figure 17b reveals the switching waveforms of the lagging bridge arm switch in various load conditions. As seen in Figure 17a, hard switching was implemented; to improve efficiency, the dead time was designed to occur between the switch series resonance and the wave trough to reduce the loss incurred from hard switching. When the load current was greater than 80 A, the lagging bridge arm switch achieved ZVS, and no Miller plateau was observed in the switch gate voltage. Figure 17c presents the waveforms of the novel converter with an integrated transformer in the full-load condition. A measurement of the current ripples in the output inductor revealed waveforms similar to the analysis result proposed in Section II. The actual equivalent inductance was determined from the slope of the current.



Figure 17. Measured waveforms: (a) Turn on transient of low side switch of leading leg at 30 and 45 A output load. (b) Turn on transient of low side switch of lagging leg at 45 and 80 A output load (c) output capacitor current ripple at full output load.

In the full-load condition, the temperatures of the main components were as shown in Figure 18. The temperature of the bridge arm switch was approximately 43 $^{\circ}$ C (Figure 18a); the highest temperature was observed in the secondary winding of the transformer at 94 $^{\circ}$ C (Figure 18b).



Figure 18. Measured temperatures: (a) primary-side switch and (b) secondary winding of the transformer.

5. Conclusions

This study involved the application of a four-leg ferrite core that integrated a transformer and an inductor on a center-tap phase-shifted full-bridge converter and the analysis of the integrated transformer and formulae in six switching modes. Subsequently, a method was proposed for determining the dimensions of the ferrite core and, according to the resultant core volume, a design procedure was established for optimizing component loss. A converter was used to verify the performance of the proposed integrated transformer; analyze the loss distribution of the transformer; and measure relevant parameters including the waveform and efficiency to validate the proposed design procedure. Finally, in accordance with the design optimization procedure, an integrated magnetic component was designed and applied in a phase-shifted full-bridge converter to achieve an input voltage of 400 V_{DC}, output voltage of 12 V_{DC}, output power of 1.7 kW, and switching frequency of 80 kHz. The highest conversion efficiency achieved by the converter was 96.7%, and the power density was 150 W/inch³.

Author Contributions: Y.-C.L. and C.-Y.X. conceived and designed the prototype and experiments; C.-Y.X., C.-C.H., P.-C.C. performed the experiments; Y.-C.L.; C.-Y.X. analyzed the data; Y.-C.L., C.-Y.X., C.-C.H., and H.-J.C. contributed equipment/materials/analysis tools; Y.-C.L.; C.-Y.X. and C.-C.H. wrote the paper. All authors have read and agreed to the published version of the manuscript.

Funding: This work was supported in part by the Ministry of Science and Technology, Taiwan under Grant 109-2221-E-197 -014 - and 109-2221-E-002 -097 -.

Institutional Review Board Statement: Not applicable.

Informed Consent Statement: Not applicable.

Data Availability Statement: Not applicable.

Conflicts of Interest: The authors declare no conflict of interest. The funders had no role in the design of the study; in the collection, analyses, or interpretation of the data; in the writing of the manuscript, or in the decision to publish the results.

References

- Yang, B.; Lee, F.C.; Zhang, A.J.; Huang, G. LLC resonant converter for front end DC/DC conversion. In Proceedings of the APEC Seventeenth Annual IEEE Applied Power Electronics Conference and Exposition (Cat. No. 02CH37335), Dallas, TX, USA, 10–14 March 2002; IEEE: Piscataway, NJ, USA, 2002; pp. 1108–1112.
- Yang, B.; Chen, R.; Lee, F.C. Integrated magnetic for LLC resonant converter. In Proceedings of the APEC Seventeenth Annual IEEE Applied Power Electronics Conference and Exposition (Cat. No. 02CH37335), Dallas, TX, USA, 10–14 March 2002; IEEE: Piscataway, NJ, USA, 2002; pp. 346–351.
- Lu, B.; Liu, W.; Liang, Y.; Lee, F.C.; Van Wyk, J.D. Optimal design methodology for LLC resonant converter. In Proceedings of the Twenty-First Annual IEEE Applied Power Electronics Conference and Exposition, 2006. APEC '06, Dallas, TX, USA, 19–23 March 2006; IEEE: Piscataway, NJ, USA, 2006; pp. 533–538.
- 4. Redl, R.; Sokal, N.O.; Balogh, L. A novel soft-switching full-bridge DC/DC converter analysis, design consideration, and experimental results at 1.5 kW, 100 kHz. *IEEE Trans. Power Electron.* **1991**, *6*, 408–418. [CrossRef]
- Redl, R.; Balogh, L.; Edwards, D.W. Switch transitions in the soft-switching full-bridge PWM phase-shift DC/DC converter: Analysis and improvements. In Proceedings of the Intelec 93: 15th International Telecommunications Energy Conference, Paris, France, 27–30 September 1993; IEEE: Piscataway, NJ, USA, 2002; pp. 350–357.
- Redl, R.; Balogh, L.; Edwards, D.W. Optimum ZVS full-bridge dc/dc converter with PWM phase-shift control: Analysis, design considerations, and experimentation. In Proceedings of the 1994 IEEE Applied Power Electronics Conference and Exposition—ASPEC'94, Orlando, FL, USA, 13–17 February 1994; IEEE: Piscataway, NJ, USA, 2002; pp. 159–165.
- Cho, J.G.; Sabate, J.A.; Lee, F.C. Novel full bridge zero-voltage transition PWM DC/DC converter for high power applications. In Proceedings of the 1994 IEEE Applied Power Electronics Conference and Exposition-ASPEC'94, Orlando, FL, USA, 13–17 February 1994; IEEE: Piscataway, NJ, USA, 2002; pp. 143–149.
- Ruan, X.; Liu, F. An improved ZVS PWM full-bridge converter with clamping diodes. In Proceedings of the 2004 IEEE 35th Annual Power Electronics Specialists Conference (IEEE Cat. No.04CH37551), Aachen, Germany, 20–25 June 2004; IEEE: Piscataway, NJ, USA, 2004; pp. 1476–1481.
- Badstuebner, U.; Biela, J.; Faessler, B.; Hoesli, D.; Kolar, J.W. An optimized 5kW, 147 W/in3 telecom phase-shift DC-DC converter with magnetically integrated current doubler. In Proceedings of the 2009 Twenty-Fourth Annual IEEE Applied Power Electronics Conference and Exposition, Washington DC, USA, 15–19 February 2009; IEEE: Piscataway, NJ, USA, 2009.

- 10. Badstuebner, U.; Biela, J.; Faessler, B.; Hoesli, D.; Kolar, J.W. Optimization of a 5 kW telecom phase shift DC–DC converter with magnetically integrated current doubler. *IEEE Trans. Ind. Electron.* 2001, *58*, 4736–4745. [CrossRef]
- 11. Sun, J.; Webb, K.F.; Mehrotra, V. Integrated magnetics for current-doubler rectifiers. *IEEE Trans. Power Electron.* **2004**, *19*, 582–590. [CrossRef]
- Chen, W.; Hua, G.; Sable, D.; Lee, F.C. Design of High Efficiency, Low Profile Low Voltage Converter with Integrated Magnetics. In Proceedings of the APEC 97-Applied Power Electronics Conference, Atlanta, GA, USA, 27 February 1997; IEEE: Piscataway, NJ, USA, 2002.
- Xu, P.; Wu, Q.; Wong, P.L.; Lee, F.C. A novel integrated current doubler rectifier. In Proceedings of the APEC 2000. Fifteenth Annual IEEE Applied Power Electronics Conference and Exposition (Cat. No.00CH37058), New Orleans, LA, USA, 6–10 February 2000; IEEE: Piscataway, NJ, USA, 2000; Volume 2, pp. 735–740.
- Sun, J.; Webb, K.F.; Mehrotra, V. An improved current-doubler rectifier with integrated magnetics. In Proceedings of the APEC. Seventeenth Annual IEEE Applied Power Electronics Conference and Exposition (Cat. No. 02CH37335), Dallas, TX, USA, 10–14 March 2002; IEEE: Piscataway, NJ, USA, 2002; pp. 831–837.
- Lu, Z.; Yang, H.; Zhang, A.J. New magnetic integration of full-wave rectifier with center-tapped transformer. In Proceedings of the 2014 International Power Electronics and Application Conference and Exposition, Shanghai, China, 5–8 November 2014; IEEE: Piscataway, NJ, USA, 2015.
- Pietkiewicz, A.; Tollik, D. Coupled-inductor current-doubler topology in phase-shifted full-bridge dc-dc converter. In Proceedings of the INTELEC-Twentieth International Telecommunications Energy Conference (Cat. No. 98CH36263), San Francisco, CA, USA, 4–8 October 1998; IEEE: Piscataway, NJ, USA, 2002; pp. 41–48.
- Wong, P.L.; Wu, Q.; Xu, P.; Yang, B.; Lee, F.C. Investigating coupling inductors in the interleaving QSW VRM. In Proceedings of the APEC 2000. Fifteenth Annual IEEE Applied Power Electronics Conference and Exposition (Cat. No.00CH37058), New Orleans, LA, USA, 6–10 February 2000; IEEE: Piscataway, NJ, USA; pp. 973–978.
- Wong, P.L.; Lee, F.C.; Jia, X.; Van Wyk, D. A novel modeling concept for multi-coupling core structures. In Proceedings of the APEC 2001. Sixteenth Annual IEEE Applied Power Electronics Conference and Exposition (Cat. No.01CH37181), Anaheim, CA, USA, 4–8 March 2001; IEEE: Piscataway, NJ, USA, 2002.
- Xu, P.; Lee, F.C. Design of high-input voltage regulator modules with a novel integrated magnetic. In Proceedings of the APEC 2001. Sixteenth Annual IEEE Applied Power Electronics Conference and Exposition (Cat. No.01CH37181), Anaheim, CA, USA, 4–8 March 2001; IEEE: Piscataway, NJ, USA, 2002; Volume 1.
- Xu, P.; Ye, M.; Lee, F.C. Single Magnetic Push-Pull Forward Cooveener Fatunng Built-in Input Filter and Coupled-Inductor Current Doubler for 48V VRM. In Proceedings of the IEEE Applied Power Electronics Conference and Exposition (APEC'02), Dallas, TX, USA, 10–14 March 2002; IEEE: Piscataway, NJ, USA, 2002.
- 21. Xu, P.; Ye, M.; Wong, P.L.; Lee, F.C. Design of 48 V voltage regulator modules with a novel integrated magnetics. *IEEE Trans. Power Electron.* **2002**, *17*, 990–998.
- Zhou, H.; Wu, T.X.; Batarseh, I.; Ngo, K.D. Comparative investigation on different topologies of integrated magnetic structures for current-doubler rectifier. In Proceedings of the 2007 IEEE Power Electronics Specialists Conference, Orlando, FL, USA, 17–21 June 2007; IEEE: Piscataway, NJ, USA, 2007; pp. 337–342.