



Article An Improved Finite Control Set Model Predictive Current Control for a Two-Phase Hybrid Stepper Motor Fed by a Three-Phase VSI

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Abstract: In this paper, an improved finite control set model predictive current control (FCS-MPCC) is proposed for a two-phase hybrid stepper motor fed by a three-phase voltage source inverter (VSI). The conventional FCS-MPCC selects an optimal voltage vector (VV) from six active and one null VVs by evaluating a simple cost function and then applies the optimal VV directly to the VSI. Though the implementation is simple, it features a large current ripple and total harmonic distortion (THD). The proposed improved FCS-MPCC builds an extended control set consisting of 37 VVs to replace the original control set with only seven VVs. The increase in the amount of VVs helps to regulate the current more accurately. In each control period, the improved FCS-MPCC takes advantage of deadbeat control to calculate a reference VV, and only the three VVs adjacent to the reference VV are predicted and evaluated, which decrease the computational workload significantly. Build waveform patterns for all VVs in the unbalanced circuit structure to modulate the optimal VV using discrete space vector modulation, which improves the current quality in reducing current ripple and THD. The comparative simulations and experimental results validate the effectiveness of the proposed method.

Keywords: stepper motor; voltage source inverter; model predictive control; finite control set

1. Introduction

Hybrid stepper motors (HSM) are widely used in industrial and consumer applications such as textile machines, robotic arms, scanners, printers, etc., due to their low manufacturing cost, high reliability, and open-loop control capabilities [1]. HSMs can operate either in open-loop or closed-loop, and both have similar drive topologies, i.e., each stator winding is driven by an independent H-bridge, H-bridge switches are regulated by a current controller according to the reference current, which is, in turn, generated by a position or speed controller.

Generally, HSMs are operated in the open-loop and have been heavily investigated. The expensive position or speed sensor is no longer needed, which is appealing for industrial and domestic applications [2]. Because the position or speed controller cannot acquire the real position and speed information in real-time, the waveform of reference current needs to be pre-determined. Generally, the maximum current amplitude allowed is chosen to generate maximum torque to avoid step-out. This no-optimal drive method causes low energy efficiency, large torque ripple, and resonance problems [3]. Microstepping has been used to improve the motion stability and positioning resolution, which enables the HSM used in the more demanding applications [4]. However, it is rare to use microstepping in position or speed tracking applications, as microstepping is carried out at a fixed stepping rate. A frequency modulation-based microstepping algorithm is proposed in [5], enabling



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Copyright: © 2022 by the authors. Licensee MDPI, Basel, Switzerland. This article is an open access article distributed under the terms and conditions of the Creative Commons Attribution (CC BY) license (https:// creativecommons.org/licenses/by/ 4.0/). the microstepping to be used in real-time precise position tracking. However, problems remain, such as step-out due to load torque variation, low energy efficiency, etc.

With the demanding positioning accuracy and the progress of control theory, the position and speed closed loop is broadly used nowadays. Numerous control strategies, such as the conventional proportional integral (PI) [6] and proportional integral derivative (PID) [7], and advanced control strategies, such as fuzzy logic control (FLC) [8], neural network (NN) [9], neuro-fuzzy control (NFC) [10], sliding mode control (SMC) [11], and model predictive control (MPC) [12], are widely used. Generally, a cascade structure is employed, i.e., the outer loop generates reference electromagnetic torque firstly; then, it uses maximum torque per ampere, direct torque control, and field-oriented control (FOC), etc., to modulate a reference current in real-time according to rotor position and speed; finally, the inner current controller regulates stator winding current according to the reference current. In addition, with the continuous improvement of the computing power of microcontrollers, state observer-based sensorless control is a promising method in space or cost restrict occasions, which estimates position and speed information instead of measuring them. A sliding mode observer (SMO)-based sensorless controller is proposed for the speed control of the HSM in [13], where the chattering is solved by embedding FLC into SMO to adjust the observer gain adaptively.

In high-performance applications, no matter what means the position and speed controller adopts, the inner current controller usually uses closed-loop strategies, such as PI [14] and hysteresis control [11]. PI gains widely successful in the application of linear time-invariant systems, but it is difficult to obtain satisfactory results for nonlinear time-varying systems such as stepper motors. Hysteresis control is robust in parameter variations and load torque disturbance, but it results in variable switching frequency, further increasing the switching loss and electromagnetic interference emission [15]. MPC is not a new technology, and it has been in use for decades due to its intuitive concept, simple implementation, straightforward handling of nonlinearities and constraints, and good dynamic performance [16]. In earlier industrial applications, limited by the computing power of the hardware at the time, the implementation of MPC has been restricted to slowly varying process industries. Nowadays, along with the revolutionary development of microprocessor technology, MPC has been implemented successfully in fast-varying systems, such as power converters and drives [17–19].

The MPC is classified into finite control set MPC (FCS-MPC) and continuous control set MPC (CCS-MPC) when applied to power converters [20]. CCS-MPC uses a predictive model to compute a continuous control signal and then utilizes a modulator to generate the desired signal to power converters. By taking advantage of the inherent discrete nature of the power converters, FCS-MPC evaluates only a limited number of switching states of the power converter to solve the optimization problem, and then exerts the optimal control action directly to converters [21].

MPC has been successfully applied in induction motors, brushless DC motors, and permanent magnet synchronous motors. Ref. [12] employs two discrete MPCs with the same structure to adjust the direct-axis current i_d and quadrature-axis current i_q under the synchronous d-q frame, respectively. Though CCS-MPC has advantages in long-horizon prediction, its algorithm is complex, and the case of the optimal root exceeding the maximum voltage has not been solved in [12]. Ref. [22] presents a synchronous frame current control strategy for a two-phase linear stepper motor fed by a three-phase VSI. Though they improve the space-vector pulse width modulation (SVPWM) to match the unbalanced circuit structure, the maximum winding voltage is about 70% of the power supply, which is low energy efficiency. In this study, an improved finite control set model predictive current control (FCS-MPCC) is proposed for a two-phase HSM fed by a three-phase VSI.

The main contributions of this paper are as follows:

(1) The two-phase HSM is fed by a low-cost, high-compatibility three-phase two-level VSI, and two-phase currents are regulated simultaneously by an improved FCS-MPCC.

- (2) An extended control set having 37 VVs is built to replace the conventional control set with only six active VVs and one null VVs. The increased VVs improve current quality in rejecting of current ripple and decreasing total harmonic distortion (THD).
- (3) Compared with the conventional FCS-MPCC, although the number of admissible VVs in the control set is greatly increased, the workload of determining an optimal VV is significantly reduced. This is because only three VVs adjacent to the reference VV are evaluated in each control period; where the reference VV is obtained using deadbeat control, correspondingly, seven VVs need to be processed in the conventional method.
- (4) Build general waveform patterns for each sector, and then instantiate each generalized waveform using corresponding action time to obtain 37 specific waveforms for all VVs. Discrete space vector modulation (DSVM) modulates an optimal VV using the specific waveform related to the VV.

The remainder of the paper is organized as follows. In Section 2, the dynamic model of HSM is introduced. Section 3 presents the conventional FCS-MPCC. Section 4 presents the improved FCS-MPCC. Section 5 contains the simulations and experimental results. Finally, Section 6 concludes this article.

2. Dynamic Model of Two-Phase HSM

The dynamics of the two-phase HSM can be represented in the state-space form [23],

$$\frac{d}{dt}\theta = \omega$$

$$\frac{d}{dt}\omega = \frac{1}{J}(-K_m i_a \sin(N_r \theta) + K_m i_b \cos(N_r \theta) - B\omega - \tau_L)$$

$$\frac{d}{dt}i_a = \frac{1}{L}(v_a - Ri_a + K_m \omega \sin(N_r \theta))$$

$$\frac{d}{dt}i_b = \frac{1}{L}(v_b - Ri_b - K_m \omega \cos(N_r \theta))$$
(1)

where v_a , v_b and i_a , i_b are the voltages and currents in stator windings A and B, respectively, *L* is the induction of the winding, *R* is the resistance of the winding, ω is the rotor mechanical (angular) speed, θ is the rotor mechanical (angular) position, *J* is the inertia of the rotor, *B* is viscous friction coefficient, τ_L is the load torque, K_m is the motor torque constant, and N_r is the number of rotor teeth.

The config of the power circuit is shown in Figure 1, a common end formed by connecting the two stator windings A– and B– in series, and the other two open ends of the stator windings A+ and B+ connect to the three legs of VSI, respectively. The upper switches of the three-phase VSI are controlled by S_1 , S_2 , and S_3 , correspondingly, \overline{S}_1 , \overline{S}_2 , and \overline{S}_3 regulate lower switches, S_x and \overline{S}_x are complementary signals, and x = 1, 2, 3.



Figure 1. Three-phase VSI fed two-phase HSM.

When $S_x = 1$, the controlled switch is closed; otherwise, the controlled switch is opened. The three-phase two-level VSI has eight switching state combinations, and each combination generates a VV, i.e.,

$$V_{i} = V_{s}(S_{1} + S_{2}e^{j\frac{\pi}{2}} + S_{3}\sqrt{2}e^{j\frac{3\pi}{4}})$$
⁽²⁾

where V_j represents a VV, j = 0, ..., 7, $S_x(x = 1, 2, 3)$ denotes a switching state, and V_s is the supply voltage. The eight VVs consisting of six active VVs and the other two null VVs. The projection of VVs in the stationary α - β frame forms an asymmetrical hexagon divided equally into six sectors. The active $V_1, V_2, ...$, and V_6 are located sequentially at sectors I, II, ..., and VI, respectively, and the two null V_0 and V_7 are located at the origin of the hexagon, as shown in Figure 2.



Figure 2. Six sectors and eight VVs in the stationary α - β frame.

3. The Conventional FCS-MPCC

The FCS-MPCC takes advantage of the feature that VSI has a finite number of switching states to simplify the MPC optimization problem. It consists of predicting currents by enumerating all VVs in the voltage control set, evaluating the prediction currents by a cost function, and directly applying the optimal switching state s_0 with the minimum function value to the VSI. Figure 3 illustrates a block diagram of the conventional FCS-MPCC.



Figure 3. Block diagram of the conventional FCS-MPCC.

3.1. Predictive Model of Current

According to Equation (1), electrical equations of the HSM in α - β frame can be written in the form of complex vectors,

$$\frac{\mathrm{d}}{\mathrm{d}t}\boldsymbol{i}_{s} = \frac{1}{L}(\boldsymbol{v}_{s} - R\boldsymbol{i}_{s} - \mathrm{j}K_{m}\omega e^{iN_{r}\theta}) \tag{3}$$

where $i_s = i_a + ji_b$ and $v_s = v_a + jv_b$ are the winding current and voltage vectors, respectively. The values of v_a and v_b are equal to the projection of V_j on the α and β axis. Because predictive currents of the two null VVs are the same, the control set includes the six active VVs and one null V_0 . For small sampling times, Equation (3) can be discretized with first-order Euler's method,

$$\mathbf{i}_{s}(k+1) = \mathbf{i}_{s}(k) + \frac{T_{s}}{L}(\mathbf{v}_{s}(k) - R\mathbf{i}_{s}(k) + \mathbf{j}K_{m}\omega(k)e^{\mathbf{j}N_{r}\theta(k)})$$
(4)

where T_s is the sampling period, and k represents the kth sampling.

3.2. Cost Function

The cost function is used to evaluate the predicted current, and its form is very flexible. In current control applications, minimizing the current tracking error is the primary goal. Therefore, the cost function includes a current tracking error term, and it has the largest weighting coefficient. Usually, a current limit item that restricts the maximum current flowing through the stator windings is used to improve the safety and stability of the motor. In some applications, it is also critical to limit the switching times of the electronic switch, which is achieved by adding a switching limit item. The general form of a cost function is,

$$g = |i_a - i_a^*| + |i_b - i_b^*| + \lambda_1 g_{L1} + \lambda_2 n_c$$
(5)

where i_a^* and i_b^* are the reference currents of two-phase windings, respectively, λ_1 and λ_2 are weighting coefficients of the two limiting items, respectively, g_{L1} is used to limit the maximum current passing through windings, and n_c is used to limit the number of switching times.

FOC is used to modulate reference current; therefore, i_a^* and i_b^* are obtained by applying inverse Park transformation to i_d and i_q ,

$$\begin{bmatrix} i_a^*\\ i_b^* \end{bmatrix} = \begin{bmatrix} \cos(N_r\theta) & -\sin(N_r\theta)\\ \sin(N_r\theta) & \cos(N_r\theta) \end{bmatrix} \begin{bmatrix} i_d\\ i_q \end{bmatrix}$$
(6)

where i_q is provided by the speed loop, and $i_d = 0$ for simplicity.

The maximum current limit item is defined as

$$g_{L1} = \begin{cases} 0 & \text{if } \sqrt{i_a^2 + i_b^2} \le I_{\max} \\ \text{inf } & \text{if } \sqrt{i_a^2 + i_b^2} > I_{\max} \end{cases}$$
(7)

where I_{max} is the maximum current allowed by the stator winding. The current used in this application is small, and the switching state is optimized in the selection of the optimal VV, ignoring the switching limit term, which is $\lambda_2 = 0$.

In each control cycle, the control algorithm needs to perform current prediction and evaluation operations to all VVs in the control set and select an optimal v_0 based on the minimum value principle,

$$\boldsymbol{v}_0 = \operatorname{argmin}_{\{j=0,\dots,6\}} g(\boldsymbol{V}_j) \tag{8}$$

3.3. Compensation for Computation Delay

When implementing a control algorithm on the digital signal processor (DSP)-based platform, the computational time delay problem should not be ignored. It has an adverse

influence and will deteriorate the performance of the FCS-MPCC, especially in the case of long predictive horizon control [24]. The time delay problem can be overcome by using a two-step-ahead prediction method [21], i.e., (1) put $i_s(k)$ and $v_o(k)$ into Equation (4) to estimate current at (k + 1)th; (2) predict current of all VVs at (k + 2)th by replacing $i_s(k + 1)$, which would be measured at (k + 1)th with the estimated current $\hat{i}_s(k + 1)$ calculated at (k)th. The new prediction equation is obtained by using Equation (4) shifted one step forward in time, i.e.,

$$\mathbf{i}_{s}(k+2) = \hat{\mathbf{i}}_{s}(k+1) + \frac{T_{s}}{L}(\mathbf{v}_{s}(k+1) - R\hat{\mathbf{i}}_{s}(k+1) - \mathbf{j}K_{m}\omega(k+1)e^{\mathbf{j}N_{r}\theta(k+1)})$$
(9)

The cost function needs to be redefined, i.e., the optimal VV forces i_s to trace i_s^* at the (k + 2)th instant [25].

At last, the optimal switching state s_o is applied to three-phase VSI directly, avoiding using a modulator. Though a simple structure, the electronic switching frequency is not constant, which leads to spread harmonic spectra of the output signal [26]. Furthermore, current ripple and THD is high due to only one VV is applied to the VSI in a control period.

4. The Improved FCS-MPCC

To improve the current quality of the conventional FCS-MPCC, an extended control set having 37 VVs is constructed to replace the original control set having only six active and one null VVs. The new VV, also called virtual or synthesized VV, is implemented using DSVM.

4.1. Virtual VV Syntheses by DSVM

According to the working principle of DSVM, firstly, a whole control period T_s is divided into several parts. Then, a virtual VV is synthesized by applying original VVs to the VSI sequentially in a whole control period. Assuming T_s is subdivided into N parts equally, the virtual VV can be expressed as follows:

$$\boldsymbol{v}_{vir} = \sum_{j=1,2,\dots,N} t_N \boldsymbol{v}_j \tag{10}$$

where

$$t_N = \frac{T_s}{N}$$

$$v_j \in \{V_0, V_1, \dots, V_6\}$$
(11)

The total number N_c , including real and virtual VVs, is

$$N_c = 6\sum_{k=1}^{N} k + 1$$
 (12)

In this design, N = 3, and therefore, $N_c = 37$. All VVs are shown in Figure 4 and are distinguished by a colored dot: black dots represent the original seven VVs; red, yellow, and blue dots indicate virtual VVs, which are synthesized by two active VVs, two active VVs and V_0 , and one active VV and V_0 , respectively. The superscript and subscript of the VV detail the synthesis information. For example, V_{1N^2} is synthesized by V_1 and V_0 , and action times are $T_s/3$ and $2T_s/3$, respectively; correspondingly, V_{N12} is synthesized by V_0 , V_1 , and V_2 , and action times are all $T_s/3$.



Figure 4. Real and virtual VVs in α - β frame.

The increased VVs contribute to improving the current quality. However, it is difficult to evaluate all VVs in one control period under the current computation power of the DSP. A natural idea is to exclude some unnecessary VVs to decrease the computation time. Usually, a direct or indirect reference VV is pre-computed, and then these VVs located near the reference VV are selected into a new control set. The optimization algorithm evaluates VVs in the new control set only [27]. Deadbeat predictive control is one of the most effective methods to compute the direct reference VV and is detailed in the following section.

4.2. Computational Cost Reduction by Deadbeat Control

Deadbeat control is a branch of predictive control, and it is employed broadly in motor drives. It is classified into deadbeat predictive torque and flux control and deadbeat predictive current control (DPCC). The principle of DPCC is to calculate a manipulated input to force the current to reach its reference value after one sampling period [28]. Assuming $i_s(k + 1) = i_s^*(k + 1)$, according to the discretized Equation (4), the manipulated input $v_s(k)$ is,

$$\boldsymbol{v}_{s}(k) = \frac{L}{T_{s}}(\boldsymbol{i}_{s}^{*}(k+1) - \boldsymbol{i}_{s}(k)) + R\boldsymbol{i}_{s}(k) + jK_{m}\omega(k)e^{jN_{r}\theta(k)}$$
(13)

The computational time delay also exists in the implementation of DPCC. Usually, a one-step-ahead prediction method is used, i.e., a manipulated VV $v_s(k + 1)$, obtained at the *k*th instant, is modulated at the (k + 1)th instant. The manipulated VV is obtained using Equation (13) shifted one step forward in time,

$$\boldsymbol{v}_{s}(k+1) = \frac{L}{T_{s}}(\boldsymbol{i}_{s}^{*}(k+2) - \hat{\boldsymbol{i}}_{s}(k+1)) + R\hat{\boldsymbol{i}}_{s}(k+1) + jK_{m}\omega(k+1)e^{jN_{r}\theta(k+1)}$$
(14)

Only the three VVs adjacent to $v_s(k + 1)$ are manipulated by the control algorithm. As shown in Figure 4, assuming v_s^* is a reference VV obtained by DPCC, V_{1^2N} , V_{1^22} , and V_{12N} , adjacent to v_s^* , need to be manipulated. In a word, the amount of VVs of the conventional and improved FCS-MPCC are 7 and 37, respectively. However, the number of VVs that need to be predicted and evaluated for the two methods are seven and three, respectively. Therefore, the computation time for the optimization algorithm of the improved FCS-MPCC does not increase with the increased VVs.

4.3. Switching State Generation

The improved FCS-MPCC utilizes DSVM to modulate optimal VV, which is a distinct difference from conventional FCS-MPCC. As the general SVPWM cannot be applied directly

in an unbalanced circuit structure, we build new waveforms for each VV. As waveform patterns of the six sectors have strong similarities, we detail the waveforms of all the VVs in sector I. These virtual VVs, V_{N^21} , V_{N1^2} , V_{N12} , V_{1^22} , and V_{12^2} , are synthesized by V_0 , V_1 , and V_2 , and discrete Equation (10) is further simplified,

$$v_{vir} = T_0 V_0 + T_1 V_1 + T_2 V_2 \tag{15}$$

where the action times T_0 , T_1 , and T_2 are taken from 0, $T_s/3$, $2T_s/3$, and T_s . Table 1 details the action time for each virtual VV and original VVs V_0 , V_1 , and V_2 .

VVs	T_0	T_1	T_2
V_0	T_s	0	0
V_{N^21}	$2T_{s}/3$	$T_s/3$	0
$V_{\rm N1^2}$	$T_s/3$	$2T_{s}/3$	0
V_1	0	T_s	0
<i>V</i> _{N12}	$T_s/3$	$T_s/3$	$T_s/3$
$V_{1^{2}2}$	0	$2T_{s}/3$	$T_s/3$
V_{12^2}	0	$T_s/3$	$2T_{s}/3$
V_2	0	0	T_s

Table 1. Detailed action times for all VVs in sector I.

Proper action orders also play an important role, as different action orders result in different waveform patterns. In this design, the action orders can approximately output constant switching frequency, which improves the harmonic spectra of the current waveforms. Figure 5 illustrates a generalized waveform pattern in a PWM period of sector I, including action order, duty cycle, and switching scheme. It has the following properties:

- (1) The noninverted control signals for S_1 , S_2 , and S_3 are symmetrical to the middle of the PWM period. This is because the symmetrical waveforms are easy to be realized in DSP; furthermore, additional harmonics exist around the switching frequency when the asymmetry waveforms are used.
- (2) The switching orders are S_{000} - S_{100} - S_{110} - S_{110} - S_{100} - S_{000} , where numeral subscripts represent the switching states of the noninverted control signal S_1 , S_2 , and S_3 . Correspondingly, VSI generates V_0 - V_1 - V_2 - V_7 - V_2 - V_1 - V_0 sequentially, which is identical to the general SVPWM.
- (3) Each control period starts and ends with S_{000} , and S_{111} is inserted into the control period. Therefore, each noninverted control signal switches twice per PWM period except when the duty cycle is 0% or 100%, which can assure the control signal switching at constant frequency except for the cases in which the duty cycle is 0% or 100%.



Figure 5. Generalized waveform pattern for VVs in sector I.

The specific waveform of each VV in the sector I is obtained by instantiating the generalized waveform pattern using corresponding action time. Virtual $V_{N^{2}1}$ and $V_{N1^{2}}$ are synthesized by V_{0} and V_{1} , where the only difference between them lies in the action time for T_{0} and T_{1} . Therefore, they share the most similar waveform patterns, as shown in Figure 6b,c, respectively. V_{0} , V_{1} , and V_{2} using the same action time, $T_{s}/3$, synthesize V_{N12} , as shown in Figure 6e. Virtual $V_{1^{2}2}$ and $V_{12^{2}}$ are synthesized by V_{1} and V_{2} only, so T_{0} is zero, and their waveforms are shown in Figure 6f,g, respectively. The real V_{0} , V_{1} , and V_{2} are also generated by DSVM, and waveforms are illustrated in Figure 6a,d,h, respectively.



Figure 6. Instantiated waveform patterns for each VV in sector I. (a) V_0 ; (b) V_{N^21} ; (c) V_{N1^2} ; (d) V_1 ; (e) V_{N12} ; (f) V_{1^22} ; (g) V_{12^2} ; (h) V_2 .

Generalized waveform patterns for other sectors are obtained employing similar method used in sector I. The six generalized waveforms located at the corresponding sectors of the asymmetrical hexagon are illustrated in Figure 7. As can be seen, they share some features, e.g., fixed switching orders among control signals for each sector, etc.

4.4. Overall Improved FCS-MPCC Scheme

The block diagram of the proposed improved FCS-MPCC is shown in Figure 8, and the whole control procedures are summarized as follows:

- (1) Measure current $i_s(k)$, rotor mechanical position $\theta(k)$, and speed $\omega(k)$ at the *k*th instant.
- (2) Modulate the optimal VV $v_o(k)$ using DSVM, where $v_o(k)$ was obtained at the (k-1)th sampling period.
- (3) Estimate current $\hat{i}_s(k+1)$, taking advantage of $\hat{i}_s(k)$, $v_o(k)$, and $\theta(k)$, etc.
- (4) Predict a reference VV $v_s^*(k+1)$ using DPCC, and then build a new control set V_n , consisting of only three VVs adjacent to $v_s^*(k+1)$.
- (5) Predict current $i_s(k+2)$ for each VV in V_n , and then evaluate them using a cost function; finally, determine the optimal VV $v_o(k+1)$ based on the minimum value principle.



Figure 7. Generalized waveform patterns in all six sectors.



Figure 8. Block diagram of the improved FCS-MPCC.

5. Simulation and Experimental Verification

In this design, a speed regulator is built using MATLAB/Simulink to evaluate the performance of HSM using different current regulation strategies. It adopts cascade control, where the outer speed loop uses PI, and the inner current loop utilizes (1) conventional FCS-MPCC, (2) improved FCS-MPCC, and (3) traditional dual H-bridges PI, respectively. The conventional FCS-MPCC has been implemented as a benchmark method. Key parameters of the two-phase HSM are listed in Table 2, the sampling frequency is 40 kHz, gains of speed PI controllers are $K_p = 1.31 \times 10^{-3}$ and $K_i = 1.03$, and gains of current PI controller are $K_p = 28$ and $K_i = 1.4 \times 10^4$. These PI gains are obtained using the Ziegler–Nichols tuning method and validated using MathWorks script command.

Table 2. Key specifications of the two-phase HSM.

Parameter	Value	
Phase resistance (<i>R</i>)	0.42 Ω	
Phase inductance (L)	1.38 mH	
Torque constant (K_m)	0.25 Nm/A	
Rotor inertia (J)	$280 imes 10^{-7} m Nm \ s^2/rad$	
Viscous friction coefficient (B)	$5 imes 10^{-3}~{ m Nm\cdot s/rad}$	
Number of teeth on the rotor (N_r)	50	
Supply voltage (V_s)	36 V	

5.1. Simulation Verification

In the following simulations and experiments, the rotor speed obtained using the conventional FCS-MPCC, the improved FCS-MPCC, and the linear PI are represented by

 ω_c , ω_i , and ω_{pi} , respectively. Correspondingly, τ_c , τ_i , and τ_{pi} represent the torque obtained by these methods, respectively. The first simulation evaluates the transient and steady-state performance of the system without load torque disturbance. The reference speed ω^* steps to 240, 720, and 240 rpm at 0.0, 0.1, and 0.2 s, respectively, from standstill. For all methods, real speed ω tracks ω^* well, setting time is about 0.03 s, and there is no steady-state error, as shown in Figure 9a. The torque response is shown in Figure 9b, where the reference torque τ^* is modulated by the speed PI controller, and the significant torque ripple is observed in the conventional FCS-MPCC.



Figure 9. Speed, torque, and current responses of the HSM without load torque disturbance. (a) Speed response; (b) torque response; (c) current response of conventional FCS-MPCC in α - β frame; (d) current response of improved FCS-MPCC in α - β frame; (e) current response of dual H-bridges PI in α - β frame.

Current responses are illustrated in Figure 9c–e. For all methods, waveforms vary in a sinusoidal fashion, where the amplitude is proportional to rotor mechanical speed, and the frequency equals the product of the rotor angular frequency and the number of teeth on

the rotor. However, there are distinct differences in the aspect of current quality, such as the current ripple.

The current ripple of conventional FCS-MPCC is the largest, as shown in Figure 9c. The reason for this is that the amount of effective VVs is not enough to accurately control current, and a larger current ripple inevitably occurs. As for the improved FCS-MPCC, the number of effective VVs is significantly increased, the current control is more accurate, and it has better current quality, as shown in Figure 9d.

A larger current ripple also exists in the traditional PI, as shown in Figure 9e. The PI controller regulates the current by controlling the duty cycle of the two-level PWM. The H-bridge will not work in freewheeling mode, and the current ripple is $V_sT_s/(2L)$, while it is only $V_sT_s/(8L)$ when the three-level PWM is used. A mixed-mode PWM for high performance HSM applications is proposed in [29], which makes a hybrid of the two-level PWM strategies to reduce current ripple, but it is difficult to implement the algorithm.

The second simulation evaluates the transient and steady-state performance of the system under load torque disturbance when HSM runs at high speed. The reference speed ω^* steps to 750 rpm at instant 0 s without load torque disturbance, and then a 0.2 Nm disturbance is applied to the system at 0.1–0.2 s.

All methods present excellent speed tracking abilities, as shown in Figure 10a. Firstly, the rotor accelerates to 750 rpm and runs steadily until a disturbance is being exerted to the system. Then, the speed descends instantly to about 563.2, 568.8, and 591.1 rpm, respectively, is restored to 750 rpm in about 0.03 s, and then runs steadily until the disturbance is being released. When the disturbance is removed, the speed ascends instantly to about 919, 912.1, and 910.5 rpm, respectively, is restored to 750 rpm in about 0.750 rpm in about 0.03 s, and then runs steadily until the number of the speed ascends instantly to about 919, 912.1, and 910.5 rpm, respectively, is restored to 750 rpm in about 0.03 s, and then runs steadily.

The torque response is shown in Figure 10b. As can be seen, τ^* increases to 0.59 Nm from 0.39 Nm when the load torque is being applied, and torque ripple exists for all three methods, where the conventional FCS-MPCC has the largest torque ripple. As is known, the instantaneous torque τ is composed of electromagnetic torque and reluctance torque [29]. The instantaneous torque is further simplified to $\tau = K_m i_q$ by keeping $i_d = 0$ and ignoring detent torque τ_d . As a result, the torque and speed responses are determined by the quality of the current, i.e., the improved current quality contributes to reducing torque and speed ripple.

The current responses in d-q frame are illustrated in Figure 10c–e. For all three methods, i_d pulsates at 0 A, where the conventional FCS-MPCC has the largest pulsation amplitude, about 0.5 A, and the other two have approximately identical pulsation amplitude, i.e., 0.3 A. The quadrature current component i_q varies according to τ^* , and its average value increases to 2.32 A from 1.59 A when the load torque is being applied. Ripple also exists in i_q , where conventional FCS-MPCC has the largest pulsation amplitude, and the other two have approximately identical pulsation amplitude, and the other two have approximately identical pulsation amplitude, and

The superiority of the proposed method is not only reflected in its high current tracking capability and low current ripple, but also in smaller THD. To further compare the performance, the current THD of all methods obtained at various speeds without load torque disturbance is shown in Figure 11. By investigating simulation data, three important conclusions have been obtained: (1) the THD of the improved FCS-MPCC is smaller than the THD of the conventional FCS-MPCC in the whole speed ranges, and huge differences exist at low-speed ranges; (2) compared with the traditional dual H-bridges PI method, the THD of the improved FCS-MPCC is slightly higher, which is mainly caused by the unbalanced circuit structure; (3) the THD of the improved FCS-MPCC fluctuates at 10.0 when the rotor speed is higher than 420 rpm, so its THD is approximately independent of the speed.



Figure 10. Speed, torque, and current response of the HSM with load torque disturbance. (a) Speed response; (b) torque response; (c) current response of conventional FCS-MPCC in d-q frame; (d) current response of improved FCS-MPCC in d-q frame; (e) current response of dual H-ridges PI in d-q frame.

5.2. Experimental Verification

Experiments were carried out on a front-leg mechanism of a stairs-climbing wheelchair robot. The experimental setup is composed of HSM, motor drive, main controller, a front-leg of wheelchair robot with a multi-stage gear reducer, etc., as shown in Figure 12. The main controller plans the movement of the front-leg mechanism, and HSM is the actuator to lift the front leg. The parameters of HSM and motor drive are identical to the MATLAB simulation, and TMS320F28035 is used in both motor drive and main controller.



Figure 11. THD comparison at various speeds for all methods.



Figure 12. Experimental setup.

The experiment verifies the performance of using a multi-step speed strategy to drive the front-leg to complete lifting motion. The reference speed ω^* steps to 600 and 300 rpm at 0 and 0.15 s, and conventional FCS-MPCC, improved FCS-MPCC, and dual H-bridges PI are used to regulate current, respectively. The speed response is shown in Figure 13a, where the setting time is about 0.03 s, and there is no steady-state error, which is consistent with simulations.

As mentioned above, the quadrature component i_q illustrates the torque response effectively. In the d-q frame, current responses obtained by the three control strategies above are shown in Figure 13b–d, respectively. The direct axis component i_d pulsates at 0 A for all the methods, where conventional FCS-MPCC has the largest ripple, about 0.6 A; correspondingly, the ripple of the other two methods is almost the same, about 0.2 A. The quadrature component i_q varies according to τ^* , and its value descends to 1.4 A from 2.0 A when ω^* steps to 300 rpm from 600 rpm. The ripple also exists in i_q , and conventional FCS-MPCC has the largest ripple, about 0.6 A; correspondingly, the ripple of the other two methods is about 0.2 A. Although the maximum ripple of the two strategies is almost the same, the curve of the improved FCS-MPCC is much smoother; therefore, it is more robust to load disturbance.





Figure 13. Speed and current responses of HSM under load. (**a**) Speed response; (**b**) current response of conventional FCS-MPCC in d-q frame; (**c**) current response of improved FCS-MPCC in d-q frame; (**d**) current response of the dual H-bridges PI in d-q frame; (**e**) current response of conventional FCS-MPCC in α - β frame; (**f**) Current response of improved FCS-MPCC in α - β frame; (**g**) current response of the dual H-bridges PI in α - β frame.

In the α - β frame, current responses obtained by the three control strategies above are shown in Figure 13e–g, respectively. For all methods, the current varies in sinusoidal

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fashion, where the amplitude and frequency are 2.1 A and 1.5 A and 500 Hz and 250 Hz, respectively, when ω^* are 600 and 300 rpm. Furthermore, the minimum ripple can be observed in the improved FCS-MPCC, which contributes to improving current quality and decreasing THD.

6. Conclusions

In this paper, an improved FCS-MPCC is proposed for a two-phase HSM fed by a three-phase VSI. It is a hybrid control strategy that integrates the advantages of both FCS-MPCC and CCS-MPCC. On the one hand, the optimal VV is selected based on FCS-MPCC, inheriting the advantages of intuitive concept and simple implementation. On the other hand, the optimal VV is modulated using DSVM, being similar to CCS-MPCC, which has advantages of high current quality and low THD. The proposed method mainly consists of (1) building an extended control set that includes 37 VVs to replace the original control set with only seven VVs; (2) decreasing computational time for the optimization algorithm, using DPCC to obtain a reference current, where only the three VVs adjacent to the reference VV are evaluated in each control period; and (3) modulating the optimal VV using DSVM. In summary, the amount of VVs in the control set of the improved FCS-MPCC is 5.29 times of the conventional FCS-MPCC, but the amount of VVs required for current prediction and evaluation per control cycle is 0.43 times the conventional method; the only cost is that the microprocessor must provide a modulator module. Its superiorities have been confirmed through comparative simulations and experimental tests, the results of which show that: (1) compared with the conventional FCS-MPCC, it not only significantly improves the current quality but also reduce the computational workload, which enables it to be running in an economical processor; and (2) compared with the traditional dual H-bridges PI strategy, it has approximately identical performance indices, but it benefits in simple hardware structure, low cost, fast dynamic response, and strong robustness to load disturbances, etc.

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