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Highly Integrated Online Multi-Channel Electrochemical Impedance Spectroscopy Measurement Device for Fuel Cell Stack

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Abstract: Electrochemical impedance spectroscopy (EIS) can provide information about the internal state of fuel cells, which makes EIS an important tool for fuel cell fault diagnosis. However, high cost, large volume, and poor scalability are limitations of existing EIS measurement equipment. In this study, a multi-channel online fuel cell EIS measurement device was designed. In this device, based on multi-phase interleaved Boost topology and average current control, an excitation source, which can output 1~500 Hz, 10 A sinusoidal excitation current was designed and verified by model simulation. Then, based on the quadrature vector digital lock-in amplifier (DLIA) algorithm, an impedance measuring module that can achieve precise online impedance measurement and calculation was designed. A prototype was then built for the experiment. According to the experiment test, the amplitude error of the excitation source is less than 1.8%, and the frequency error is less than 0.3%. Compared with the reference data, the impedance measured by the prototype has a modulus error of less than 3.5% and a phase angle error of less than 1.5°. Moreover, the waveform control and impedance extraction function of the EIS measurement device is implemented on an embedded controller, which can cut the price and reduce the volume.



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Keywords: proton exchange membrane fuel cell (PEMFC); electrochemical impedance spectroscopy (EIS); online impedance measurement; sinusoidal current excitation; digital signal processing

1. Introduction

The current energy consumption structure based on traditional fossil fuels has caused serious environmental problems. It has become global consensus to develop clean energy and achieve carbon neutrality [1,2]. Among many clean energy sources, hydrogen as a good energy carrier, has the advantages of zero pollution, high efficiency, and a wide range of uses [3]. The fuel cell is a power generation device that directly converts the chemical energy in hydrogen into electrical energy through an electrochemical reaction. It is one of the best ways to utilize hydrogen energy at this stage, with the advantages of cleanliness and high efficiency [4,5]. Among various fuel cells, the proton exchange membrane fuel cell (PEMFC) is currently the most concerning type and is considered an ideal power source for vehicles [6,7].

However, poor reliability and durability are the main factors that make it difficult for PEMFC to be widely used [8]. Therefore, efficient monitoring and fault diagnosis technology is of great significance to the improvement of life and performance of fuel cells [9]. In recent years, fuel cell fault diagnosis methods based on online impedance measurement have begun to receive attention and research [10,11]. EIS data contain abundant internal information of fuel cell, which is helpful to analyze the state of fuel cells, adjusting working parameters and implementing control strategies [12–15]. Gallo et al. combined an EIS-based diagnostic algorithm and an online natural aging estimation algorithm to refine detection features extraction and predict the remaining useful life of a

fuel cell stack during operation [16]. Janicka et al. used dynamic EIS to fit the equivalent circuit parameters of a fuel cell under different current loads and air humidity and then determined the optimal working conditions [17]. Pivac et al. used EIS and an equivalent circuit model to analyze the degradation effects on the functioning of the catalyst layer, and proposed to use the low-frequency intercept as an indicator of fuel cell degradation [18]. The principle of EIS is to obtain a complete impedance spectrum by applying voltage or current disturbances of different frequencies to the electrochemical system and measuring the response and the amplitude-frequency characteristics produced by the system.

When EIS measurement is performed, a weak excitation current or voltage perturbation must be applied to the fuel cell, so the excitation source must be designed [19]. Commonly used excitation source topologies include those based on DC/DC converters and those based on linear power supplies. Among them, the topology based on the linear power supply has the advantages of simple structure and easy control. However, the disadvantage of this topology is low efficiency and large heat generation, and it is rarely used in engineering [20–22]. On the other hand, DC/DC converter-based topologies are difficult to control and have inherent current ripple. Nonetheless, the advantages of high efficiency and flexible topologies make DC/DC converter-based schemes widely used [23–28].

At present, there are three commonly used fuel cell impedance measurement schemes: The first is to directly measure the amplitude and phase characteristics of the response signal to the excitation and calculate the impedance. The main drawback of this method is that there are many analog circuits, weak anti-interference ability, and poor reliability [29]. The second is based on various digital signal processing algorithms (including FFT, wavelet transform, etc.). This scheme has been widely used, but the execution of the signal processing algorithm has high requirements on the performance of the processor, so in most cases the upper computer is required to complete the data processing work [30–34]. The third option is to use an integrated chip with integrated EIS measurement capabilities. However, such chips are oriented toward low-power devices or biomedical fields and are not suitable for fuel cell systems [35].

In commercialized fuel cell systems, there are still few examples of applying impedance-based fault diagnosis techniques. The main reason is that most of the existing impedance measuring devices and excitation sources are scientific and experimental equipment designed for single, small-scale fuel cells [36]. Such devices are not only bulky, expensive, and poorly scalable, but also cannot meet the voltage and current level requirements of the fuel cell system. In addition, those impedance measurement systems that have been put into practical applications have the following problems: they can only measure impedance at a few fixed frequencies with insufficient accuracy, and the impedance measurement channel is limited [10]. Therefore, the development of an embedded online EIS measurement device with low cost, small size, high precision, and strong scalability is beneficial to the application of online impedance diagnosis of the fuel cell, improving the overall performance of the fuel cell and reducing the full-cycle cost of the system.

In this study, a multi-channel online EIS measurement device based on the embedded controller was designed. This device includes a fuel cell excitation source and an impedance measuring module. The excitation source is based on a multi-phase interleaved parallel Boost topology, which has the advantages of low current ripple, large excitation amplitude, and high conversion efficiency. Based on the orthogonal vector DLIA algorithm, the impedance measuring module realizes the function of online real-time impedance measurement and calculation on a low-power embedded platform. The device uses the galvanostatic method to measure EIS at frequencies from 1 to 500 Hz. Both excitation control and impedance calculation process are performed in the embedded controller, which has the advantages of small size and low cost. In addition, the impedance measuring module has eight impedance measurement channels and has good scalability, which can meet the needs of multi-channel measurement of high-power fuel cells. In Section 2, the topology design, parameter calculation, and simulation results of the fuel cell excitation source are introduced. In Section 3, the signal processing structure and impedance information

extraction algorithm of the online impedance measuring module is introduced. Finally, the performance of the multi-channel online EIS measurement device is verified and evaluated on a fuel cell test bench.

2. Design of Excitation Source

2.1. Design Metrics for Excitation Source

The excitation source uses a DC/DC converter structure, and the overall structure is shown in Figure 1. The input of the converter is connected to the fuel cell, and the output is connected to the DC bus or the energy storage battery pack. The controller continuously adjusts the switching duty ratio of the power components, so that the output current tracks the ideal sine wave signal and realizes the excitation to the fuel cell.

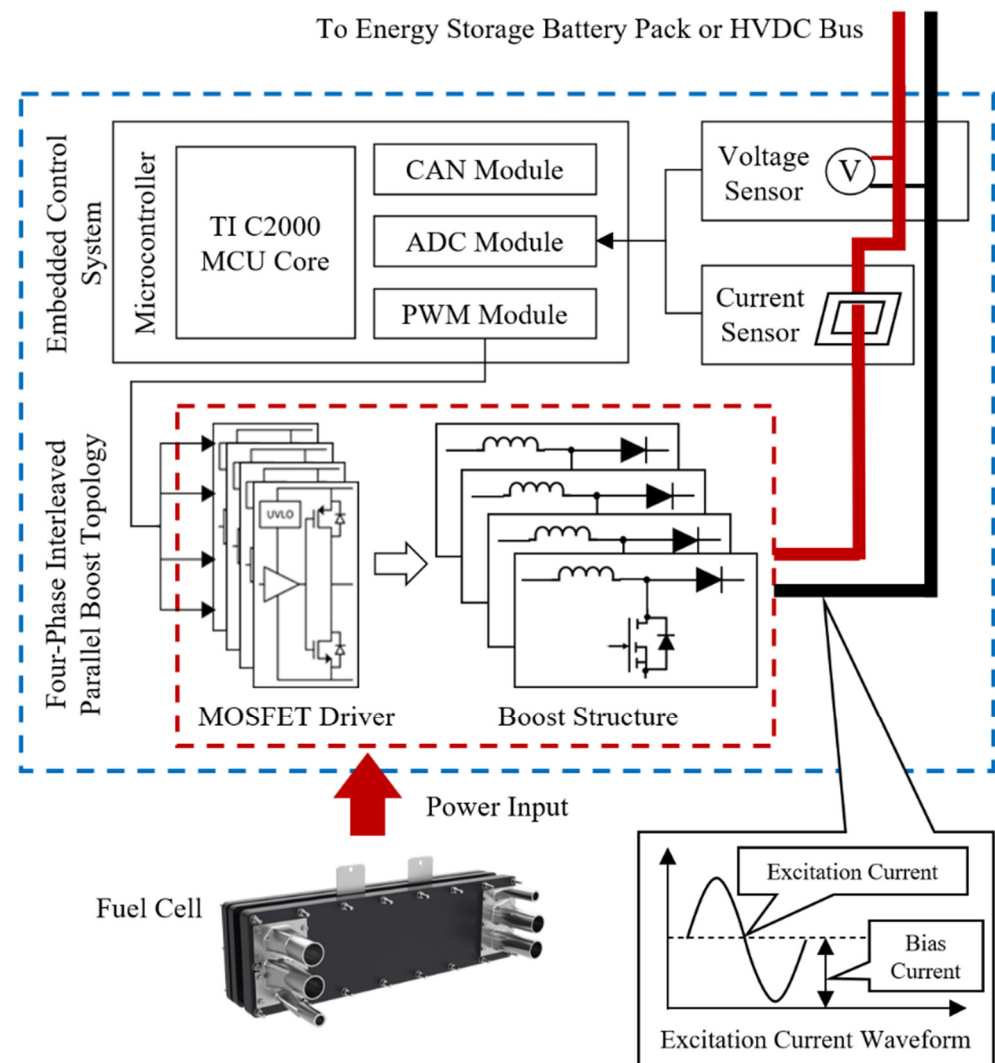


Figure 1. Hardware structure diagram of the excitation source.

When designing the excitation source, it is necessary to determine the rating power and voltage level of the fuel cell to be adapted for further parameter calculation. The fuel cell tested in this study has six cells, the active area is 227 cm^2 , the maximum current density is 1.6 A/cm^2 (the cell voltage is 0.58 V at this time). The polarization curve of the fuel cell is shown in Figure 2.

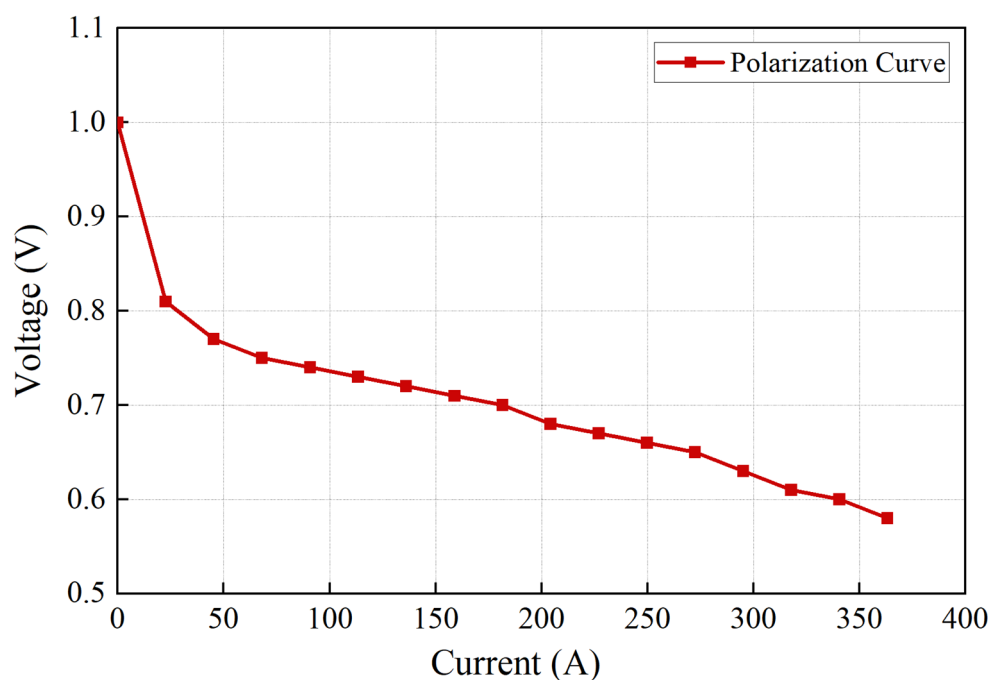


Figure 2. Polarization curve of the adapted fuel cell.

To ensure that the fuel cell response is approximately linear during the impedance measurement, the amplitude and current ripple of the excitation waveform should be as small as possible under the premise of ensuring a sufficient signal-to-noise ratio. It is generally considered that the excitation amplitude should not exceed 5% of the DC of the fuel cell [37]. In addition, the excitation source is a unidirectional DC/DC converter, and the output current is not allowed to be reversed. Therefore, the forward offset current also must be included in the output waveform. Finally, the parameters of the excitation source are determined as shown in Table 1.

Table 1. Parameters of fuel cell excitation source.

Parameters	Value
Input voltage range	4.0~6.0 V
Output voltage range	10.5~13.5 V
Excitation current waveform	Single-frequency sine wave
Excitation frequency range	1~500 Hz
Excitation current amplitude	10.0 A
DC offset current	12.0 A
Input current ripple	<100 mA (1% of excitation amplitude)

2.2. Design of Multi-Phase Interleaved Parallel Boost Topology

In a fuel cell system, the commonly used DC/DC converter topology is the Boost topology and its variants, which have the disadvantage of large current ripple. Excessive current ripple will not only reduce the signal-to-noise ratio of the impedance measurement but also reduce the life of the fuel cell. To suppress current ripple and reduce the volume and weight of filter inductors and capacitors, multi-phase interleaved parallel Boost topology is often used in engineering [38,39]. The schematic diagram of multi-phase interleaved parallel Boost topology is shown in Figure 3. The parallel structure distributes the current on each branch, reducing the stress on the power components. Through the phase control of the driving PWM waveform of each branch, the interleaving and cancellation of the current ripple are realized which effectively reduces the total current ripple while increasing the equivalent switching frequency [40]. However, the components required for the multi-phase interleaved parallel topology also increase according to the number of branches,

which increases the complexity of the system, reduces the reliability, and increases the total loss. Considering factors such as efficiency, cost, and control difficulty, a four-phase interleaved parallel topology was used.

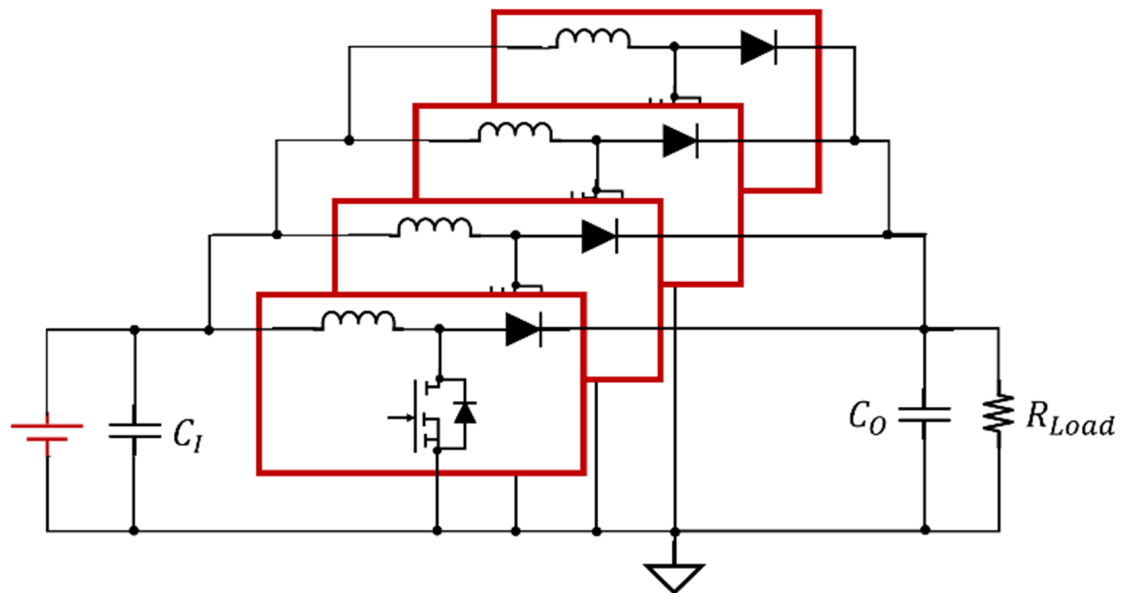


Figure 3. Multi-phase interleaved parallel Boost topology.

2.2.1. Determination of Switching Frequency

In the multi-phase interleaved parallel Boost topology there are several parameters to be determined including branch structure, switching frequency, and branch filter inductance value. The following parameters are selected or calculated below.

To improve efficiency, MOSFETs were used to replace high-side diodes. The improved circuit is shown in Figure 4. During operation, the gate voltage of the high-side MOSFET is synchronized with the phase of the rectified voltage, so it is called synchronous rectification. During the freewheeling phase, the high-side power component is turned on, and since the voltage drop across the MOSFET is much smaller than that of the power diode, conduction losses can be significantly reduced.

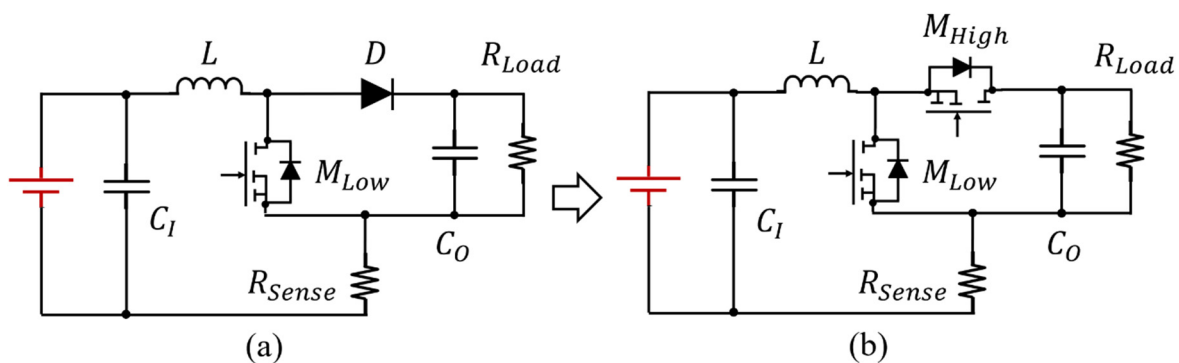


Figure 4. Schematic of diode rectification and synchronous rectification: (a) diode rectification; (b) synchronous rectification.

Then, the switching frequency of the power component needs to be determined. In the Boost topology the power components work in the hard switching mode, and the loss is mainly composed of conduction loss and switching loss. Low switching frequency reduces switching losses but requires larger filter inductors and capacitors to ensure stable output and low ripple. Increasing the switching frequency can improve the dynamic response capability of the system, but the requirements for the controller are increased.

The conduction loss is calculated by Equation (1), where I is current, $R_{DS(on)}$ is the on-state resistance of the MOSFET, and D is the duty of the drive signal.

$$P_{cond} = I^2 \times R_{DS(on)} \times D \tag{1}$$

Switching losses are estimated by Equations (2) and (3). Where V_{out} is the output voltage, and f is the switching frequency. $R_{dri(on)}$ is the on-state resistance of the gate driver, which is 1.25 Ω ; V_{dri} is the gate driving voltage, which is 18 V; $V_{gs(th)}$ is the gate threshold voltage, which is 4.0 V; Q_{gd} and Q_{gs} are the amounts of charge from the gate to the drain and source, which are 4 nC and 17 nC.

$$P_{SW} = \frac{I \times V_{out}}{2} \times f \times (t_{LH} + t_{HL}) \tag{2}$$

$$t_{LH} = t_{HL} = \left(Q_{gd} + \frac{Q_{gs}}{2} \right) \times \frac{R_{dri(on)}}{V_{dri} - V_{gs(th)}} \tag{3}$$

After considering factors such as control difficulty, power loss and the weight of the filter inductor, the switching frequency is set at 100 kHz. At this point, the total loss on each MOSFET is about 0.247 W, and no additional thermal design considerations are required.

2.2.2. Inductor Design

The excitation source is designed to operate in continuous current mode. For DC/DC converters, the filter inductor has an important impact on current ripple and current continuity. The value of the filter inductor is calculated as follows. First, the duty range of the PWM signal is calculated by Equation (4). The obtained duty range is 42.86~70.37%.

$$D = \frac{V_{out} - V_{in}}{V_{out}} \tag{4}$$

The suppression effect of the interleaved parallel Boost topology on the input current ripple is related to the duty and the number of interleaved phases [41]. When evaluating the ripple current, the inductor current ripple ratio $K(D)$ is introduced, and the definition of $K(D)$ is shown in Equation (5). Wherein Δi_i is the total current ripple after each phase current ripple is superimposed, Δi_L is the single-phase current ripple, and D is the duty of the PWM signal.

$$K(D) = \frac{\Delta i_i}{\Delta i_L} \tag{5}$$

For the four-phase interleaved parallel Boost topology, the expression of ripple ratio $K(D)$ is segmented and analyzed by the duty. As shown in Figure 5, it is a diagram showing the relationship between the switching states of the low side power component of each phase and the current waveform under different duty ranges in a four-phase interleaved parallel topology.

In one switching cycle, the current change in the inductor can be considered linear and satisfy Equation (6). Where V_L is the inductor voltage, L is the inductor value, and dI_L/dt is the inductor current change rate. Assuming that the switching period is short enough, dI_L can be replaced by the inductor ripple amplitude Δi_L , and dt can be replaced by the inductor current rise time t_{up} .

$$V_L = L \cdot \frac{dI_L}{dt} = L \cdot \frac{\Delta i_L}{t_{up}} \tag{6}$$

where the duty D is greater than 0 and less than 25%, as shown in Figure 5a. During the rising phase, one of the low side power components is turned on and the remaining three are turned off. For the branch in which the low side power component is turned on, the inductor voltage V_L is equal to the input voltage V_{in} , and the inductor current increases. For the other branch, the inductor voltage V_L is equal to $V_{in} - V_{out}$, and the inductor current

drops, where V_{out} is the output voltage. At this time, the total current ripple Δi_{Lo} is the sum of the ripple current of each branch inductor Δi_{Lx} , as shown in Equation (7).

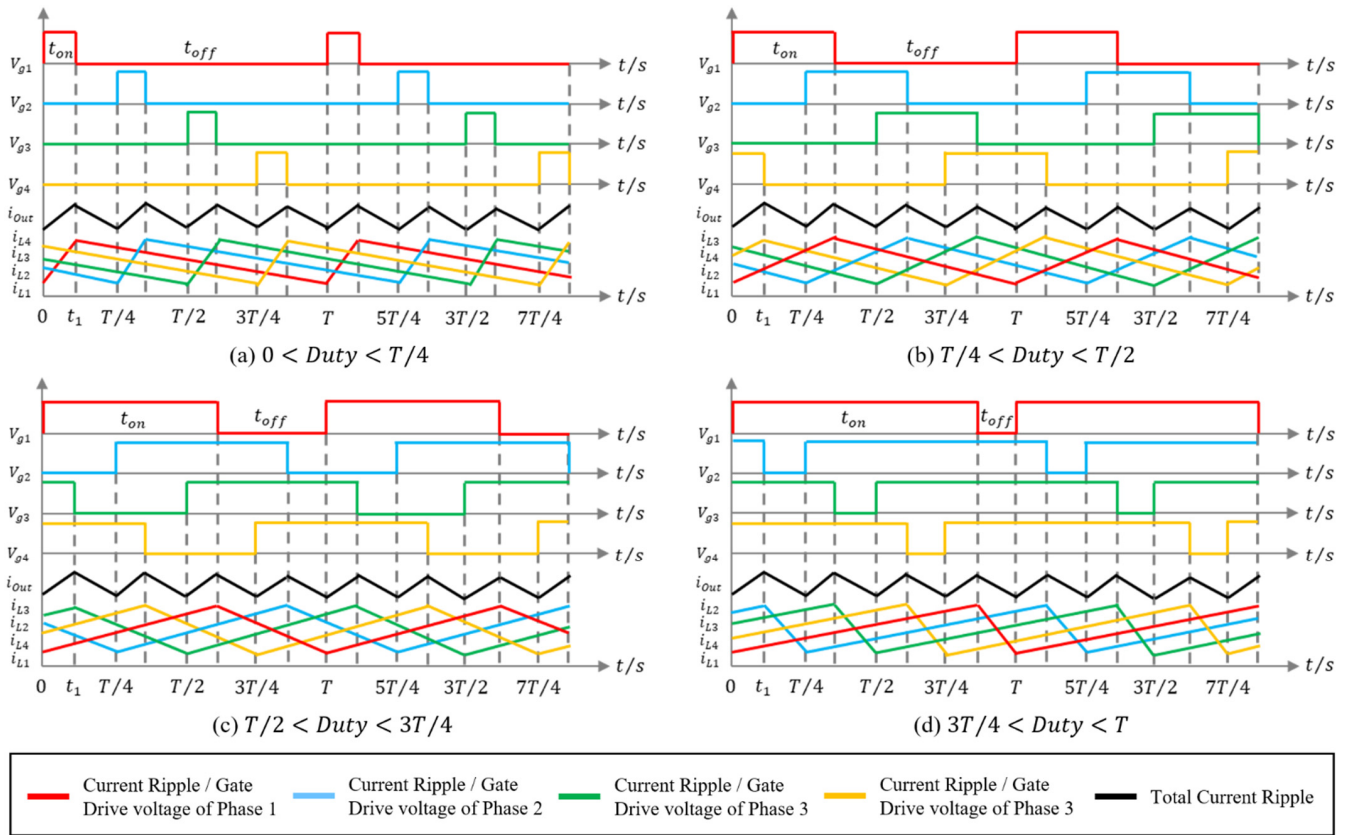


Figure 5. Gate drive voltage V_{gx} and current ripple i_{Lx} of each branch of the four-phase interleaved parallel boost topology: (a) $0 \leq D < 25\%$; (b) $25\% \leq D < 50\%$; (c) $50\% \leq D < 75\%$; (d) $75\% \leq D < 100\%$.

$$\Delta i_{Lo} = \sum_{x=0}^4 \Delta i_{Lx} = t_{up} \cdot \left(\frac{V_{in}}{L_1} + \frac{V_{in} - V_{out}}{L_2} + \frac{V_{in} - V_{out}}{L_3} + \frac{V_{in} - V_{out}}{L_4} \right) \quad (7)$$

where the current rise time t_{up} is equal to the total current ripple period T_0 multiplied by the equivalent duty D' . D is the switching duty of the power component, T is the switching period of the power component, and T_0 is the equivalent total switching period, as shown in Equations (8) and (9).

$$t_{up} = T_0 \cdot D' \quad (8)$$

$$D' = \frac{T_0 - (1 - D) \cdot T}{T_0} = \frac{\frac{T}{4} - (1 - D) \cdot T}{\frac{T}{4}} = 4D - 3 \quad (9)$$

Equation (9) is substituted into Equation (7). The multi-phase Boost topology is known to have the following properties: $L_1 = L_2 = L_3 = L_4 = L$ and $V_{in} = (1 - D) \cdot V_{out}$. Equation (10) is simplified and obtained, where f is the switching frequency of the power component.

$$\Delta i_{Lo} = D \cdot T \cdot \frac{V_{in}}{L} \cdot \frac{1 - 4D}{1 - D} = \frac{D \cdot V_{in}}{L \cdot f} \cdot \frac{1 - 4D}{1 - D} \quad (10)$$

For the single-phase Boost topology, the calculation of the inductor current ripple is shown in Equation (11).

$$\Delta I_L = \frac{D \cdot (1 - D) \cdot V_{out}}{L \cdot f} = \frac{D \cdot V_{in}}{L \cdot f} \quad (11)$$

Equation (10) is divided with Equation (11) to obtain the expression of the $K(D)$ when the switching duty D is greater than 0 and less than 25%, as shown in Equation (12).

$$K(D) = \frac{1 - 4D}{1 - D} \left(0 \leq D < \frac{1}{4} \right) \quad (12)$$

Accordingly, the equations in the other three cases can be calculated and simplified to obtain the full expression of $K(D)$. The ripple ratio $K(D)$ is multiplied by Equation (11) to obtain the expression of inductor current ripple for the four-phase interleaved parallel Boost topology. As shown in Equation (13), the required minimum inductance value can be calculated from the input voltage, ripple amplitude, and switching frequency.

$$\Delta I_0 = K(D) \cdot \Delta I_L = \begin{cases} \frac{D \cdot (1-4D)}{1-D} \cdot \frac{V_{in}}{L \cdot f} & \left(D \leq \frac{1}{4} \right) \\ \frac{(4D-1)(1-2D)}{2(1-D)} \cdot \frac{V_{in}}{L \cdot f} & \left(\frac{1}{4} < D \leq \frac{1}{2} \right) \\ \frac{(2D-1)(3-4D)}{2(1-D)} \cdot \frac{V_{in}}{L \cdot f} & \left(\frac{1}{2} < D \leq \frac{3}{4} \right) \\ (4D-3) \cdot \frac{V_{in}}{L \cdot f} & \left(D > \frac{3}{4} \right) \end{cases} \quad (13)$$

According to the calculation results and given parameters above, when the input voltage is 4.0 V and the output voltage is 13.5 V, the corresponding maximum duty is 70.37%; when the input voltage is 6.0 V and the output voltage is 10.5 V, the corresponding minimum duty is 42.86%. The boundary conditions are substituted into Equation (13) to obtain the minimum value of the inductance as 53.56 μH .

2.3. Excitation Source Simulation Model and Simulation Results

To verify the output capability and waveform quality of the designed multi-phase interleaved parallel Boost topology and to adjust the excitation source control strategy and control parameters, the SimPowerSystem toolbox of MATLAB Simulink was used to build a simulation model. The simulation model of the four-phase interleaved parallel Boost circuit is shown in Figure 6. The parameters of the fuel cell model used the data of the adapted stack. The output terminal was connected to a lead-acid battery model, the rated voltage of lead-acid battery is 12 V, the capacity is 200 Ah, the initial SoC (State of Charge) is 50%, and the remaining parameters were automatically generated by the built-in model. The output sinusoidal excitation amplitude was set to 10 A and the offset current to 12 A.

As shown in Figure 7, the control model uses dual PID loops to implement the average current control logic. The actual excitation amplitude and DC offset of each excitation cycle are used as the feedback value of the outer loop. The outer loop calculates the target current value according to the error between the feedback value and the reference waveform. The target current value and the current sensor feedback value were used as the input parameters of the inner loop PID controller. The inner loop control frequency is 50 kHz, which directly controls the duty of the PWM signal to adjust the output current amplitude. Since at low excitation frequencies the control period is much longer and the inner loop control can achieve good results, the outer loop is effective only when the excitation frequency is greater than 400 Hz.

First, the filter inductor was optimized through simulation. Considering the manufacturing error and the attenuation of the inductance under high current, it was necessary to increase the inductance value appropriately to increase the margin and improve the performance under severe working conditions. However, too large inductance value leads to instability, increased losses, and a significant increase in cost. The details of the current ripple are shown in Figure 8 at the excitation frequency of 500 Hz, when the inductors are 54 μH , 120 μH , and 180 μH . The total current ripple frequency was 400 kHz, as expected. It can be seen from the simulation results that the inductor ripple amplitude can be reduced from 68 mA to 30 mA with a 120 μH inductor. However, increasing the inductance to 180 μH can only further reduce the ripple amplitude of 11 mA, but the DC loss and cost of the inductor all increased significantly. The simulation proves that the 120 μH induc-

tor achieves a good balance between ripple suppression and economy. Therefore, it was determined to use a 120 μH inductor in the prototype.

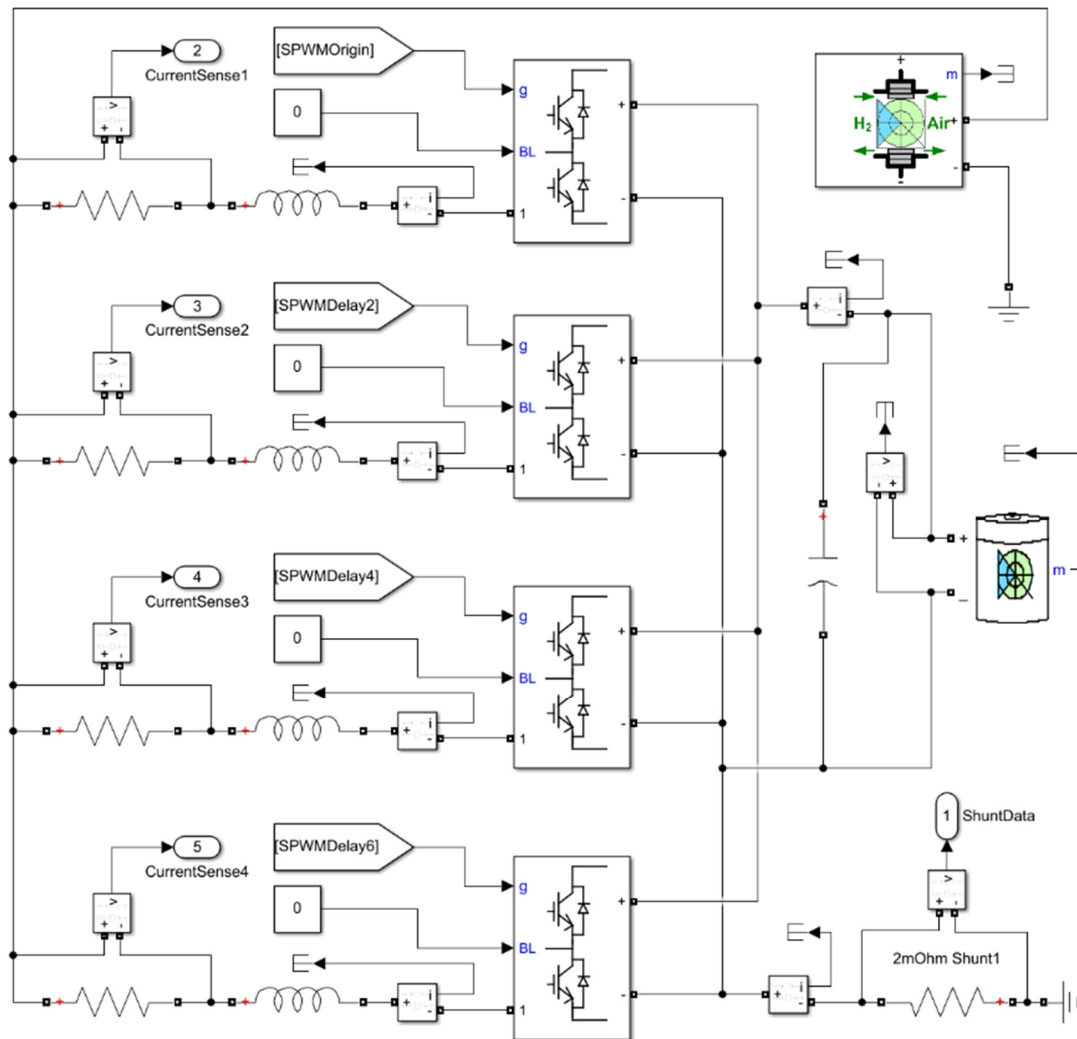


Figure 6. Simulation model of four-phase interleaved parallel topology circuit.

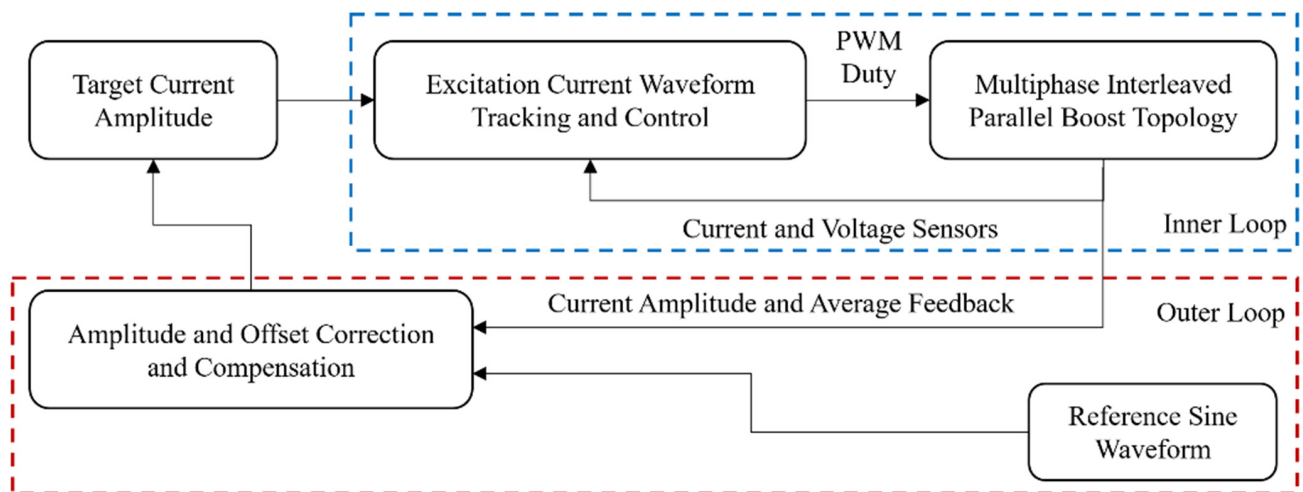


Figure 7. Block diagram of the closed-loop control model of excitation current waveform.

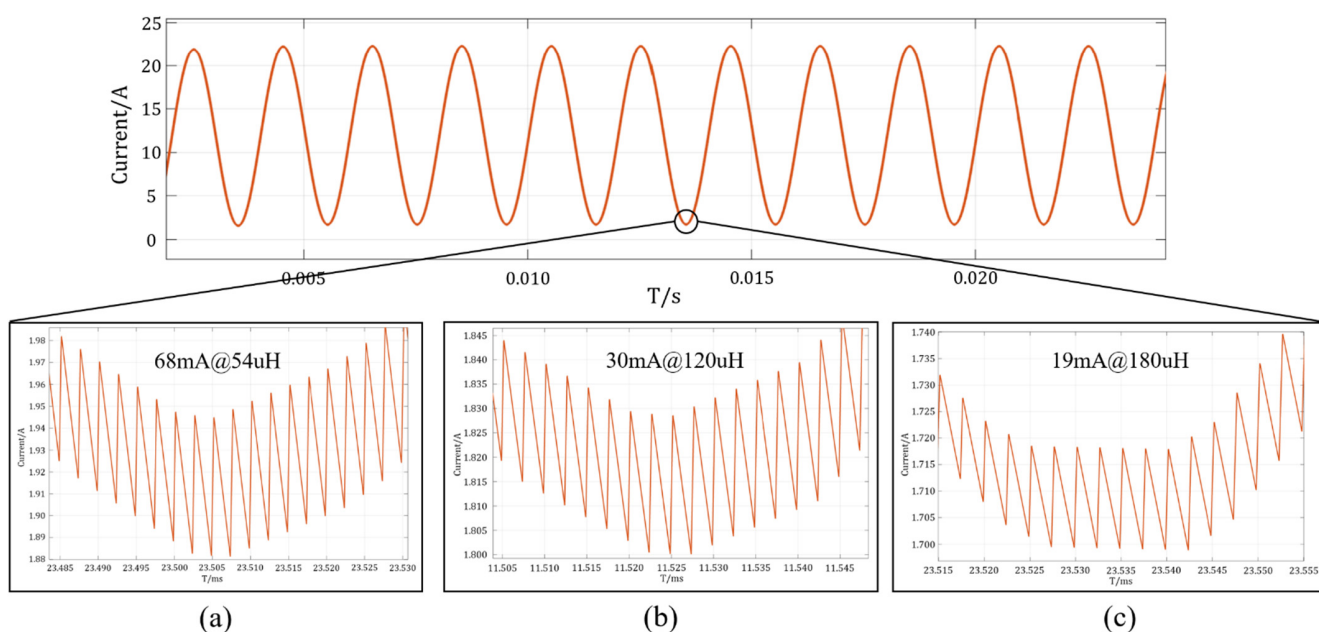


Figure 8. Ripple current of output waveforms with different inductances: (a) 54 μ H; (b) 120 μ H; (c) 180 μ H.

Then, the output performance of the circuit model was analyzed. As shown in Figure 9, the excitation waveform and partial magnification under the excitation frequency of 10 Hz, 100 Hz, 200 Hz, and 500 Hz were observed. The red curve is the output waveform, and the blue curve is the reference waveform. At each frequency, the output current waveform is a sine wave, and the distortion is small. In terms of waveform tracking ability, the lower the excitation frequency, the more accurate the excitation source can track the reference waveform.

The error analysis of the output waveform characteristics relative to the set value at each frequency is shown in Table 2. The excitation current amplitude error did not exceed 222 mA (1.85%), the offset current amplitude error did not exceed 154 mA (1.54%), and the phase error did not exceed 3.60° (1.00%, at the excitation frequency of 500 Hz). It was proven that the four-phase interleaved parallel Boost topology and the dual-loop PID control model have good waveform tracking and output capabilities which meet the design requirements.

Table 2. Output waveform characteristics and the error relative to the reference waveform at different frequencies.

Frequency/Hz	Maximum Current/A	Minimum Current/A	Average Offset Current/A	Excitation Amplitude/A	Phase Difference to Reference/°
500	21.781	1.775	11.778	10.003	3.60°
200	22.158	1.850	12.004	10.154	1.80°
100	22.058	1.939	11.998	10.060	2.16°
10	22.001	1.999	12.000	10.002	-

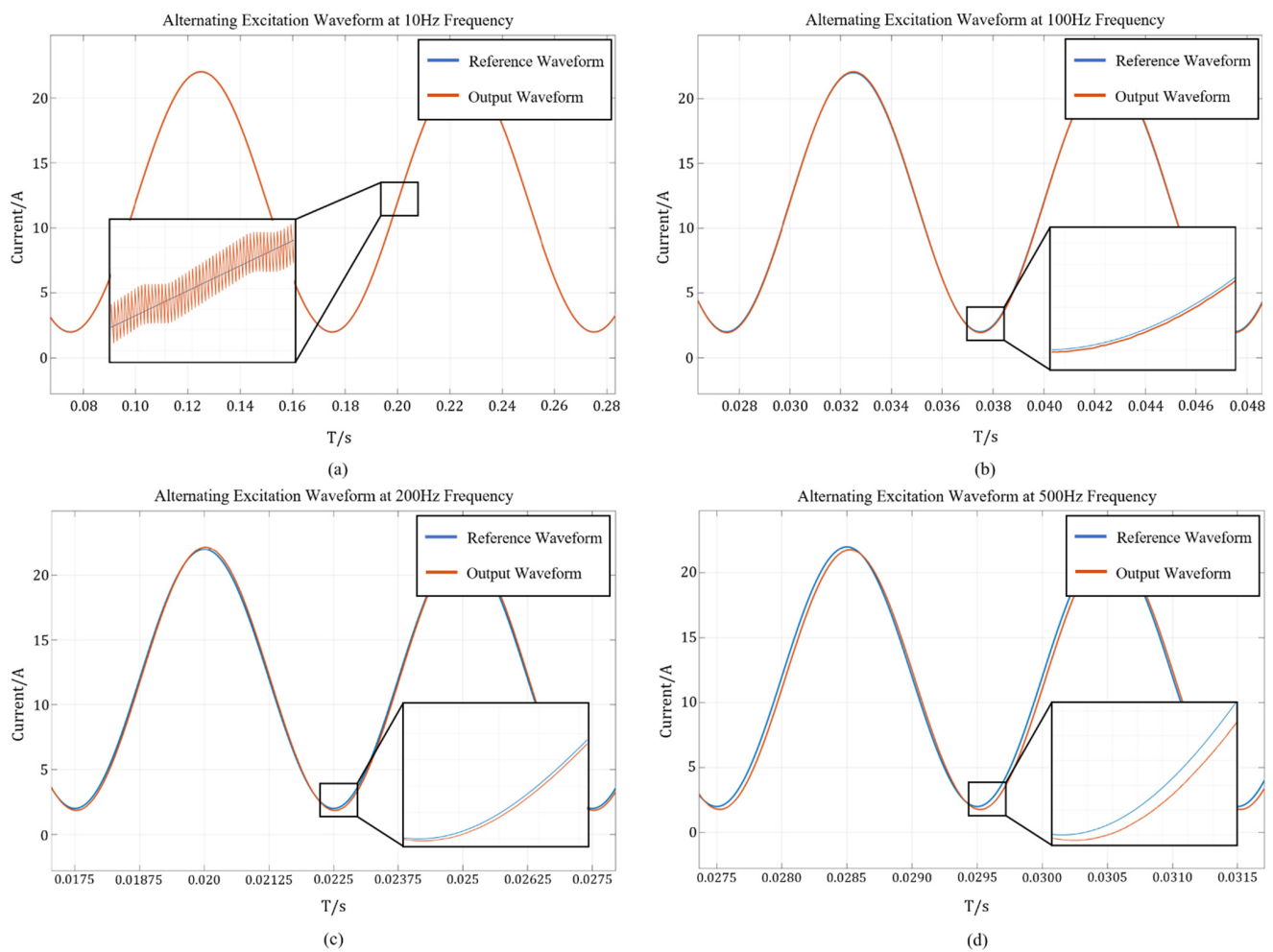


Figure 9. Comparison of the output waveform and reference waveform at different output frequencies: (a) 10 Hz; (b) 100 Hz; (c) 200 Hz; (d) 500 Hz.

3. Design of Impedance Measuring Module

3.1. Signal Acquisition and Processing Circuit

Figure 10 shows the overall structure of the designed impedance measuring module. It has eight voltage measuring channels and one current measuring channel. During the impedance measurement process, the voltage and current signals are first filtered by the low-pass filter, and the protection circuit realizes the suppression and absorption of transient voltage spikes. The voltage signal of a single cell is connected to the differential amplifier to remove the common-mode voltage signal. The amplifier output is the synthesis of the fuel cell voltage and the response voltage to the excitation. Subsequently, the voltage signal is gated by the multiplexer and enters the post-stage circuit for processing. The input signal of the current sensor also goes through the pre-processing of the differential amplifier. The gated voltage and current signals are then conditioned and buffered by the instrumentation amplifier, at which stage the signal amplitude can be adjusted. Finally, the high-precision ADC samples the two signals continuously and synchronously. The MCU is responsible for controlling the ADC, reading the conversion data, and gating the required voltage signal.

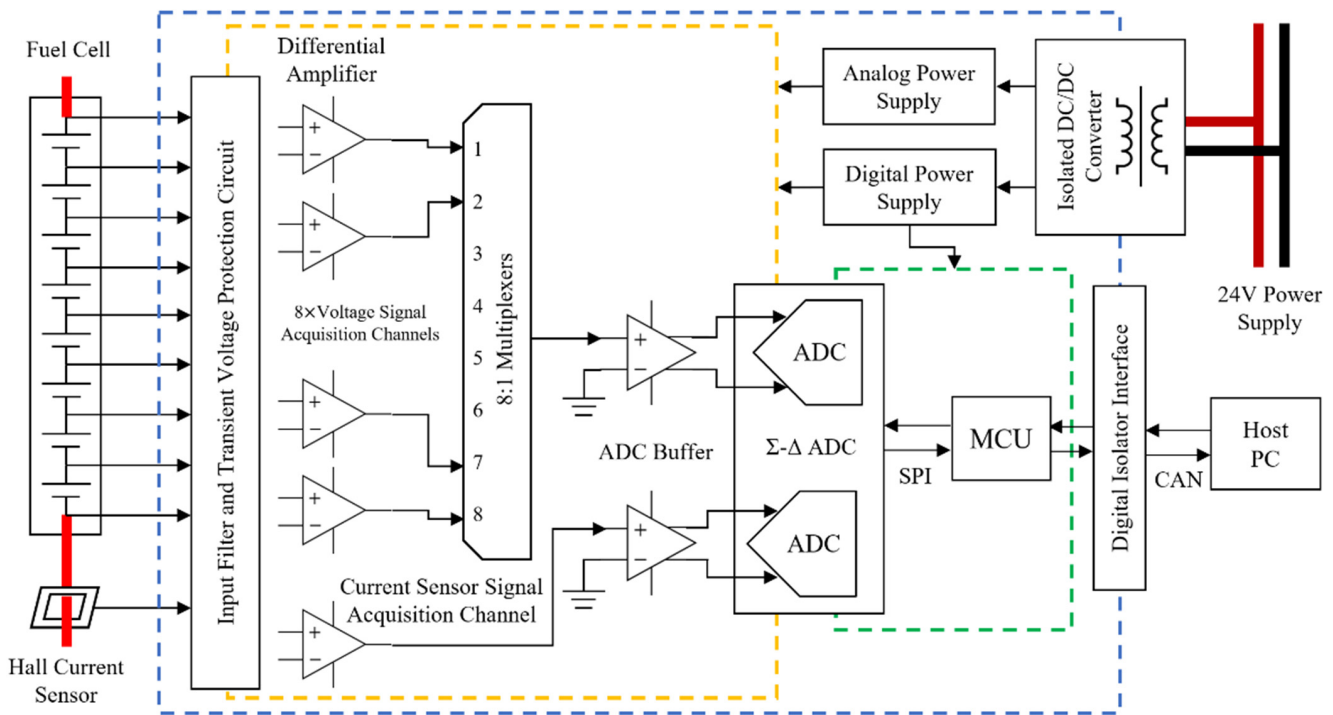


Figure 10. Hardware structure diagram of the impedance measuring module.

The power supply of the module is an isolated DC/DC converter to isolate the ground plane from the outside. The ground plane of the measuring module is only connected to the lowest potential point of the cells to be measured. The highest common-mode voltage it needs to withstand is the sum of the voltages of eight cells. The measuring module can be used to measure the impedance of a single fuel cell anywhere in the stack. Multiple modules can also be used in parallel for EIS measurements on large stacks.

3.2. Impedance Information Extraction Method

Signal sampling acquires time-domain data, while impedance analysis requires frequency-domain information of the response signal. In engineering, the commonly used method is Fast Fourier Transform (FFT). However, there are the following problems in the implementation of FFT in the embedded controller: First, the FFT calculation can only be performed after N data points are completely collected. A large amount of memory is required to store the sampled data. Second, the FFT calculation takes a long time and occupies a lot of computing resources of the controller. Finally, the result of FFT is the signal amplitude and phase evenly distributed at $N/2$ frequency points in the frequency range of $0 \sim f_s/2$ (f_s is the sampling frequency). Due to the limited number of excitation frequencies in a single measurement, most of the FFT calculation results will be discarded, which is a waste of computing resources.

In this study, the impedance calculation used a quadrature vector digital lock-in amplifier (DLIA), which is based on the idea of cross-correlation detection [42]. The principle of cross-correlation detection is: if the frequency of the signal to be measured is f_0 , a reference signal with the same frequency and no noise can be used for cross-correlation operation with the signal to be measured. The input signal expression is $x(t) = x_1(t) + n(t)$, where $x_1(t)$ is the signal to be measured and $n(t)$ is the noise signal. If the reference signal is $y(t)$, assuming that the noise signal is random and independent of the reference signal, the cross-correlation calculation process is shown as Equation (14).

$$\begin{aligned}
 R_{xy}(\tau) &= \lim_{T \rightarrow \infty} \frac{1}{2T} \int_{-T}^T x(t)y(t-\tau)dt \\
 &= \lim_{T \rightarrow \infty} \left[\frac{1}{2T} \int_{-T}^T x_1(t)y(t-\tau)dt + \frac{1}{2T} \int_{-T}^T n(t)y(t-\tau)dt \right] \\
 &= R_{x_1y}(\tau) + R_{ny}(\tau)
 \end{aligned} \quad (14)$$

where $y(t)$ is related to the signal to be measured $x_1(t)$ but not related to the noise signal $n(t)$, there is $R_{ny}(\tau) = 0$, that is, $R_{xy}(\tau) = R_{x_1y}(\tau) + R_{ny}(\tau) = R_{x_1y}(\tau)$. After cross-correlation detection, the noise is removed, and the signal-to-noise ratio is improved. Because the output signal amplitude is related to the phase difference between the signal under test and the reference signal, the process is called Phase Sensitive Detector (PSD).

It is known that the excitation signal is a sine wave, and the response signal expression is $x(t) = U_s \cos(2\pi f_0 t) + n(t)$, where f_0 is the excitation frequency. The in-phase reference signal is $y_c(t) = U_y \cos(2\pi f_0 t + \varphi)$. Since the phase angle cannot be obtained only by using the in-phase reference signal, a quadrature reference signal $y_s(t) = U_y \sin(2\pi f_0 t + \varphi)$ is added to form a quadrature vector DLIA.

The discrete sequence is obtained by sampling the input signal, the sampling frequency is $f_s = N \cdot f_0$, and N is a positive integer. If the signal is sampled for p cycles, the total number of sampling points is $M = N \times p$. Finally, the discrete sequences of the signal to be measured, the in-phase reference signal, and the quadrature reference signal are obtained as shown in Equation (15).

$$x_1[k] = U_s \cos\left[\frac{2\pi k}{N}\right]; y_c[k] = U_y \cos\left[\frac{2\pi k}{N} + \varphi\right]; y_s[k] = U_y \sin\left[\frac{2\pi k}{N} + \varphi\right] \quad (15)$$

The cross-correlation calculation process is as Equations (16) and (17):

$$R_{xy_c} = \frac{1}{M} \sum_{k=0}^{M-1} x[k]y_c[k] = \frac{1}{M} \sum_{k=0}^{M-1} x_1[k]y_c[k] = \frac{U_s U_y \cdot \cos(\varphi)}{2} \quad (16)$$

$$R_{xy_s} = \frac{1}{M} \sum_{k=0}^{M-1} x[k]y_s[k] = \frac{1}{M} \sum_{k=0}^{M-1} x_1[k]y_s[k] = \frac{U_s U_y \cdot \sin(\varphi)}{2} \quad (17)$$

Combining Equations (16) and (17), the amplitude and phase angle of the response signal with frequency f_0 can be obtained. The final impedance calculation process is shown in Figure 11. The amplitude and phase of the response voltage signal and the excitation current signal were calculated using two DLIAs, respectively. The ratio of the amplitude of the voltage signal to the amplitude of the current signal is the modulus of the impedance. The phase difference between the two signals is the phase angle of the impedance. Then through the trigonometric function calculation, the real and imaginary parts of the impedance can be obtained.

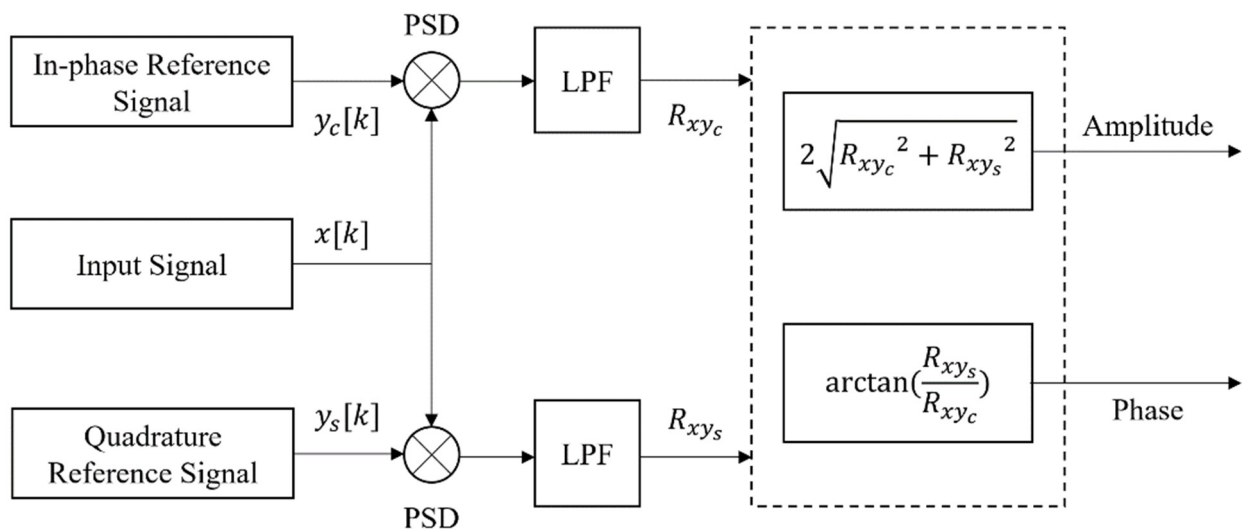


Figure 11. Schematic diagram of quadrature vector digital lock-in amplifier (DLIA).

The DLIA can update the values of R_{xyc} and R_{xys} through the computing resources that are idle between two samplings. Reference sequences can be pre-generated and stored in ROM to save memory. In the test, sampling at a speed of 16 kSPS, a total of 2048 data of float data are collected and processed. When calculated on a microcontroller (running at 200 MHz), the FFT algorithm requires 33 kB of memory and consumes 2.4 ms to perform the calculation. Correspondingly, since it is not necessary to store all the sampled data, the DLIA algorithm only takes up less than 1 kB of memory, and the calculation process is performed synchronously with the sampling process. In this way, the signal amplitude and phase results can be output immediately after each sampling cycle. The introduction of DLIA enables the impedance information extraction process to be implemented in a low-cost embedded controller, reducing the size and cost of the module.

4. Experiments and Results

After completing the design of the online fuel cell EIS measurement device, a prototype was fabricated for verification. The prototype uses low-cost embedded controllers, the size of the excitation source is 22 cm × 18 cm × 8 cm, and the size of the impedance measuring module is 17 cm × 13 cm × 4 cm. Experiments were performed on a fuel cell test bench and reference impedance data were collected using a commercial data acquisition system. The main steps of the experiment include the accuracy verification of the data acquisition system based on a standard shunt, the excitation source output experiment, and the impedance measurement experiment.

4.1. Fuel Cell Test Bench

The fuel cell test bench includes programmable DC electronic loads, data acquisition systems, and fuel cell control systems. The fuel cell parameters used for testing are introduced in Section 2. The maximum input power of the DC electronic load is 1.2 kW, and the maximum input current is 240 A. The data acquisition system has four acquisition channels and can upload the acquired data to the host computer for impedance calculation. The impedance measured by the data acquisition system is used as reference data to evaluate the measurement accuracy of the prototype. Due to the limited number of sampling channels, one sampling channel is used to collect current sensor signals. For the remaining three sampling channels, each channel samples the voltage signals of two adjacent cells. The fuel cell control system is used to provide the gas and coolant required by the fuel cell stack, and to adjust working parameters such as temperature and humidity. The total current measurement uses a LEM HTA-200S Hall current sensor with a range of 200 A. During the test, the excitation source works in parallel with the electronic load and charges the 12 V lead-acid battery. The impedance measuring module performs impedance measurement on six cells. The schematic diagram of the test platform is shown in Figure 12.

The operating parameters of the fuel cell are shown in Table 3. At this time, the excitation source output was set to a sinusoidal excitation current of 6 A and an offset current of 8 A. The input current of the DC electronic load was set to 151 A. When measuring, the fuel cell ran continuously for more than 15 min under the given operating conditions. After the voltage of each cell was observed to stabilize, excitation was applied to the fuel cell by the excitation source. The data acquisition system performs impedance measurement first, and then the impedance measuring module performs the measurement. The impedance at each frequency point as measured three times and the results were averaged as the final data. After completing the impedance measurement at one frequency point, the fuel cell was run at a steady current (159 A) under the preset parameters for 5 min, and then the impedance measurement was performed at the next frequency. This cycle was repeated until the impedance data acquisition of all frequency points was completed.

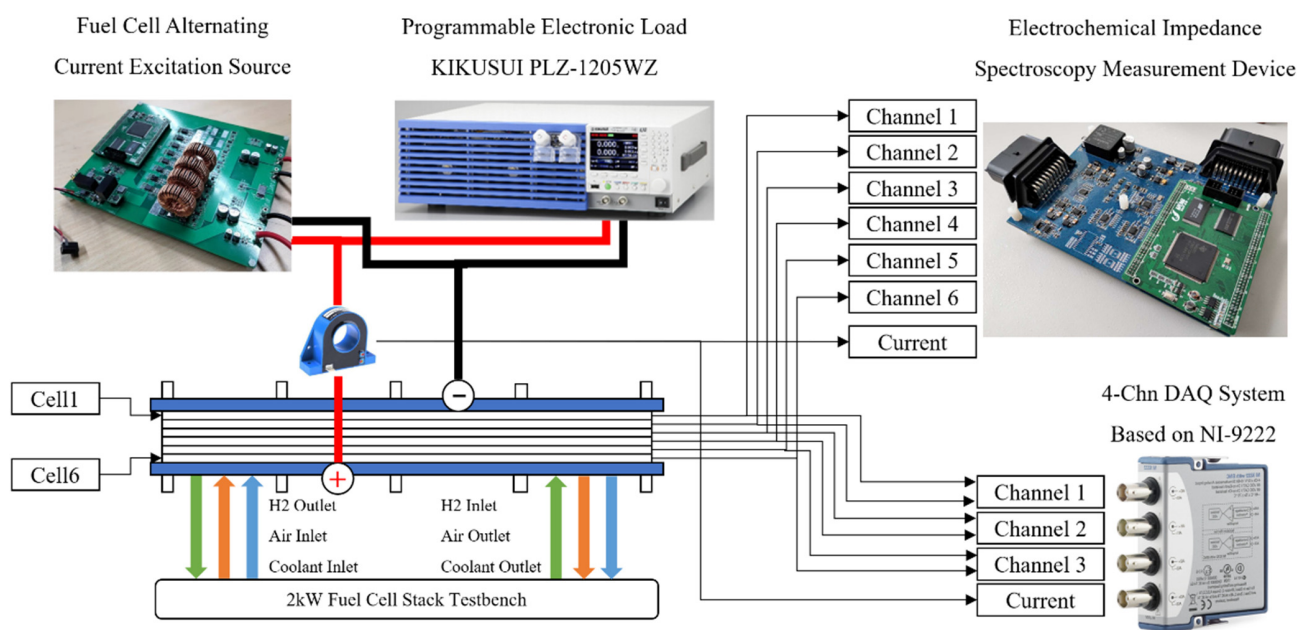


Figure 12. Schematic diagram of fuel cell test bench.

Table 3. Fuel cell operating parameters.

Parameters	Value
Air pressure	170 kPa
Hydrogen pressure	190 kPa
Stoichiometric ratios	2:2 (H ₂ /Air)
Relative humidity	50%
Coolant temperature	70 °C
Output current	159 A
Single-cell voltage	0.71 V

4.2. Data Acquisition System Measurement Accuracy Verification

In previous studies, the accuracy of this data acquisition system has been verified [13]. In the frequency range of 0.5~1000 Hz, the data acquisition system has good accuracy and stability for the measurement results of the impedance modulus and phase angle of the sample shunt. The error of impedance modulus did not exceed $\pm 0.75\%$. The error of phase angle did not exceed $\pm 1.5\%$. The test results proved that the measurement results of the data acquisition system can be used as reference data.

4.3. Prototype Testing and Results

In the experiment, the output capability of the excitation source was verified first. The impedance was then measured by an impedance measuring module. Finally, the measurement results were summarized and discussed.

4.3.1. Test Results of Excitation Source

A CYBERTEK CP8030H Hall current sensor probe was used to observe the waveform of excitation current. The sensor has a range of ± 30 A and measurement accuracy of 1%. The sensor signal was fed into a PicoScope 5444B oscilloscope. The waveforms of excitation currents at 1 Hz, 10 Hz, 100 Hz, and 500 Hz were selected for analysis, the excitation amplitude was 6 A, and the offset current was 8 A. Figure 13 shows the waveforms and partial magnifications. It can be seen from the figure that the output current waveform was a sine wave. As can be seen from the enlarged image, except for the inherent current ripple, the current waveform has no obvious distortion, glitches, or oscillations.

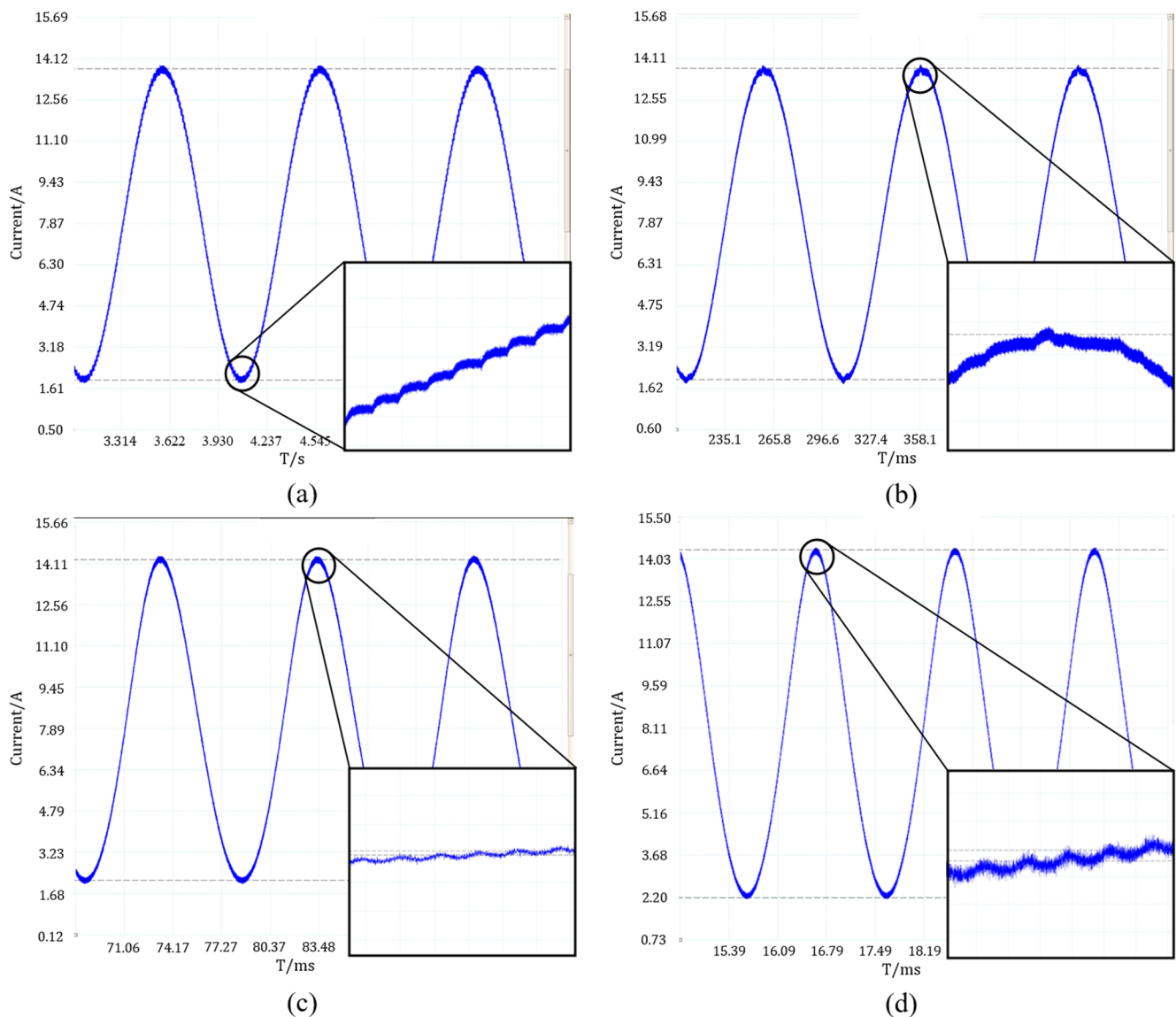


Figure 13. Excitation current waveform and partial magnification at different frequencies: (a) 1 Hz; (b) 10 Hz; (c) 100 Hz; (d) 500 Hz.

Parameters such as excitation current amplitude, ripple, and frequency were measured. The parameters of the excitation waveform at each frequency are shown in Table 4. The error of the excitation waveform parameter relative to the set value is shown in Table 5. The data show that the excitation amplitude error did not exceed 1.8%, the frequency error did not exceed 0.3%, and the offset current error did not exceed 3.5%. The result proves that the excitation source has high output precision. The maximum current ripple amplitude was 105.7 mA, which is slightly larger than the design specification, which may be related to the circuit design and the output signal glitch of the sensor. Experiments show that the output of the prototype has high precision and good waveform characteristics. Although in some working conditions, the current ripple was slightly larger than the design value, it can meet the requirements for impedance measurement.

Table 4. Excitation waveform characteristics and the error relative to the reference waveform at different frequencies.

Frequency/Hz	Maximum Current/A	Minimum Current/A	Average Offset Current/A	Excitation Amplitude/A	Ripple Amplitude/mA	Waveform Frequency/Hz
1	13.738	1.936	7.838	5.902	88.82	0.998
10	13.762	1.971	7.866	5.895	105.70	10.02
100	14.241	2.182	8.211	6.029	80.45	100.00
500	14.373	2.196	8.283	6.087	92.88	499.99

Table 5. Errors between excitation waveform parameters and set values.

Frequency/Hz	Offset Current Error	Excitation Amplitude Error	Excitation Frequency Error
1	−2.0%	−1.6%	−0.2%
10	−1.7%	−1.8%	+0.3%
100	+2.6%	+0.5%	0
500	+3.5%	+1.5%	−0.1%

4.3.2. Test Results of Impedance Measuring Module

The impedance data measured by the prototype and data acquisition system are shown in Figure 14. The fuel cell impedance at 1 Hz, 10 Hz, 50 Hz, 100 Hz, 200 Hz, and 500 Hz was measured in the experiments. Depending on the excitation frequency, the sampling rate is adjustable from 8 kSPS (500 Hz) to 32 SPS (1 Hz). For the convenience of comparison and expression, the impedance per unit area ($\text{m}\Omega/\text{cm}^2$) was used as the unit.

Figure 15 is a Nyquist plot and a Bode plot based on impedance data measured by the prototype and data acquisition system. It can be seen from Figure 15 that the Nyquist plots and frequency response characteristic curves of each fuel cell measured by the prototype are close to the measurement results of the data acquisition system. In addition, there are also differences in the frequency response characteristics of fuel cells at different positions. This phenomenon may be caused by the uneven distribution of parameters such as liquid water, reactive gases, and temperature inside the fuel cell. Through the analysis of EIS data, the state identification and fault diagnosis of the fuel cell can be realized.

For the convenience of comparison, the impedance data measured by the prototype are added in pairs and compared with the reference data. The error between the two sets of data is shown in Figure 16. The error between the impedance modulus measured by the prototype and the reference data is less than 3.5%, and the phase error is less than 1.5° , which can meet the needs of embedded online impedance measurement. When the excitation frequency is 500 Hz, the impedance modulus is small and the response voltage is weak, which requires high sensitivity and precision of the impedance measuring module. With the decrease in the excitation frequency, the impedance modulus value and the response amplitude gradually increase, which is beneficial to improving the measurement signal-to-noise ratio. However, due to the low frequency of the signal, the sampling period is correspondingly prolonged, and a longer sampling time will introduce more random errors from the outside, resulting in greater uncertainty in the measurement results of low-frequency impedance. Overall, the prototype has good impedance measurement accuracy, which can meet the needs of online embedded fuel cell impedance spectroscopy measurement.

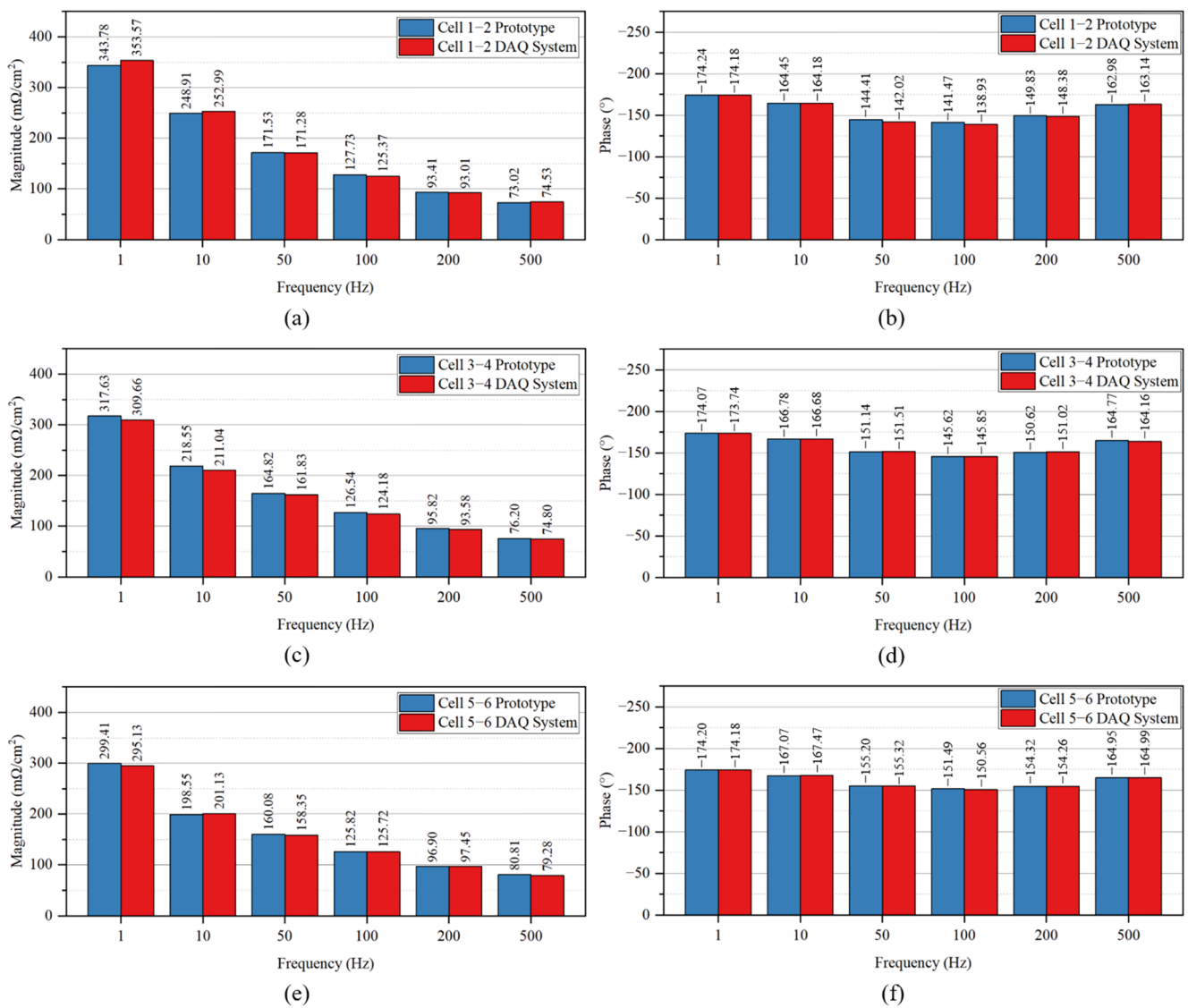
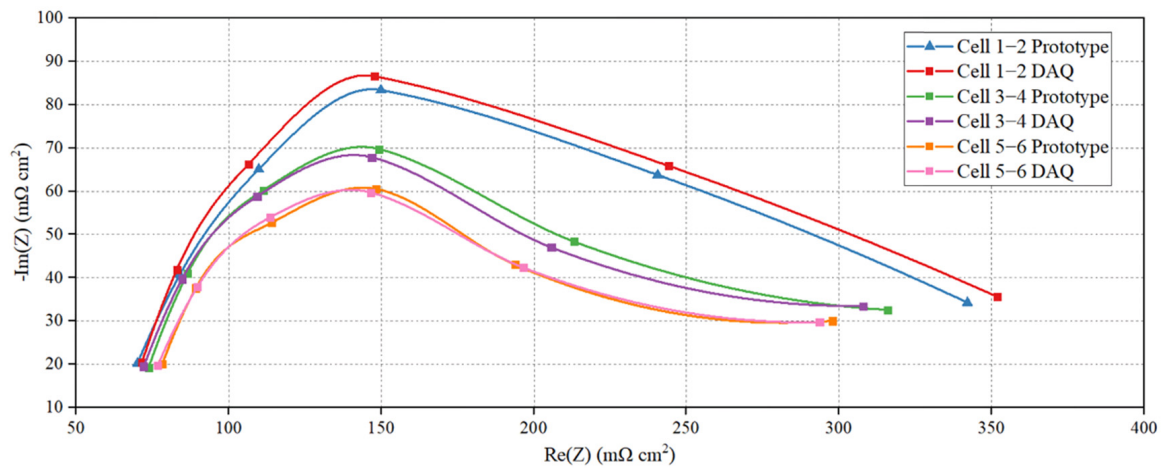
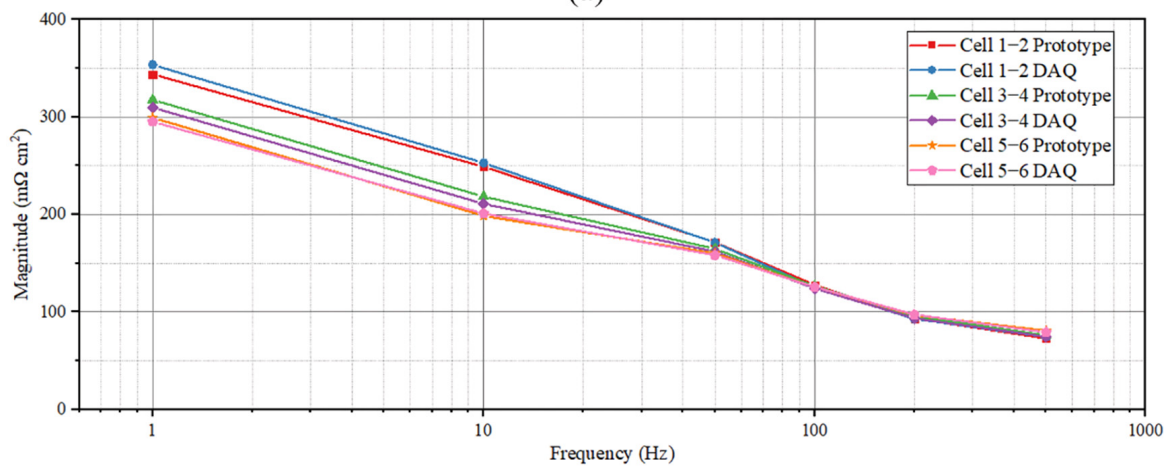


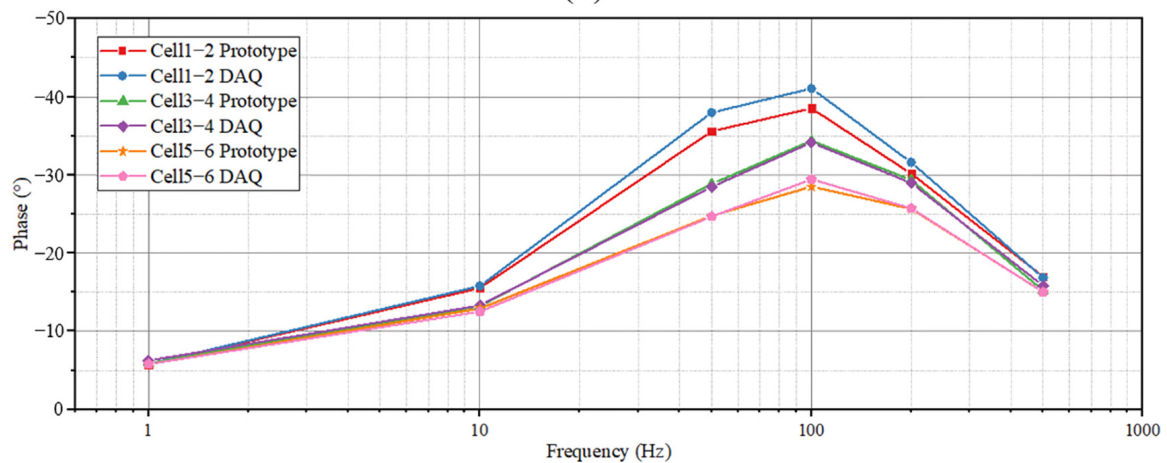
Figure 14. Impedance data measured by the prototype and data acquisition system (DAQ system): (a) modulus of cell 1 and 2; (b) phase of cell 1 and 2; (c) modulus of cell 3 and 4; (d) phase of cell 3 and 4; (e) modulus of cell 5 and 6; (f) phase of cell 5 and 6.



(a)



(b)



(c)

Figure 15. Nyquist and Bode plots based on the impedance data measured by the prototype and the DAQ system: (a) Nyquist plot; (b) magnitude diagram of the Bode plot; (c) phase angle diagram of the Bode plot.

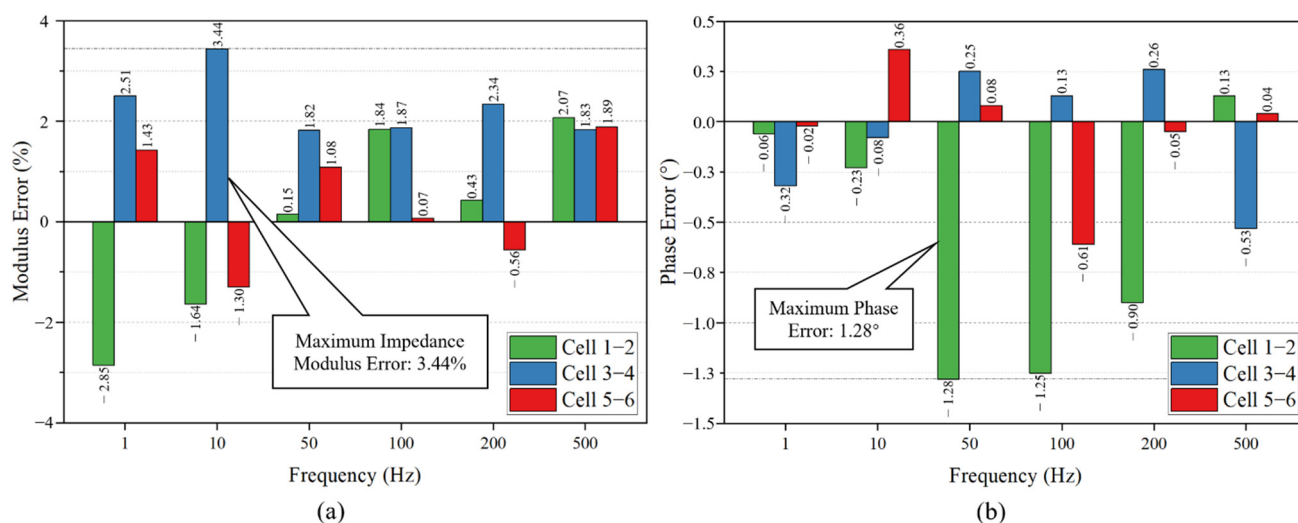


Figure 16. The error between the measured data of the prototype and the reference data: (a) modulus error; (b) phase error.

5. Conclusions

Durability and reliability are the main technical difficulties that restrict the wide application of fuel cells. Impedance-based fault diagnosis technology is an effective method to solve this problem. In this study, according to the problems of existing fuel cell impedance measurement equipment, a low-cost, embedded excitation source and online impedance measuring module are developed for a fuel cell stack. The excitation performance and measurement accuracy were tested on the fuel cell test platform.

The main conclusions of this paper are as follows:

- (1) A DC/DC converter based on a four-phase interleaved parallel Boost topology was used as the excitation source. The multi-phase interleaved structure reduces the stress of the power components, increases the equivalent switching frequency, and is beneficial to suppress the current ripple. The excitation source control was based on a double PID closed loop, and the average current control strategy was adopted. Then, the feasibility and output performance of the design was verified by simulation, and the parameters of the filter inductor were optimized;
- (2) A multi-channel impedance measuring module was designed. Using orthogonal vector DLIA, the online impedance information extraction was realized on the embedded platform with limited computing resources and power consumption. Thereby, the impedance measurement and calculation can be realized using a low-cost embedded controller. In addition, the impedance measurement module has good scalability;
- (3) According to the above design, a prototype of the EIS measurement device is fabricated. The device has a small size and can be easily integrated into the fuel cell system. Experiment results conducted on the fuel cell test platform proved that the excitation waveform output by the designed excitation source has an amplitude error of no more than 1.8%, a frequency error of no more than 0.3%, and a maximum current ripple of 105.6 mA. The error between the impedance modulus value measured by the designed impedance measuring module and the reference is within 3.5%, and the error between the phase angle and the reference is within 1.5°. This device can meet the needs of embedded online EIS measurement;
- (4) The total cost of the fabricated impedance measurement device prototype is USD 793 (USD 304 for the excitation source, USD 389 for the impedance measuring module, and USD 100 for the Hall current sensor) and can be further reduced by optimizing the design and mass production. By comparison, the Autolab electrochemical workstation is priced at USD 30,000, and Yokohama Inc. sells an impedance test system for USD 75,000. The two measurement systems described above can be used for EIS

measurement and analysis of a variety of electrochemical systems. However, these measurement systems are bulky, expensive, and have limited measurement channels. On the other hand, the designed online multi-channel EIS measurement device has low cost, low power consumption, and good scalability, and can be easily embedded into the fuel cell system which is more in line with the requirements of fuel cell fault diagnosis.

In conclusion, in this study an online multi-channel EIS measurement device for fuel cells was designed and verified experimentally with good excitation performance and measurement accuracy. Moreover, the device has the characteristics of small volume, low cost, and good expandability. The device has good application prospects in impedance-based fuel cell condition monitoring and fault diagnosis applications, which helps to improve the useful life of the fuel cell.

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