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Article

New Scheme for Seamless Operation for Stand-Alone Power Systems

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Abstract: On remote islands photovoltaic (PV) panels with battery energy storage systems (BESSs) supply electric power to customers in parallel operation with engine generators (EGs) to reduce fuel consumption and environmental burden. A BESS operates in voltage control mode when it supplies power to loads alone, while it operates in current control mode when it supplies power to loads in parallel with the EG. This paper proposes a smooth mode change of the BESS from current control to voltage control by using initial value at the output of integral part in the voltage controller, and a smooth mode change from voltage control to current control by tracking the EG output voltage to the BESS output voltage using a phase-locked loop (PLL). The feasibility of the proposed scheme was verified through computer simulations and experiments with a scaled prototype.

Keywords: photo voltaic (PV) panels; battery energy storage system (BESS); engine generator (EG); voltage control; current control; phase-locked loop (PLL); seamless operation

1. Introduction

Recently, many projects to install photovoltaic (PV) panels and battery energy storage systems (BESSs) on remote islands have been implemented in many countries to save fuel and to reduce environmental burden by primarily supplying electric power using PV panels and BESSs, and supplying additionally required electric power through complementary operation of engine generators (EGs) connected in parallel.

In this system, the electric power obtained from PV panels is generally charged in the BESS and the charged electric power is supplied to loads first to save the EG operation. If the electric power supplied by the BESS is less than the electric power required by the loads, then the EG connected in parallel is operated to cover the electric power shortage. If the electric power required by the loads decreases and can be supplied by the BESS alone, then the EG will be separated from the network. When the EG has been connected in parallel with a BESS, the EG operates to control the voltage and frequency of the system and the BESS operates in the current control mode. However, when the EG has been separated, the BESS operates in the voltage control mode to control the voltage and frequency of the system [1,2].

Therefore, if the EG that has been operated in parallel with the BESS is separated because of load reduction, the mode of BESS operation should be quickly changed from current control to voltage control. If the load increases, the EG should be activated again and begin to control the voltage and frequency after achieving synchronization with the BESS, and the latter should quickly change the operation from voltage control to current control. The operation changes from voltage control to current control and from current control to voltage control should be carried out without excessive transient phenomena, so that no interruption in the electric power supply to the loads occurs [3–5].

To carry out stable mode changes, indirect current control techniques have been studied recently [6,7]. However, this technique brings about severe transients in the case of single-phase currents because instantaneous current cannot be controlled, and control models cannot be easily obtained in the case of three-phase currents, because nonlinear functions are involved in creating voltage command values and the amount of calculations is large because the control structure is more complicated.

A capacitor voltage controller that was defined in [8] was designed using existing control theory by tracking the sine and cosine tables in the control block. However, the magnitude and phase angle of capacitor voltage can be tracked by using the generator inductance, so accurate inductance values must be known and dynamic characteristics are highly dependent on the inductance variation.

In [9] an accurate control model was proposed, in which power quality improvement was achieved by indirect current control. However, this method has the disadvantage of needing to know the accurate coupling inductance to the power system. A simple change of reference voltage was proposed in [10] which only focused on the voltage control issue of the grid-tied single-phase inverter.

In [11,12] a seamless transition sequence was precisely expressed. However, inverter voltage and grid current could experience unstable situations because the grid current has a value unequal to the voltage control output. A cascaded current-voltage control was proposed in [13] for improving the power quality of loads. However, this method was concentrated on reducing the total harmonic distortion (THD) of load voltage and current, and not concentrated on the seamless operation.

In addition, mode change methods using droop control have also been proposed [14,15]. However, these methods have drawbacks such as their slow dynamic characteristics due to the droop operation and limited maximum output due to the inability of achieving discrete current control.

A multi-loop voltage controller that includes a capacitor differential voltage feedback loop and an internal reference voltage feed-forward path was proposed in [16] for maintaining the operation voltage and reducing the transient response time. However, this method brings about an output voltage distortion regardless of any sudden voltage changes due to mode transition.

Both the current controller and the feed-forward voltage controller for a seamless mode transfer from grid-connected to islanding modes under an overvoltage condition are proposed in [17]. However, the seamless transfer strategy from the islanding to grid-connected mode is not described, and the active and reactive load powers are required when a reference grid current is calculated from the reference output voltage.

To supplement the drawbacks of the existing mode change schemes, this paper presents a new scheme to add initial values to the output of integral control in the voltage controller to enable smooth mode changes from current control to voltage control. The proposed scheme has the following advantages over the previously proposed schemes: first, it does not require complicated equations to achieve seamless operation; second, unlike indirect current control, the proposed scheme has stable and fast dynamic characteristics by using a conventional cascade control method without an additional controller. In addition, the output voltage of the EG was made to follow the output voltage of the BESS and to match the level and phase for smooth mode change when the EG is reconnected.

2. System Configuration and Control Scheme

Figure 1 shows a basic configuration of a stand-alone electric power system installed on a remote island. During the daytime on sunny days, the electric power produced by PV panels is charged to the BESS through a direct current to direct current (DC-DC) converter and supplied to loads through a direct current to alternating current (DC-AC) inverter. The EG is connected to the BESS in parallel and operates only when the remaining charged capacity of the BESS is not sufficient. Therefore, this system has the advantage of minimizing the use of the EG to save fuel and mitigate the environmental burden.

Because the inverter's alternating current (AC) output voltage contains some harmonics, an output filter is installed between the inverter and the load. The EG is connected through a transformer and a solid-state transfer switch (STS) that is composed of back-to-back connected thyristor switches.

To implement the proposed seamless operation, the output voltage and current of the BESS, the voltage and current of the load, and the voltage and current of the EG at the STS were measured.

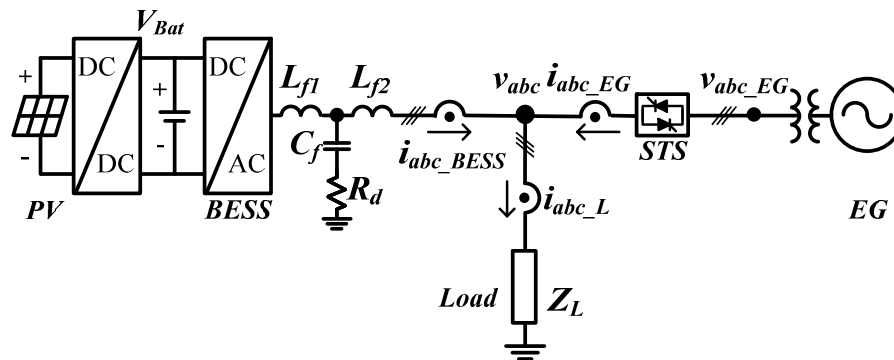


Figure 1. System configuration of a battery energy storage system (BESS) connected to a load generator.

2.1. Current Control during Parallel Operation

The current and voltage of the BESS are controlled on the synchronous reference frame. The three-phase voltage and current measured through the sensors are converted into direct current components through the static and rotating reference frame transformations described in Equations (1) and (2), respectively:

$$\begin{bmatrix} f_{ds} \\ f_{qs} \end{bmatrix} = \frac{2}{3} \begin{bmatrix} 1 & -\frac{1}{2} & -\frac{1}{2} \\ 0 & \frac{\sqrt{3}}{2} & -\frac{\sqrt{3}}{2} \end{bmatrix} \begin{bmatrix} f_a \\ f_b \\ f_c \end{bmatrix} \quad (1)$$

$$\begin{bmatrix} f_{de} \\ f_{qe} \end{bmatrix} = \begin{bmatrix} \cos\theta & \sin\theta \\ -\sin\theta & \cos\theta \end{bmatrix} \begin{bmatrix} f_{ds} \\ f_{qs} \end{bmatrix} \quad (2)$$

Figure 2 shows the current controller of the BESS using a PI control. To remove the coupled components between the d -axis current and the q -axis current, the mutually coupled components were compensated in the control output. The load voltages V_{de} and V_{qe} were separately compensated to improve the dynamic characteristics of the system [18]. The reference voltages $V_{de_inv}^*$ and $V_{qe_inv}^*$ of the BESS outputted through current control are expressed by Equation (3):

$$\begin{aligned} V_{de_inv}^* &= (k_{pc} + \frac{k_{ic}}{s})(I_{de}^* - I_{de}) - \omega L I_{qe} + V_{de} \\ V_{qe_inv}^* &= (k_{pc} + \frac{k_{ic}}{s})(I_{qe}^* - I_{qe}) + \omega L I_{de} + V_{qe} \end{aligned} \quad (3)$$

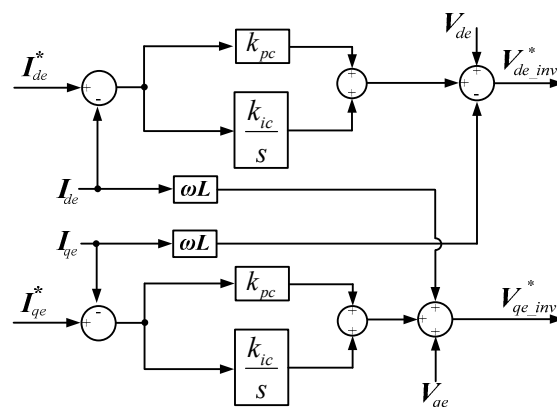


Figure 2. Current control block diagram.

The current controller has function of controlling active power P and reactive power Q . The active power and the reactive power can be expressed with respect to the current and voltage on the rotating reference frame as shown by Equation (4):

$$\begin{aligned} P &= 1.5(V_{de}I_{de} + V_{qe}I_{qe}) \\ Q &= -1.5(V_{de}I_{qe} - V_{qe}I_{de}) \end{aligned} \quad (4)$$

where V_{de} and V_{qe} represent three-phase voltages on the synchronous reference frame, I_{de} and I_{qe} represent three-phase currents on the synchronous reference frame.

If the PLL of the load voltage operates in normal condition, the active power can be expressed by the d -axis component current and the reactive power can be expressed by the q -axis component current. Therefore, the reference value for current control can be obtained from the base power:

$$I_{de}^* = \frac{P_{ref}}{1.5V_{de}}, \quad I_{qe}^* = -\frac{Q_{ref}}{1.5V_{qe}} \quad (5)$$

2.2. Voltage Control during Separate Operation

If the EG that has been operated in parallel is separated, the load voltage cannot be maintained constant. Therefore, the BESS will perform voltage control on behalf of the EG. The BESS will constantly control the frequency at 60 Hz and the line-to-line voltage at 220 V. Since the phase-A voltage should be outputted at 180 V to generate the line-to-line voltage constantly at 220 V, the reference voltage V_{de}^* is set to 180 V and the reference voltage V_{qe}^* is set to 0 V.

Figure 3 shows the configuration of the voltage controller. The d -axis and q -axis components of the reference voltage are compared with the actually measured values and the differences are entered into the proportional integral (PI) controller to obtain the reference currents I_{de}^* and I_{qe}^* .

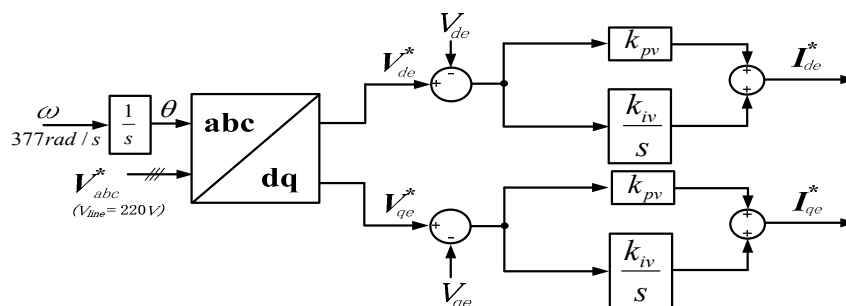


Figure 3. Voltage control block diagram.

The reference current generated as the output of the voltage controller is supplied as the input of the current controller shown in Figure 2. Therefore, the voltage controller operates in a double loop control mode. In this double loop control method, not only is the voltage controlled but also the current is controlled indirectly so that sudden changes in the current do not occur and the system can operate stably.

3. Operation Stability Analysis

Figure 4 shows the equivalent circuit for the entire system including the BESS, LCL filter, load, and EG. To analyze the stability of the control system for the EG's connected mode and disconnected mode, transfer functions were derived for both voltage control and current control. Since normally the BESS supplies electric power to loads and the EG operates as a backup power supply, it was assumed that no reverse current would flow to the EG in any event. In addition, the EG was assumed to be connected to loads through a transformer with leakage inductance.

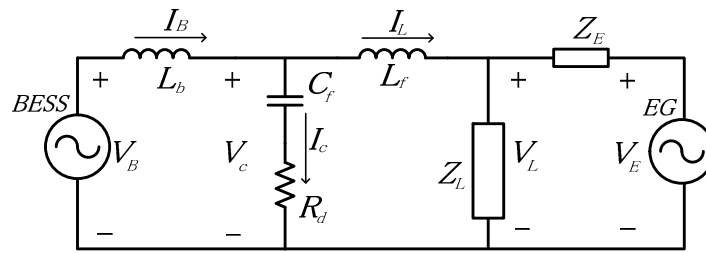


Figure 4. Equivalent circuit of the entire system.

The circuit parameters that are used in the equivalent circuit are defined as follows:

V_B , BESS output voltage; I_B , BESS output current; L_b , BESS inverter reactor; V_C , Filter capacitor voltage; I_C , Filter capacitor current; C_f , Filter capacitor; R_d , Damping resistor; L_f , Filter reactor; V_L , Load voltage; I_L , Load current; Z_L , Load impedance; V_E , Engine generator voltage; Z_E , Engine generator impedance.

In connected operation, if Kirchoff's voltage and current laws are applied, Equations (6)–(8) will be derived, as described in [19]:

$$I_B = \frac{1}{L_b s} (V_B - V_C) \quad (6)$$

$$V_C = \frac{1 + C_f R_d s}{C_f s} (I_B - I_L) \quad (7)$$

$$I_L = \frac{V_C - V_L}{s L_f} \quad (8)$$

In order to derive the current control scheme, it is assumed that Z_E is much smaller than Z_L . Then I_L can be expressed as follows:

$$I_L \cong \frac{V_C}{s L_f} \quad (9)$$

If the above three Equations (6), (7) and (9) are solved simultaneously and organized into a transfer function $G_C(s)$ with respect to I_L and V_B , Equation (10) can be derived:

$$G_C(s) = \frac{C_f R_d s + 1}{L_b L_f C_f s^3 + (L_b + L_f) C_f R_d s^2 + (L_b + L_f) s} \quad (10)$$

The block diagram for closed-loop current controller using Equation (10) can be shown in Figure 5. In this block diagram, k_{pc} is the proportional gain of the current controller and k_{ic} is the integral gain of the current controller.

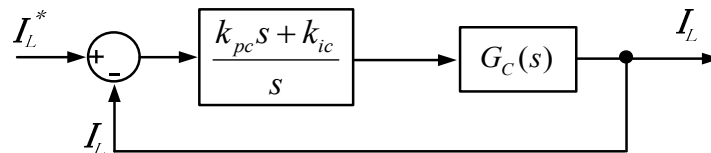


Figure 5. Current controller closed-loop block diagram.

In disconnected operation, the EG is separated from the load and the BESS carries out voltage control. Since the load voltage V_L is controlled by the BESS, not by the EG, it will affect the transfer function. Thus a transfer function for I_L can be derived again as shown in Equation (11):

$$I_L = \frac{1}{L_f s} (V_C - V_L) \quad (11)$$

Using Equations (6), (7) and (11), a transfer function $G_D(s)$ in the disconnected operation can be derived by Equation (12):

$$G_D(s) = \frac{C_f R_d s + 1}{L_b L_f C_f s^3 + C_f (L_b + L_b R_d + L_f R_d) s^2 + (C_f R_d + L_b + L_f) s + 1} \quad (12)$$

The block diagram for closed-loop voltage controller using Equation (12) can be shown using double loop control as presented in Figure 6. In Figure 6, k_{pv} is the proportional gain of the voltage controller and k_{iv} is the integral gain of the voltage controller. In addition, $Z_L(s)$ represents the load impedance and V_L represents the load voltage. Since the BESS performs load voltage control, the final output is the load voltage V_L .

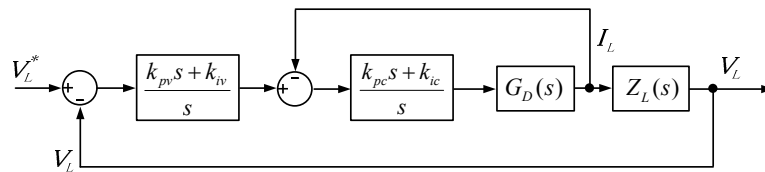


Figure 6. Voltage controller closed-loop block diagram.

Figure 7 shows the Bode diagrams for the current control transfer function and voltage control transfer function. The gains of current control are $k_{pc} = 10$, $k_{ic} = 800$ and the gains of voltage control are $k_{pv} = 1$ and $k_{ic} = 5$. Assuming that the load had a constant capacity, an RL ($R = 10 \Omega$, $L = 3 \text{ mH}$) circuit was used. On reviewing these Bode diagrams, it can be seen that the current controller and the voltage controller have sufficient phase margins so that the control is stable.

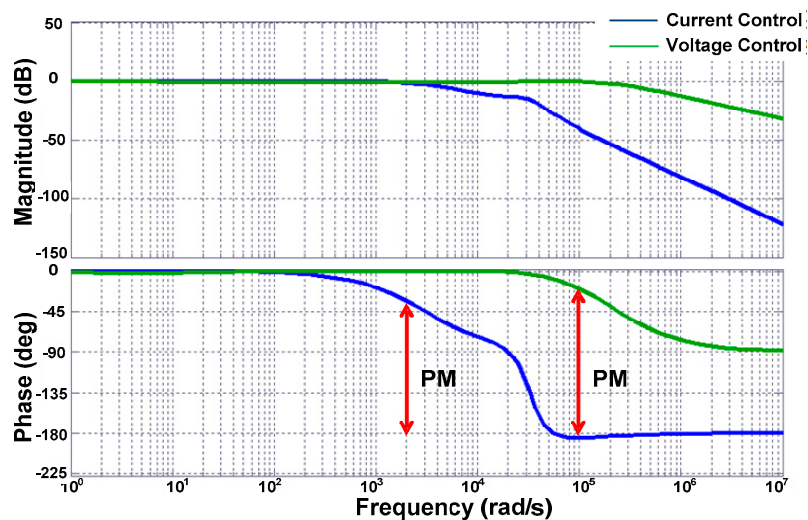


Figure 7. Bode diagram of closed-loop transfer function.

4. Proposed Seamless Operation

4.1. Mode Change into Separate Operation

Since the BESS is only operated in current control mode during the connected operation, the integral value of the voltage controller is always 0 and that the individual phases of the three-phase load voltage are balanced, the load current can be expressed by the DC components of d -axis and q -axis load current I_{de_L} and I_{qe_L} on the synchronous reference frame.

The load currents I_{de_L} , I_{qe_L} expressed on the synchronous reference frame are respectively the sum of the d -axis and q -axis currents I_{de_EG} , I_{qe_EG} and the d -axis and q -axis currents I_{de_BESS} , I_{qe_BESS} as shown in Equation (13):

$$\begin{aligned} I_{de_L} &= I_{de_EG} + I_{de_BESS} \\ I_{qe_L} &= I_{qe_EG} + I_{qe_BESS} \end{aligned} \quad (13)$$

Since I_{de_EG} , I_{qe_EG} are equal to 0 immediately after the separation of the EG, the load currents I_{de_L} , I_{qe_L} will decrease and the load voltages will also decrease accordingly. Therefore, to recover the load voltage, the amount of electric power supplied from the EG to the load should be additionally supplied by the BESS. For the BESS to recover the normal load voltage through voltage control, a certain amount of transient time is taken immediately after the mode change. Since this transient time is determined by the accumulated value of the integral gains in the voltage controller, the transient time can be reduced by appropriately adjusting the integral gains. However, if the integral gains are not accurately set, the operation of the entire system can become unstable.

In this paper, a scheme to reduce transient time without changing the controller's gains was proposed. When the voltage control of the BESS has reached the normal state, the voltage controller's integral values I_{de_i} and I_{qe_i} become same as the current control's inputs I_{de}^* and I_{qe}^* . If the current control is operated in normal, the values will also become same as the load currents $I_{de_L}^0$ and $I_{qe_L}^0$ as shown in Equation (14):

$$\begin{aligned} I_{de}^* &= I_{de_i} = I_{de_L} \\ I_{qe}^* &= I_{qe_i} = I_{qe_L} \end{aligned} \quad (14)$$

Consequently, the normal state integral value of the voltage controller is the same as the load current. Therefore, the load current is used to predict the normal state integral value. The predicted value is used as an initial value for the integrator immediately after mode change. If these integral values are assumed as $I_{de_L}^0$ and $I_{qe_L}^0$, these values will be expressed as shown by Equation (15):

$$\begin{aligned} I_{de_L}^0 &= I_{de_EG} \Big|_{t=t_0} + I_{de_BESS} \Big|_{t=t_0} \\ I_{qe_L}^0 &= I_{qe_EG} \Big|_{t=t_0} + I_{qe_BESS} \Big|_{t=t_0} \end{aligned} \quad (15)$$

Initial integral values $I_{de_L}^0$ and $I_{qe_L}^0$ are expressed as the sums of EG currents I_{de_EG} and I_{qe_EG} and BESS currents I_{de_BESS} and I_{qe_BESS} before mode change.

Figure 8 shows a configuration of the BESS's controller applied with the above initial values that are added to the integrator. Immediately after mode change, the switches in the integrator part are turned on to add $I_{de_L}^0$ and $I_{qe_L}^0$ to the integrator as shown in Equation (16):

$$\begin{aligned} I_{de}^* &= (V_{de}^* - V_{de})k_{pv} + I_{de_L}^0 \\ I_{qe}^* &= (V_{qe}^* - V_{qe})k_{pv} + I_{qe_L}^0 \end{aligned} \quad (16)$$

The switch of the integrator turns off in the next control cycle so that the initial value is not added in the next control cycle.

Through starting control with the initial values of $I_{de_L}^0$ and $I_{qe_L}^0$, the voltage controller's output currents I_{de}^* and I_{qe}^* can immediately output the normal state values. Consequently, differences between the reference voltage and the measured voltage will also decrease so that the voltage and current supplied to the load are maintained at the same levels as those before failure, even after mode change.

In order to determine the initial integral values, the load currents I_{de_L} , I_{qe_L} and the EG currents I_{de_EG} , I_{qe_EG} are necessarily measured or estimated. To estimate these currents, initial integral values $I_{de_L}^0$ and $I_{qe_L}^0$ should be determined in advance before the mode change. The BESS current can be easily obtained to control the inverter output current. The EG current can be presumed through the droop characteristics of EG. If the droop characteristics of EG does not exist, the EG current can be easily measured with an additional current sensor.

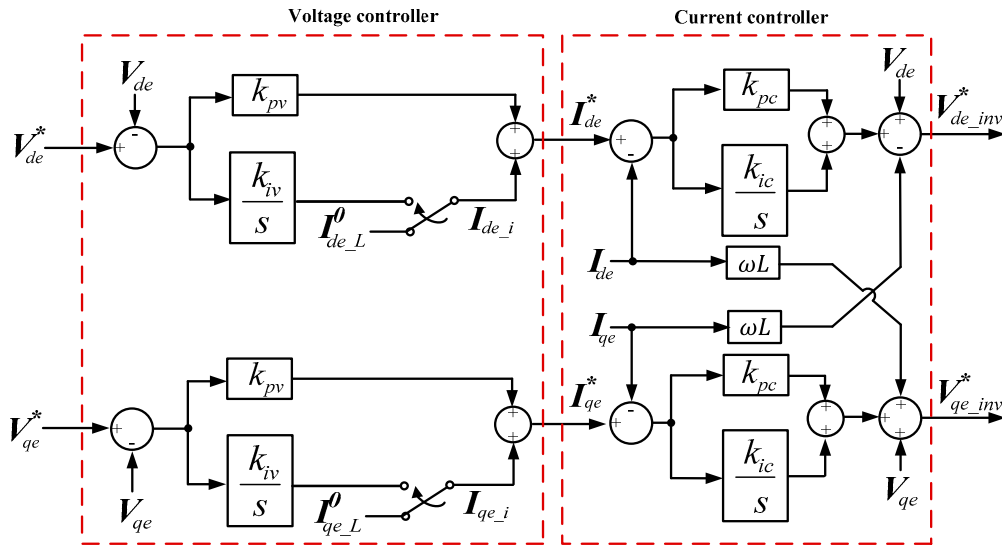


Figure 8. Proposed control block diagram for BESS.

4.2. Mode Change into Connected Operation

Since the load voltage is controlled by the BESS in a separate operation, the load voltage has a phase difference from the EG voltage when it is reconnected. If the EG is reconnected without removing this phase difference, a large transient current will flow and may adversely affect the load and the BESS. Therefore, for stable reconnection without large transient, the phases of load voltage V_{abc} and EG voltage V_{abc_EG} should be synchronized and the voltage levels should be matched. Since the EG is a rotating machine, the level and phase of the output voltage cannot be quickly adjusted. Therefore, the EG voltage V_{abc_EG} should be measured to obtain information on the level and phase, and the level and phase of the load voltage controlled by the BESS should be adjusted to follow the EG voltage.

Figure 9 shows the configuration of the PLL used to follow the phase during mode changes into a connected operation. If the selection switch is connected to C or D, phase angle control for a connected operation or a separate operation will be performed respectively.

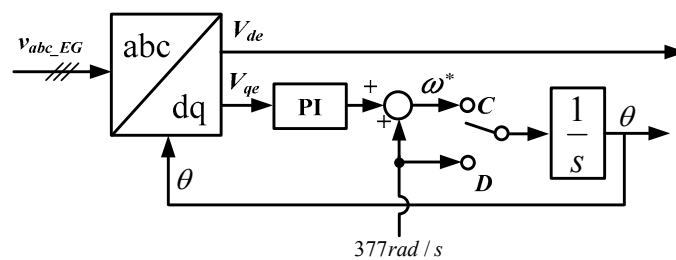


Figure 9. Phase-locked loop (PLL) for EG phase angle tracking.

In phase synchronization using a PLL, q -axis component voltage V_{qe} is used as an input of the PI controller. Three-phase voltages V_{abc} are expressed as V_{ds} and V_{qs} by a static reference frame transform as shown in Equation (17) and V_{qe} can be expressed as shown by Equation (18) using a synchronous reference frame transform:

$$\begin{aligned} V_{ds} &= V_m \cos \theta' \\ V_{qs} &= V_m \sin \theta' \end{aligned} \tag{17}$$

$$V_{qe} = V_m (-\cos \theta' \sin \theta + \sin \theta' \cos \theta) \tag{18}$$

Since $\sin\theta$ is almost equal to θ for sufficiently small phase angle, V_{qe} is expressed as shown by Equation (19):

$$V_{qe} = V_m \sin(\theta' - \theta) = V_m(\theta' - \theta) \quad (19)$$

where output θ is the phase angle of the BESS and θ' is the phase angle of the EG voltage to be followed.

Through Equation (19), it can be seen that V_{qe} indicates a phase angle error. Therefore, if the cutoff frequency of the closed-loop transfer function is sufficiently high, the PLL system can be modeled as a feedback control loop as shown in Figure 9. The BESS receives a fixed voltage as the command voltage in the voltage controller to replace the EG voltages V_{de_EG} and V_{qe_EG} on the synchronous reference frame with the reference voltage to perform voltage control.

5. Simulation and Analysis

To verify the validity of the proposed seamless operation, computer simulations were conducted using the Power System Computer Aided Design / Electromagnetic Transients including Direct Current (PSCAD/EMTDC). The circuit configuration used in the simulations is exactly same as shown in Figure 1 and individual circuit parameters are shown in Table 1.

Table 1. Circuit parameters for stand-alone power system.

Parameter	Value
Line-to-line voltage	220 V
Battery voltage	450 V
Switching frequency	10 kHz
Filter inductor L_{f1}	3 mH
Filter inductor L_{f2}	0.2 mH
Filter capacitor C_f	5 μ F
Filter damping resistor R_d	1 Ω

5.1. Mode Change into Separate Operation

In the mode change to separate operation, the case without initial value compensation to the voltage controller and the case with initial value compensation to the voltage controller were compared to each other. Figure 10 shows the mode change simulation results. It was assumed that the EG disconnection would occur at 0.4 s and the failure would be detected within 8 ms so that the controller would begin mode change at 0.408 s.

Figure 10a shows the simulation results without initial value compensation to the voltage controller. The BESS performs current control even after 0.4 s when the EG is disconnected until 0.408 s before the detection, and performs voltage control from 0.408 s. Immediately after the mode change into voltage control, it can be seen that the load voltage V_a and load current I_{a_load} have dropped below the values before the failure. In this case, it can be seen that approximately 60 ms is taken as transient time for the load voltage and current to be returned to the normal state by the voltage controller.

Figure 10b shows the results with initial integral value compensation to the voltage controller. The BESS performs current control until 0.408 s at which point the failure is detected and performs voltage control thereafter. Unlike Figure 10a, it can be seen that the transient time up to the normal state is approximately 4 ms. In addition, it can be seen that the load voltage and current did not change very much, except at the instant near the detection time.

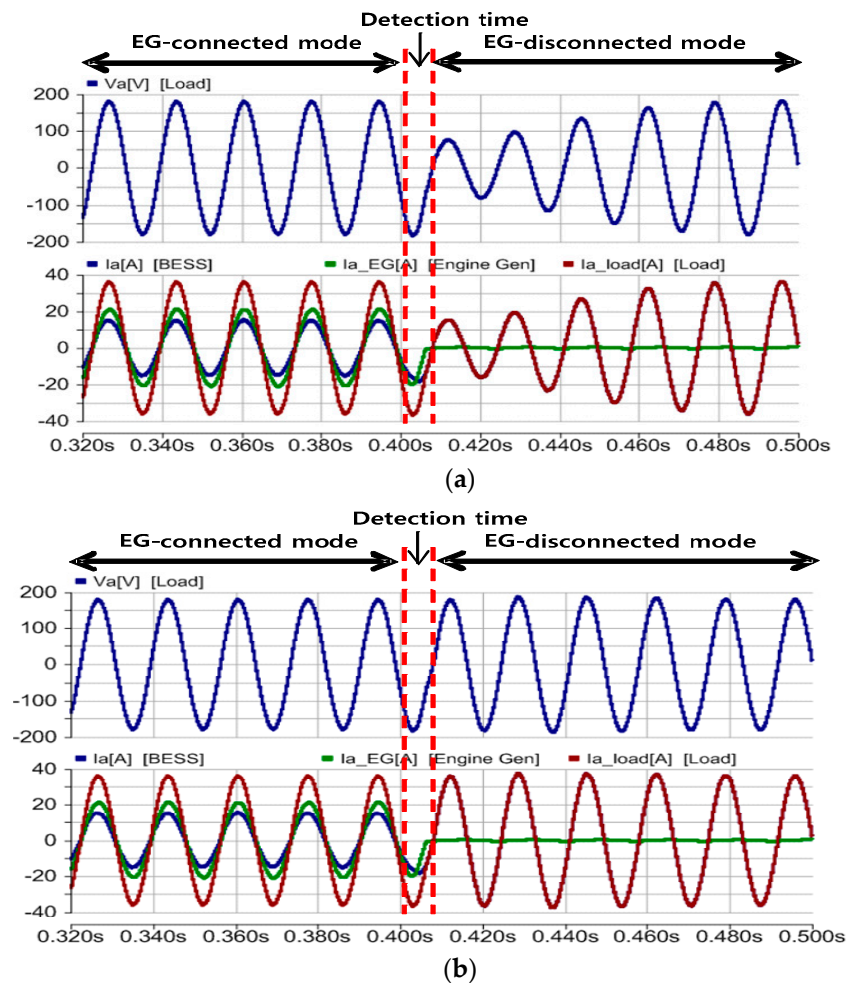


Figure 10. Mode change from current control to voltage control: (a) Without initial values; (b) With initial values. Fix mistakes in figure labels: EG-connected.

5.2. Mode Change into Reconnected Operation

Figure 11a shows the application of PLL for synchronization of the phase angle of the BESS voltage with that of the EG voltage while the BESS was supplying electric power. When the PLL synchronization begins at approximately 1.21 s, the phase angle of the load voltage V_a begins to be matched with that of the EG voltage V_{a_EG} . If the level of the load voltage is controlled after performing phase synchronization, the EG voltage will be fixed at the DC value on the synchronous reference frame so that the voltage can be matched without any large transients.

When the phase of the load voltage and that of the EG voltage have been matched through phase synchronization and voltage level control, the STS will be closed and the mode of the BESS will be changed from voltage control to current control.

Figure 11b presents the simulation results that show the process through which the EG were reconnected to operate in parallel. After performing the synchronization described in the above, the connecting operation begins at 1.72 s. It can be seen that the EG were connected without any transient currents because the EG voltage V_{abc_EG} is synchronized with the load voltage V_{abc} .

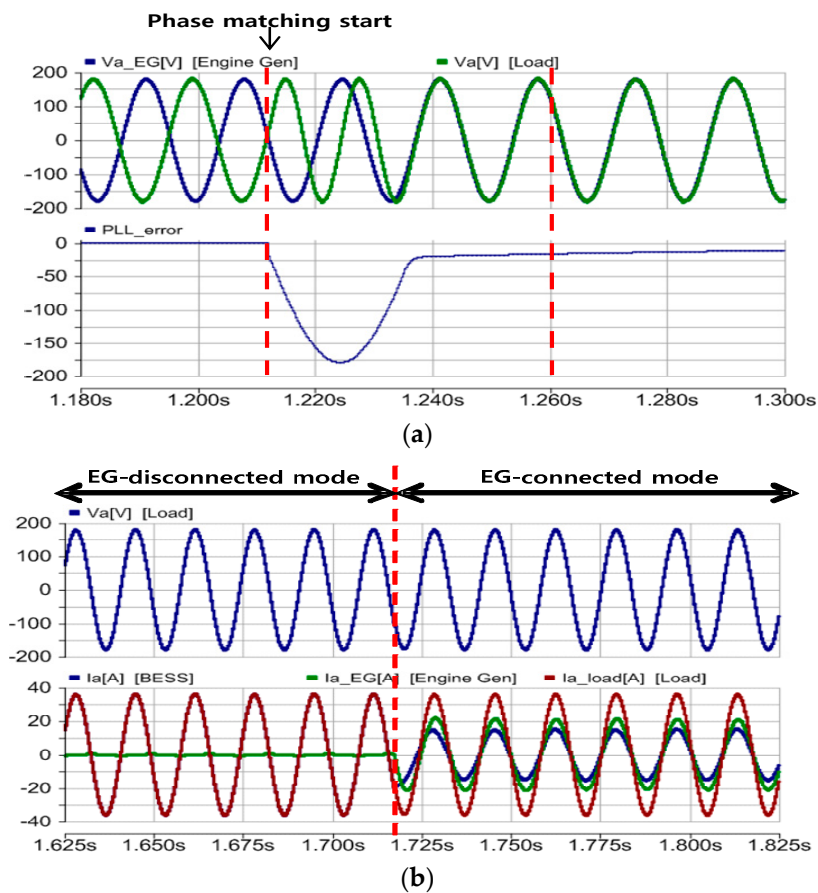


Figure 11. Mode change from voltage control to current control: (a) Phase angle tracking; (b) Voltage and current variations. Fix mistake in figure label: EG-connected

6. Hardware Experiment

To confirm the implementation feasibility of the proposed mode change, a hardware prototype was constructed in the laboratory based on the circuit configuration shown in Figure 1. The circuit parameters are exactly same as those shown in Table 1. The proposed mode change algorithm was programmed in C-code and loaded on a digital signal processor (DSP) control board based on TMS320F28335 which was fabricated in the laboratory. The experimental conditions were set identically to the simulation conditions and the detection time was set identical to 8 ms to compare with the simulation results. In addition, the on/off operation of STS was remotely controlled in the DSP control board.

6.1. Mode Change into Separate Operation

Figure 12 shows the experimental results where the mode was changed from a connected operation to a separate operation. Figure 12a shows the experimental results of the load voltage and current, the BESS current, and the EG current, in which the initial values were not applied. When the EG has been separated, the BESS changes the mode from current control to voltage control. The load voltage decreases during the detection time because the BESS is performing current control during that time. The load current decreases while the BESS current increases slowly. The operation mode of the BESS is changed into the voltage control approximately 8 ms later, so the load voltage slowly increases up to the nominal voltage approximately 5 cycles later. The load voltage and current reach their nominal values about 80 ms later.

Figure 12b shows the experimental results in which the initial values were applied to the voltage controller. As with the case where the initial values were not applied, the BESS performs current control until the detection and after then it performs voltage control. However, unlike Figure 12a, the load voltage rapidly increases up to the nominal voltage approximately one cycle later. The load voltage and current reach the nominal values about 16.7 ms later.

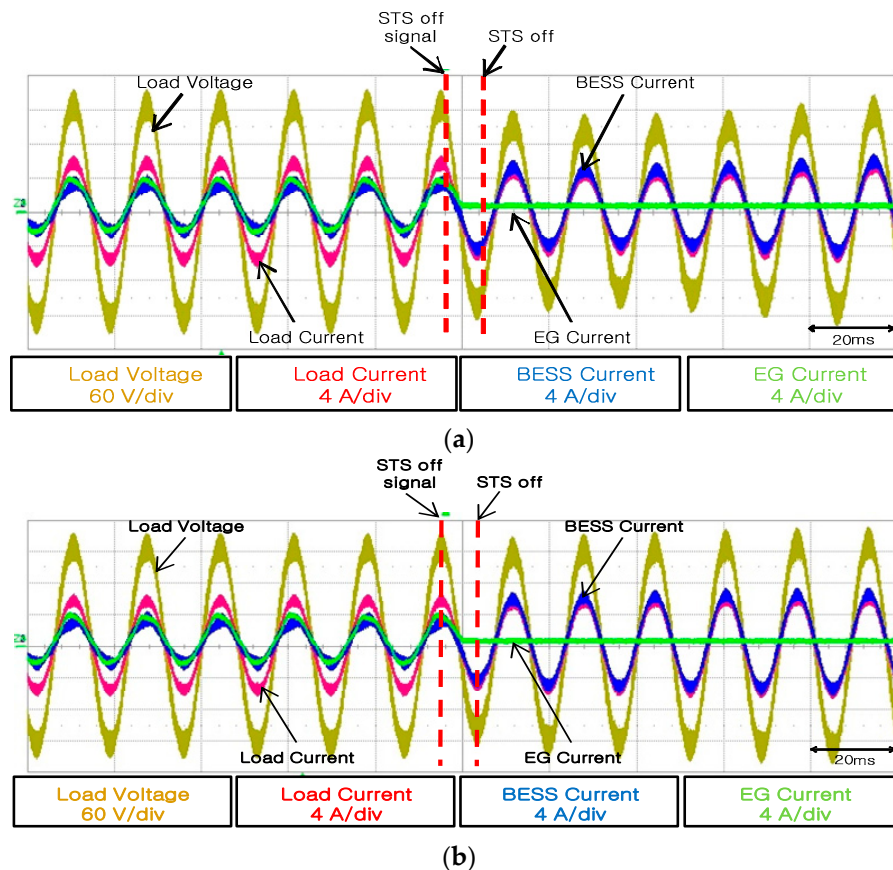


Figure 12. Mode change from current control to voltage control: (a) Without initial values; (b) With initial values.

6.2. Mode Change into Connected Operation

Figure 13 shows experimental results when the mode was changed from a separate operation to a connected operation. Figure 13a shows that the load voltage is smoothly synchronized with the EG voltage through the PLL shown in Figure 9. It takes about 6 cycles in phase locking. When the phase angle of the load voltage has become the same as that of the EG voltage, the STS is closed and the operation is changed into the connected operation.

Figure 13b shows the experimental results of the load voltage and current, the EG current, and the BESS current when the mode has been changed into a connected operation after the phase synchronization. Since the load voltage accurately followed the EG voltage, the connection could be made without any transients.

When the mode was changed into a connected operation, the control mode of the BESS was changed into current control so that the EG current increased and the BESS current decreased to share the load current.

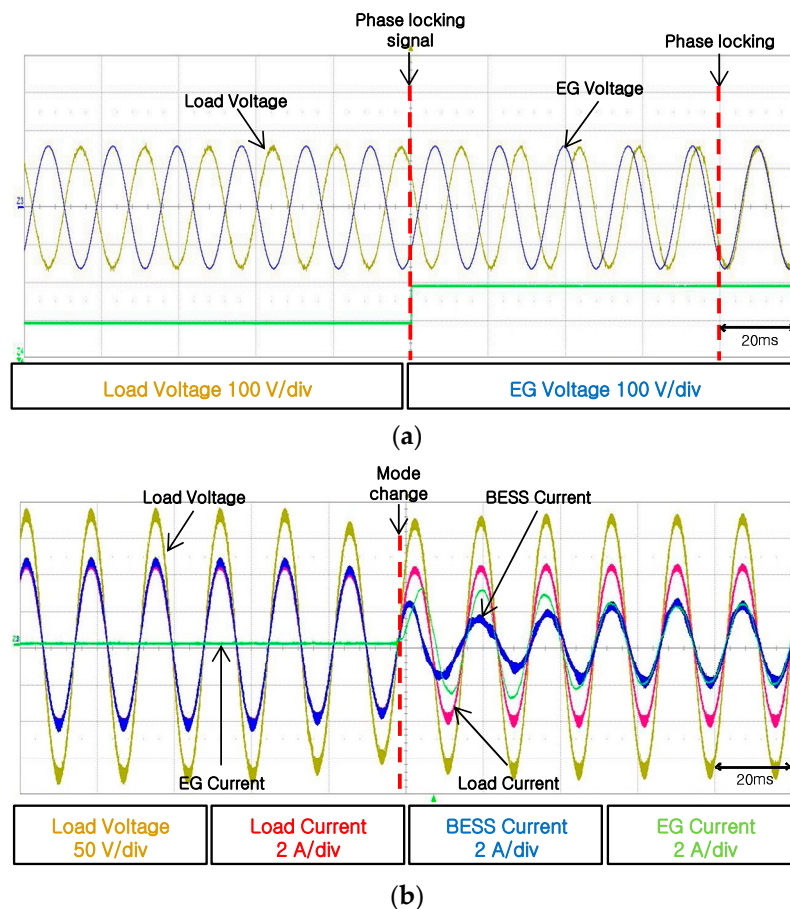


Figure 13. Mode change from voltage control to current control: (a) Phase angle tracking; (b) Voltage and current variations.

7. Conclusions

In this paper, a seamless mode change to reduce transient time during stand-alone operations was proposed. By adding appropriate initial values to the voltage controller, the proposed scheme can implement fast and stable mode changes by reducing the transient time during a mode change.

The initial values of the proposed scheme can be set as the sum of the BESS current and the EG current found out through communication or the frequency of the load voltage and can be applied in a simple manner. In addition, a method was applied to obtain information on the phase of the EG voltage when the mode is changed from a separate operation back to a connected operation and make the BESS voltage follow the phase angle of the EG voltage. The fact that mode changes were implemented smoothly without the large transients seen with the former method was identified through simulations. Through this method, the phases of two voltages can be smoothly matched so that stable connections can be made without any transient phenomena.

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