An LQR-Based Controller Design for an LCL-Filtered Grid-Connected Inverter in Discrete-Time State-Space under Distorted Grid Environment

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Keywords: LCL filter, linear quadratic regulator, digital signal processor (DSP) TMS320F28335, grid-connected inverter, internal model, distorted grid

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Article



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Abstract: In order to alleviate the negative impacts of harmonically distorted grid conditions on inverters, this paper presents a linear quadratic regulator (LQR)-based current control design for an inductive-capacitive-inductive (LCL)-filtered grid-connected inverter. The proposed control scheme is constructed based on the internal model (IM) principle in which a full-state feedback controller is used for the purpose of stabilization and the integral terms as well as resonant terms are augmented into a control structure for the reference tracking and harmonic compensation, respectively. Additionally, the proposed scheme is implemented in the synchronous reference frame (SRF) to take advantage of the simultaneous compensation for both the negative and positive sequence harmonics by one resonant term. Since this leads to the decrease of necessary resonant terms by half, the computation effort of the controller can be reduced. With regard to the full-state feedback control approach for the LCL-filtered grid connected inverter, additional sensing devices are normally required to measure all of the system state variables. However, this causes a complexity in hardware and high implementation cost for measurement devices. To overcome this challenge, this paper presents a discrete-time current full-state observer that uses only the information from the control input, grid-side current sensor, and grid voltage sensor to estimate all of the system state variables with a high precision. Finally, an optimal linear quadratic control approach is introduced for the purpose of choosing optimal feedback gains, systematically, for both the controller and full-state observer. The simulation and experimental results are presented to prove the effectiveness and validity of the proposed control scheme.

Keywords: distorted grid; digital signal processor (DSP) TMS320F28335; grid-connected inverter; internal model; linear quadratic regulator; LCL filter

1. Introduction

The increasing interest in grid-connected voltage source inverters (VSI) for renewable energy conversion systems poses a challenge to the current control design of inverter systems. In particular, the current control scheme is responsible for a high quality of injected current to meet the power quality standard of distributed generation such as the IEEE-519 in USA or the IEC 61000-3-2 in Europe [1] even under harmonically distorted grid voltages. Additionally, the filter connected between the utility grid and VSI plays an essential role to attenuate the current in high switching frequency from the pulse width modulated inverter. In general, LCL filters are regarded as being satisfactory for three-phase voltage source grid-connected inverters because they provide a better grid-side current quality with lower costs and a smaller physical size when compared to the conventional L filters. Nevertheless, the disadvantages of using LCL filters include a high-order system and the resonance behavior. As a

result, the current control strategies of LCL-filtered inverters are more difficult and complex to stabilize the system.

There are two methods to damp the resonance frequency of the LCL filter: passive damping using additional physical components on LCL circuits, and active damping implemented by modifying the control algorithm. A large number of studies in literature address the controller design in both ways. In [2], a passive resistor is added in series with the filter capacitance with the aim of attenuating the peak of LCL filter resonance. However, the main drawback of this method is that it causes extra losses through heat dissipation and overall reduction of the system efficiency. On the other hand, the active damping approaches are generally preferable and used quite commonly due to the fact that they stabilize the system without increasing the losses. An active damping realized by virtual resistance based on the capacitance current feedback is presented in [3–5]. In particular, Jia. Y et al. [3] presents the capacitance current feedback active damping implemented via a proportional gain of the feedback signal. The stability enhancement and robustness against distorted grid voltages are also discussed in this work. Similarly, the capacitor current feedback loop of the LCL filter is implemented to improve both the damping characteristic and inner-loop stability of a hierarchical control structure [4]. Furthermore, an H-infinity repetitive controller in [5] demonstrates a better performance and efficiency of the inverter by introducing the feedback of capacitor current to damp the resonance. Even though the stabilization can be achieved, those schemes increase the complexity and cost in hardware caused by extra sensors to obtain capacitor currents. As in other approaches, the studies in [6–8] present a state-space control scheme which provides a convenient and straightforward way for resonance damping. In order to avoid extra sensing devices, a full-state observer is also presented in these works, whereupon the number of sensors used in the controller is compatible with the design of the conventional L filter case.

Aside from the resonance of the LCL filter, the issue of grid voltage distortion should be taken into account in a current controller design for a grid-connected inverter. Thus, the adoption of a proportional-resonant (PR) controller in the control strategy was studied widely in both classical and modern control approaches to improve the power quality. Conventionally, the proportional-integral (PI) controllers in rotating frame and the resonant controller in the stationary frame have been studied in detail in [9], which demonstrates that an equivalent control performance can be achieved by these controllers. The research work in [10] uses multiple PI controllers that are implemented in respective reference frames rotating with the fundamental and harmonic frequencies to achieve control objectives such as reference tracking and harmonic compensation. Another approach uses a PR scheme and harmonic compensation control performing at particular frequencies in the stationary frame to restrain the disturbance caused by the distorted grid voltage [3,11-15]. However, since these approaches require two regulators to compensate both the negative and positive sequences, several regulators might be necessary in the control scheme to meet the required total harmonic distortion (THD) performance, which often leads to a significantly heavy computational burden. In order to reduce the complexity of the digital implementation, the PI and resonant (PI-RES) current control scheme constructed in the synchronous reference frame (SRF) has been studied in [16–18] to achieve multiple harmonic compensation with the number of resonant controllers reduced by half. In particular, PI-RES control schemes in the SRF are proposed for active power filters [18] or a three-phase grid converter system [16,17] for the purpose of compensating multiple harmonics.

Aside from the resonant control scheme, an H-infinity repetitive control approach was studied in [6] which presented the robustness against the system parameter variations of the LCL-type grid-connected inverter. In addition, Fu. X et al. investigated a neural network (NN)-based vector control approach for single-phase grid-connected converters to achieve an improved control performance without any damping method for LCL filter [19].

In addition to the typical control structure based on the transfer function design [3,13–15], the internal model (IM) principle proposed by Francis and Woham [20] has been applied to design controllers such as the PI and PR in the state-space. In this regard, several studies considered the IM approach to integrate control terms into the current control structure [17,21–23]. However, such

a multivariable design approach also poses a challenge to an appropriate selection of controller gains to stabilize the system as well as to ensure both the desired steady-state and transient-state performances. The current controller design using the direct pole placement method in the state-space has been accomplished in the continuous-time domain [7], as well as in the discrete-time domain [8,21]. Although the controller gains can be chosen based on the open-loop poles and the desired dynamics of the closed-loop system, the pole placement method is not an attractive way in a complex system due to the laborious process to select a large number of state-feedback and controller gains. On the other hand, the studies in [12,17,22] solve the linear matric inequalities derived from the stability condition in the Lyapunov sense to obtain the controller gains systematically. In the same vein, an optimal solution based on the linear quadratic regulator (LQR) has been presented in [23], which optimizes the cost function to calculate the optimal gains of the system.

In regard to the solution to reduce the number of needed sensors while still meeting the requirement on the availability of system state variables for full-state feedback controller, many types of observer have been studied to estimate the system state variables by using only the information from the system input and output signals. In particular, the research works in [7,8,17,21] employ the prediction-type full-state observer, while the study in [24] presents the reduced-order observer. However, there are not many studies regarding the current full-state observer in the discrete-time domain and its performance applied to three-phase LCL-filtered grid-connected inverters, even though it is known to have the advantages that the estimated value is based on the current measurement in comparison with the prediction-type observer and the impact of possible noise from the system output signals can be avoided.

This paper presents a control design methodology for a grid-connected inverter with an LCL filter in the discrete-time state-space, where the current control design is accomplished by a full-state feedback control after incorporating the integral and resonant terms into control structure. In this proposed scheme, the controller is implemented in the SRF in order that the integral control on the DC quantities can ensure zero steady-state current error. Furthermore, four harmonic components in phase currents at the 5th, 7th, 11th and 13th order can be effectively compensated at the same time with only two resonant terms at 6th and 12th order. With an aim of reducing the total number of sensors required for the control of LCL-filtered grid-connected inverters, a current full-state observer is presented in the discrete-time domain with excellent estimation capability. The augmentation of the resonant terms as well as the integral term into an inverter system model causes an increase in the number of feedback gains to be selected. To choose the feedback gains in a systematic way, the optimal linear quadratic control approach is adopted in this paper. By minimizing the cost function to satisfy the stability and robustness requirements of the system, the overall system can be designed in an effective and straightforward way. As a result, both the reference tracking and harmonic compensation capability can be achieved in an LCL-filtered grid-connected inverter with an LQR approach by using only the grid-side current sensor and grid voltage sensor. To demonstrate the effectiveness and validity of the proposed control scheme, the PSIM software-based simulation (9.1, Powersim, Rockville, MD, USA) and experiments have been carried out comprehensively by using a three-phase 2 kVA prototype grid-connected inverter under adverse grid conditions.

2. State-Space Description of a Grid-Connected Inverter with LCL Filter

2.1. Modeling of a Grid-Connected Inverter with LCL Filter

In the SRF, three-phase variables "*abc*" are transformed into two orthogonal DC phasor quantities "*dq*" by means of the Park's transformation as follows:

$$\begin{bmatrix} f_q \\ f_d \\ f_0 \end{bmatrix} = \frac{2}{3} \begin{bmatrix} \cos(\theta) & \cos(\theta - 2\pi/3) & \cos(\theta + 2\pi/3) \\ \sin(\theta) & \sin(\theta - 2\pi/3) & \sin(\theta + 2\pi/3) \\ 1/2 & 1/2 & 1/2 \end{bmatrix} \begin{bmatrix} f_a \\ f_b \\ f_c \end{bmatrix}$$
(1)

where *f* denotes the variable being transformed and θ is the rotating phasor angle.

Figure 1 shows a configuration of a three-phase grid-connected inverter with an LCL filter, in which V_{DC} denotes the DC-link voltage, R_1 , R_2 , L_1 , and L_2 are the filter resistances and filter inductances, respectively, and *C* is the filter capacitance. In the SRF, the mathematical model of the inverter system can be expressed as follows:

$$\dot{i_2^q} = -\frac{R_2}{L_2}i_2^q - \omega i_2^d + \frac{1}{L_2}v_c^q - \frac{1}{L_2}e^q$$
(2)

$$\dot{i}_{2}^{d} = -\frac{R_{2}}{L_{2}}i_{2}^{d} + \omega i_{2}^{q} + \frac{1}{L_{2}}v_{c}^{d} - \frac{1}{L_{2}}e^{d}$$
(3)

$$\dot{i}_{1}^{q} = -\frac{R_{1}}{L_{1}}i_{1}^{q} - \omega i_{1}^{d} - \frac{1}{L_{1}}v_{c}^{q} + \frac{1}{L_{1}}v_{i}^{q}$$
(4)

$$\dot{i}_{1}^{d} = -\frac{R_{1}}{L_{1}}i_{1}^{d} + \omega i_{1}^{q} - \frac{1}{L_{1}}v_{c}^{d} + \frac{1}{L_{1}}v_{i}^{d}$$
(5)

$$\dot{v}_{c}^{q} = -\omega v_{c}^{d} - \frac{1}{C} \dot{i}_{2}^{q} + \frac{1}{C} \dot{i}_{1}^{q}$$
 (6)

$$\dot{v}_{c}^{d} = \omega v_{c}^{q} - \frac{1}{C} \dot{i}_{2}^{d} + \frac{1}{C} \dot{i}_{1}^{d}$$
 (7)

where the superscript "q" and "d" denote the q-axis and d-axis variables, respectively, ω is the angular frequency of the grid voltage, i_1 is the inverter-side current, i_2 is the grid-side current, v_c is the capacitor voltage, e is the grid voltage, and v_i is the inverter output voltage.



Figure 1. Configuration of a grid-connected inverter with inductive-capacitive-inductive (LCL) filter.

From Equations (2) to (7), the continuous-time representation of inverter system can be expressed in the SRF as:

$$\dot{\mathbf{x}}(t) = \mathbf{A}\mathbf{x}(t) + \mathbf{B}\mathbf{u}(t) + \mathbf{D}\mathbf{e}(t)$$
(8)

$$\mathbf{y}(t) = \mathbf{C}\mathbf{x}(t) \tag{9}$$

where $\mathbf{x} = \begin{bmatrix} i_2^q & i_2^d & i_1^q & i_1^d & v_c^q & v_c^d \end{bmatrix}^T$ is the system state vector, $\mathbf{u} = \begin{bmatrix} v_i^q & v_i^d \end{bmatrix}^T$ is the system input vector, $\mathbf{e} = \begin{bmatrix} e^q & e^d \end{bmatrix}^T$ is the grid voltage vector, and the system matrices **A**, **B**, **C**, and **D** are expressed as:

$$\mathbf{A} = \begin{bmatrix} -R_2/L_2 & -\omega & 0 & 0 & 1/L_2 & 0\\ \omega & -R_2/L_2 & 0 & 0 & 0 & 1/L_2\\ 0 & 0 & -R_1/L_1 & -\omega & -1/L_1 & 0\\ 0 & 0 & \omega & -R_1/L_1 & 0 & -1/L_1\\ -1/C & 0 & 1/C & 0 & 0 & -\omega\\ 0 & -1/C & 0 & 1/C & \omega & 0 \end{bmatrix}$$
(10)

$$\mathbf{B} = \begin{bmatrix} 0 & 0 \\ 0 & 0 \\ 1/L_1 & 0 \\ 0 & 1/L_1 \\ 0 & 0 \\ 0 & 0 \end{bmatrix}, \mathbf{D} = \begin{bmatrix} -1/L_2 & 0 \\ 0 & -1/L_2 \\ 0 & 0 \\ 0 & 0 \\ 0 & 0 \\ 0 & 0 \end{bmatrix}, \mathbf{C} = \begin{bmatrix} 1 & 0 & 0 & 0 & 0 & 0 \\ 0 & 1 & 0 & 0 & 0 & 0 \\ 0 & 1 & 0 & 0 & 0 & 0 \end{bmatrix}$$
(11)

2.2. System Model Discretization

For a digital implementation, the discretized model of inverter system is obtained by using the zero-order hold with the sampling time T_s as [25]:

$$\mathbf{x}(k+1) = \mathbf{A}_{\mathbf{d}}\mathbf{x}(k) + \mathbf{B}_{\mathbf{d}}\mathbf{u}(k) + \mathbf{D}_{\mathbf{d}}\mathbf{e}(k)$$
(12)

$$\mathbf{y}(k) = \mathbf{C}_{\mathbf{d}}\mathbf{x}(k) \tag{13}$$

where the matrices A_d , B_d , C_d , and D_d can be calculated as follows:

$$\mathbf{A}_{\mathbf{d}} = e^{\mathbf{A}T_s} = \mathbf{I} + \frac{\mathbf{A}T_s}{1!} + \frac{\mathbf{A}^2 T_s^2}{2!} + \dots$$
(14)

$$\mathbf{B}_{\mathbf{d}} = \mathbf{A}^{-1}(\mathbf{A}_{\mathbf{d}} - \mathbf{I})\mathbf{B}, \ \mathbf{C}_{\mathbf{d}} = \mathbf{C}$$
(15)

$$\mathbf{D}_{\mathbf{d}} = \mathbf{A}^{-1} (\mathbf{A}_{\mathbf{d}} - \mathbf{I}) \mathbf{D}$$
(16)

3. Proposed Control Scheme

Figure 2 represents the proposed control scheme for a three-phase inverter connected with the utility grid through an LCL filter. The inverter is controlled by the proposed current controller through the space vector pulse width modulation (PWM). Also, the phase-locked loop (PLL) scheme is implemented to generate the phase angle of the grid voltage for the grid synchronization process. The proposed control scheme is constructed by an integral-resonant state feedback controller and a current full-state observer in the discrete-time domain with only the measurements of the grid-side currents and grid voltages. Besides, the current full-state observer is also implemented by using LCL-filter inverter model to estimate the system state variables x from the control input **u** and system outputs **y**. Those estimated states are used for the full-state feedback controller to stabilize the whole system.



Figure 2. Block diagram of the proposed integral-resonant state feedback current control scheme with observer.

3.1. Internal Model-Based Current Controller

To ensure asymptotic reference tracking as well as disturbance rejection for the harmonics in the orders of 6th and 12th in the SRF, the integral-resonant state feedback control is constructed by augmenting the integral and resonant terms in the discrete-time state-space based on the internal model principle. An integral term in the state-space is expressed as [21,25]:

$$\begin{bmatrix} \dot{x}_{i}^{q}(t) \\ \dot{x}_{i}^{d}(t) \end{bmatrix} = \mathbf{A}_{\mathbf{Pc}} \begin{bmatrix} x_{i}^{q}(t) \\ x_{i}^{d}(t) \end{bmatrix} + \mathbf{B}_{\mathbf{Pc}} \begin{bmatrix} \varepsilon^{q}(t) \\ \varepsilon^{d}(t) \end{bmatrix}$$
(17)

where $\boldsymbol{\varepsilon} = [\varepsilon^q \ \varepsilon^d]^T = \mathbf{r} - \mathbf{C}_d \mathbf{x}$ is the current error vector, $\mathbf{r} = \begin{bmatrix} i_2^{q*} \ i_2^{d*} \end{bmatrix}^T$ is the reference current vector, and $\mathbf{A}_{\mathbf{Pc}} = \begin{bmatrix} 0 & 0 \\ 0 & 0 \end{bmatrix}$, $\mathbf{B}_{\mathbf{Pc}} = \begin{bmatrix} 1 & 0 \\ 0 & 1 \end{bmatrix}$.

Similarly, resonant terms for the q-axis and d-axis in the state-space are expressed as [17,26]:

$$\begin{bmatrix} \dot{\delta}_{1i}^{q}(t) \\ \dot{\delta}_{2i}^{q}(t) \\ \vdots \\ \dot{\delta}_{1i}(t) \\ \vdots \\ \dot{\delta}_{2i}^{d}(t) \end{bmatrix} = \mathbf{A}_{\mathbf{rc}i} \begin{bmatrix} \delta_{1i}^{q}(t) \\ \delta_{2i}^{q}(t) \\ \delta_{1i}^{d}(t) \\ \delta_{2i}^{d}(t) \end{bmatrix} + \mathbf{B}_{\mathbf{rc}i} \begin{bmatrix} \varepsilon^{q}(t) \\ \varepsilon^{d}(t) \end{bmatrix} \text{ for } i = 6, \ 12$$
(18)

where
$$\mathbf{A}_{\mathbf{rc}i} = \begin{bmatrix} 0 & 1 & & \\ -(i\omega)^2 & -2\xi(i\omega) & & \\ & 0 & 1 & \\ & & -(i\omega)^2 & -2\xi(i\omega) \end{bmatrix}$$
, $\mathbf{B}_{\mathbf{rc}i} = \begin{bmatrix} 0 & 0 & \\ 1 & 0 & \\ 0 & 0 & \\ 0 & 1 \end{bmatrix}$, and ξ is a

damping factor.

As the damping ratio ξ is increased, it is well known that the magnitude of the frequency response at the resonant frequency is reduced, and the frequency response is flattened. The purpose of introducing the resonant terms is to effectively compensate the grid-side current harmonics caused by distorted grid voltages with the high gain at selective frequencies. Moreover, the proposed scheme can ensure the tracking performance of grid-side currents by adopting integral terms. Thus, damping ratio ξ is selected as zero in this study, which ensures that the harmonics from distorted voltages can be effectively compensated for by the high gain at selective frequencies.

The system states in Equations (17) and (18) are augmented as:

$$\dot{\mathbf{z}}_{\mathbf{c}}(t) = \mathbf{A}_{\mathbf{c}}\mathbf{z}_{\mathbf{c}}(t) + \mathbf{B}_{\mathbf{c}}\boldsymbol{\varepsilon}(t)$$
(19)

where $\mathbf{z_c} = [\mathbf{z_0} \ \mathbf{z_6} \ \mathbf{z_{12}}]^T$ is the entire state variables for integral and resonant terms with $\mathbf{z_0} = \begin{bmatrix} x_i^q \ x_i^d \end{bmatrix}$, $\mathbf{z_6} = \begin{bmatrix} \delta_{16}^q \ \delta_{26}^q \ \delta_{16}^d \ \delta_{26}^d \end{bmatrix}$, $\mathbf{z_{12}} = \begin{bmatrix} \delta_{112}^q \ \delta_{212}^q \ \delta_{212}^d \ \delta_{212}^d \end{bmatrix}$:

$$A_{c} = \begin{bmatrix} A_{Pc} & & \\ & A_{rc6} & \\ & & A_{rc12} \end{bmatrix}, \text{ and } B_{c} = \begin{bmatrix} B_{Pc} & \\ B_{rc6} & \\ & B_{rc12} \end{bmatrix}$$

The discrete-time counterparts of A_c and B_c can be obtained as:

$$\mathbf{A_{cd}} = e^{\mathbf{A_c}T_s} = \mathbf{I} + \frac{\mathbf{A_c}T_s}{1!} + \frac{\mathbf{A_c}^2 T_s^2}{2!} + \dots$$
(20)

$$\mathbf{B}_{cd} = \mathbf{A}_c^{-1} (\mathbf{A}_{cd} - \mathbf{I}) \mathbf{B}_c$$
(21)

Then, the entire control system can be augmented as follows:

$$\begin{bmatrix} \mathbf{x}(k+1) \\ \mathbf{z}_{\mathbf{c}}(k+1) \end{bmatrix} = \begin{bmatrix} \mathbf{A}_{\mathbf{d}} & \mathbf{0} \\ -\mathbf{B}_{\mathbf{cd}}\mathbf{C}_{\mathbf{d}} & \mathbf{A}_{\mathbf{cd}} \end{bmatrix} \begin{bmatrix} \mathbf{x}(k) \\ \mathbf{z}_{\mathbf{c}}(k) \end{bmatrix} + \begin{bmatrix} \mathbf{B}_{\mathbf{d}} \\ \mathbf{0} \end{bmatrix} \mathbf{u}(k) + \begin{bmatrix} \mathbf{D}_{\mathbf{d}} \\ \mathbf{0} \end{bmatrix} \mathbf{e}(k) + \begin{bmatrix} \mathbf{0} \\ \mathbf{B}_{\mathbf{cd}} \end{bmatrix} \mathbf{r}(k) \quad (22)$$

$$\mathbf{y}(k) = \begin{bmatrix} \mathbf{C}_{\mathbf{d}} & \mathbf{0} \end{bmatrix} \begin{bmatrix} \mathbf{x}(k) \\ \mathbf{z}_{\mathbf{c}}(k) \end{bmatrix}$$
(23)

Considering the augmented system, the state feedback control is expressed as:

$$\mathbf{u}(k) = -[\mathbf{K}_{\mathbf{x}}\mathbf{K}_{\mathbf{z}}] \begin{bmatrix} \mathbf{x}(k) \\ \mathbf{z}_{\mathbf{c}}(k) \end{bmatrix} = \mathbf{u}_{\mathbf{x}}(k) + \mathbf{u}_{\mathbf{z}}(k)$$
(24)

where $\mathbf{u}_{\mathbf{x}}(k) = -\mathbf{K}_{\mathbf{x}}\mathbf{x}(k)$ and $\mathbf{u}_{\mathbf{z}}(k) = -\mathbf{K}_{\mathbf{z}}\mathbf{z}_{\mathbf{c}}(k)$.

The augmented system in Equations (22)–(24) can be rewritten in a compact form as:

$$\mathbf{x}_e(k+1) = \mathbf{A}_e \mathbf{x}_e(k) + \mathbf{B}_e \mathbf{u}(k) + \mathbf{D}_e \mathbf{e}(k) + \mathbf{B}_{re} \mathbf{r}(k)$$
(25)

$$\mathbf{y}(k) = \mathbf{C}_e \mathbf{x}_e(k) \tag{26}$$

$$\mathbf{u}(k) = -\mathbf{K}\mathbf{x}_e(k) \tag{27}$$

where $K = [K_x K_z]$ is a set of feedback gains and $K_z = [K_{Pz} K_{6z} K_{12z}]$. The detailed block diagram of the proposed current controller is depicted in Figure 3, where A_P , A_{r6} , A_{r12} , B_P , B_{r6} , and B_{r12} denote the discrete-time counterparts of A_{Pc} , A_{rc6} , A_{rc12} , B_{Pc} , B_{rc6} , and B_{rc12} , respectively.



Figure 3. Detailed control block diagram of the proposed current controller.

3.2. Design of an Optimal Feedback Control Using Linear Quadratic Regulator (LQR) Approach

The state feedback control input $\mathbf{u}(k)$ will be an optimal control input to ensure the control performance and system stability if the gain matrix K in system Equations (25)–(27) are evaluated systematically by minimizing the discrete quadratic cost function as follows [27,28]:

$$\mathbf{J} = \frac{1}{2} \sum_{k=0}^{\infty} \mathbf{x}_e^T(k) \mathbf{Q} \mathbf{x}_e(k) + \mathbf{u}^T(k) \mathbf{R} \mathbf{u}(k)$$
(28)

where **Q** is positive semi-definite matrix and **R** is positive definite matrix.

To obtain the optimal control input vector $\mathbf{u}(k)$ in closed loop form, an $n \times n$ real symmetric matrix **P** with *n* being the number of state variables should be determined as the solution of the discrete Riccati equation as follows:

$$\mathbf{P} = \mathbf{Q} + A_e^T \mathbf{P} A_e - A_e^T \mathbf{P} B_e \left(\mathbf{R} + B_e^T \mathbf{P} B_e \right)^{-1} B_e^T \mathbf{P} A_e.$$
(29)

Then, the gain matrix *K* can be calculated in terms of **P** as follows:

$$\boldsymbol{K} = \mathbf{R}^{-1} \boldsymbol{B}_{e}^{T} \left(\boldsymbol{A}_{e}^{T} \right)^{-1} (\mathbf{P} - \mathbf{Q}).$$
(30)

The optimal control law is obtained by substituting Equations (30) to (27) as:

$$\mathbf{u}(k) = -\mathbf{R}^{-1} \mathbf{B}_e^T \left(\mathbf{A}_e^T \right)^{-1} (\mathbf{P} - \mathbf{Q}) \mathbf{x}_e(k).$$
(31)

Then, the whole control system can be re-modeled as:

$$\mathbf{x}_{e}(k+1) = \left\{ \mathbf{A}_{e} - \mathbf{B}_{e} \left[\mathbf{R}^{-1} \mathbf{B}_{e}^{T} \left(\mathbf{A}_{e}^{T} \right)^{-1} (\mathbf{P} - \mathbf{Q}) \right] \right\} \mathbf{x}_{e}(k) + \mathbf{D}_{e} \mathbf{e}(k) + \mathbf{B}_{re} \mathbf{r}(k).$$
(32)

The discrete Riccati Equation (29) can be solved by MATLAB (R2017b, The MathWorks, Inc, Natick, MA, USA) functions "dare" and "dlqr". It is obvious that all the elements in the feedback gain matrix K rely on the choice of the symmetrical weighting matrices Q and R which determine the relative importance of the state variable performance and expenditure of energy by control input signals. The larger value of Q indicates that the system is stabilized with less change in the states, while the smaller Q implies that the states would be in larger variation. Similarly, the emphasis on R represents the behavior of the system states inputs. With the larger value of R, the system is stabilized with smaller value of R. In the proposed control scheme, the weighting matrices Q and R are selected as:

$$\mathbf{Q} = \begin{bmatrix} 10^{-2} \cdot \mathbf{I}^{6 \times 6} & \mathbf{0}^{6 \times 2} & \mathbf{0}^{6 \times 8} \\ \mathbf{0}^{2 \times 6} & 6.3 \times 10^8 \cdot \mathbf{I}^{2 \times 2} & \mathbf{0}^{2 \times 8} \\ \mathbf{0}^{8 \times 6} & \mathbf{0}^{8 \times 2} & 6.3 \times 10^8 \cdot \mathbf{I}^{8 \times 8} \end{bmatrix}, \quad \mathbf{R} = \begin{bmatrix} 1 & 0 \\ 0 & 1 \end{bmatrix}$$
(33)

where $I^{n \times m}$ and $0^{n \times m}$ are the identity and zero matrices with appropriate dimensions, respectively. To improve the transient responses as well as to achieve the control objectives, a large weighting value of 6.3×10^8 is used for the state variables of the IM components z_c , while a quite small value of 10^{-2} is chosen for six system state variables. As a result, a fast reference tracking of state variables and a good suppression capability for the distorted harmonics on grid voltages can be obtained. The simulation and experimental results are presented in next section to demonstrate the performance of the optimal control scheme.

3.3. LQR-Based Current Full-State Observer in Discrete-Time

To realize a full-state feedback controller in the augmented system in Equations (25)–(27), all the system state variables should be available for feedback purpose. However, in a three-phase LCL-filtered grid-connected inverter, the additional sensing devices usually increase the total cost and hardware complexity. Therefore, in the proposed control scheme, an LQR-based discrete-time current full-state observer is employed to produce the estimated signals for the grid-side current \hat{i}_2 , the inverter-side current \hat{i}_1 , and the capacitor voltages \hat{v}_c .

Regarding to the selection of observer type, there are three alternatives which are the prediction-type observer, the current observer, and the reduced-order observer [20]. In the prediction-type observer, the estimated states $\hat{\mathbf{x}}(k)$ are determined based on the past measurement of outputs at (k - 1)T. This means that the control signal $\mathbf{u}_{\mathbf{x}}(k) = -\mathbf{K}_{\mathbf{x}}\hat{\mathbf{x}}(k)$ does not utilize the most current information on outputs $\mathbf{y}(k)$, which leads to the inaccuracy of estimated values and might cause control performance degradation. On the other hand, the reduced-order observer can solve the drawback of the prediction-type observer by using the measured states to estimate remaining unmeasurable states at time kT. However, if the measurement variables are noisy, the imprecise measured states may influence directly the feedback control inputs. For these reasons, the current full-state observer is employed for the three-phase LCL-filtered grid-connected inverter in this paper, which yields a precise estimation capability even under harmonically distorted grid voltage condition.

From the discretized model of the inverter system in Equations (12) and (13), a current full-state observer is given as follows:

$$\bar{\mathbf{x}}(k+1) = \mathbf{A}_{\mathbf{d}} \hat{\mathbf{x}}(k) + \mathbf{B}_{\mathbf{d}} \mathbf{u}(k) + \mathbf{D}_{\mathbf{d}} \mathbf{e}(k)$$
(34)

$$\hat{\mathbf{x}}(k+1) = \overline{\mathbf{x}}(k+1) + \mathbf{K}_{\mathbf{e}}[\mathbf{y}(k+1) - \mathbf{C}_{\mathbf{d}}\overline{\mathbf{x}}(k+1)]$$
(35)

where the symbol "^" denotes the estimated variables, $\mathbf{K}_{\mathbf{e}}$ is the observer gain matrix, and $\overline{\mathbf{x}}(k+1)$ is the first estimate of the state at time (k+1)T. In this type of observer, $\overline{\mathbf{x}}(k+1)$ is first calculated from the dynamics of system and input signal at kT, and then this estimation is added with the correction term in Equation (35) when the output signals are measured at time (k+1)T. Figure 4 presents a discrete-time current full-state observer with a state-feedback controller, where the estimated state variables are used to construct the state feedback control inputs.



Figure 4. State feedback control using current full-state observer in the discrete-time domain.

In order to determine the observer gain matrix K_e , the estimation error \tilde{x} is defined as:

$$\widetilde{\mathbf{x}}(k) = \mathbf{x}(k) - \widehat{\mathbf{x}}(k) \tag{36}$$

Then, the error dynamics of the observer can be obtained by subtracting Equation (35) from Equation (12) as follows:

$$\widetilde{\mathbf{x}}(k+1) = (\mathbf{A}_{\mathbf{d}} - \mathbf{K}_{\mathbf{e}}\mathbf{C}_{\mathbf{d}}\mathbf{A}_{\mathbf{d}})\widetilde{\mathbf{x}}(k)$$
(37)

To ensure that the observer is stable and the estimated states well track the actual ones, the observer gain matrix should be chosen in order that the matrix $(\mathbf{A}_d - \mathbf{K}_e \mathbf{C}_d \mathbf{A}_d)$ or $(\mathbf{A}_d^T - (\mathbf{C}_d \mathbf{A}_d)^T) \mathbf{K}_e^T)$ is stable. By applying a LQR approach similar to the design of a state feedback control, the discrete Riccati equation can be applied for observer design as:

$$\mathbf{P}_{\mathbf{o}} = \mathbf{Q}_{\mathbf{o}} + \mathbf{A}_{\mathbf{d}} \mathbf{P}_{\mathbf{o}} \mathbf{A}_{\mathbf{d}}^{T} - \mathbf{A}_{\mathbf{d}} \mathbf{P}_{\mathbf{o}} (\mathbf{C}_{\mathbf{d}} \mathbf{A}_{\mathbf{d}})^{T} (\mathbf{R}_{\mathbf{o}} + (\mathbf{C}_{\mathbf{d}} \mathbf{A}_{\mathbf{d}}) \mathbf{P}_{\mathbf{o}} (\mathbf{C}_{\mathbf{d}} \mathbf{A}_{\mathbf{d}})^{T})^{-1} (\mathbf{C}_{\mathbf{d}} \mathbf{A}_{\mathbf{d}}) \mathbf{P}_{\mathbf{o}} \mathbf{A}_{\mathbf{d}}^{T}$$
(38)

where P_0 is the solution of Riccati equation (38), and Q_0 and R_0 are weighting matrices. Hence, the observer gain K_e can be calculated in terms of P_0 as follows:

$$\mathbf{K}_{\mathbf{e}} = \mathbf{R}_{\mathbf{o}}^{-1} \mathbf{C}_{\mathbf{d}} \mathbf{A}_{\mathbf{d}} (\mathbf{A}_{\mathbf{d}})^{-1} (\mathbf{P}_{\mathbf{o}} - \mathbf{Q}_{\mathbf{o}}).$$
(39)

In this paper, the optimal observer gains can be chosen by utilizing the MATLAB function "dlqr".

4. Simulation Results

In order to verify the feasibility and validity of the proposed current control scheme, simulations were carried out for an LCL-filtered three-phase grid-connected inverter based on the PSIM software. The configuration of the inverter system and the proposed control scheme are depicted in Figure 2. The system parameters are listed in Table 1.

Parameters	Value	Units
DC-link voltage	420	V
Resistance (load bank)	24	Ω
Filter resistance	0.5	Ω
Filter capacitor	4.5	μF
Inverter-side filter inductance	1.7	mΗ
Grid-side filter inductance	0.9	mΗ
Grid voltage (line-to line rms)	220	V
Grid frequency	60	Hz

Table 1. System parameters of a grid-connected inverter.

Figure 5 represents three-phase distorted grid voltages used for the simulations. The abnormal grid voltages contain the harmonic components in the order of the 5th, 7th, 11th, and 13th with the magnitude of 5% with respect to the nominal grid voltages, which yields the THD value of 9.99%.



Figure 5. Distorted grid voltages. (**a**) Three-phase distorted grid voltages; (**b**) Fast Fourier transform (FFT) result of *a*-phase voltage.

Figure 6 shows the simulation results of the proposed current control scheme at steady-state under the distorted grid condition as in Figure 5. Figure 6a shows the grid-side current responses at the SRF with the reference currents. As can be observed from Figure 6a, the grid-side currents track the reference values well. Figure 6b,c represent the steady-state responses for the inverter-side current and capacitor voltage, respectively.

To demonstrate the transient performance of the proposed current control scheme, Figure 7 shows the simulation results under the same distorted grid condition when the *q*-axis reference current has a step change from 4 to 7 A at 0.25 s. Similarly, Figure 7a through Figure 7c represents the grid-side current responses, inverter-side current responses, and capacitor voltage responses at the SRF, respectively. As is shown in Figure 7a, the grid-side currents reach the reference very rapidly, which indicates a sufficiently fast transient response of the proposed control scheme. In addition, the fast transient performance of the proposed control scheme can be also inferred from Figure 7b,c.



Figure 6. Simulation results for steady-state responses under distorted grid voltage with the proposed controller. (**a**) Grid-side current responses at the synchronous reference frame (SRF); (**b**) Inverter-side current responses at the SRF; (**c**) Capacitor voltage responses at the SRF.



Figure 7. Simulation results for transient responses under distorted grid voltage with the proposed controller. (**a**) Current references and grid-side current responses at the synchronous reference frame (SRF); (**b**) inverter-side current responses at the SRF; (**c**) capacitor voltage responses at the SRF.

Figure 8 shows the simulation results for the inverter states and estimated states using the proposed integral-resonant state feedback control scheme with the discrete-time current full-state observer at the SRF. The optimal observer gains are obtained by using the MATLAB "dlqr" function with given inverter parameters. As can be clearly observed from Figure 8, the estimated states instantly converge to the actual ones even during oscillating transient periods, which confirms a fast and stable operation of the observer.

8

6

4

2

0

-2

8

6

4

2

0

-2

0.2

Current (A)

0.2

Current (A)

 i_2^q

 i_1^q





Figure 8. Simulation results for the proposed control scheme with the discrete-time full-state observer under step change in *q*-axis current reference; states and estimated states. (a) Waveforms of grid-side currents and estimated states at the SRF; (b) waveforms of inverter-side currents and estimated states at the SRF; (c) waveforms of capacitor voltages and estimated states at the SRF.

Figure 9 represents the simulation results for measured three-phase variables using the proposed integral-resonant state feedback control scheme with the discrete-time current full-state observer when the *q*-axis reference current has a step change. As can be seen from Figure 9a, three-phase grid-side current waveforms remain relatively sinusoidal with a desired transient performance. In fact, the



grid-side phase currents have the THD level of 3.57% in this case. Also, Figure 9b,c show actual three-phase inverter-side current waveforms and three-phase capacitor voltage waveforms.

Figure 9. Simulation results for measured three-phase variables with the proposed control scheme under step change in *q*-axis current reference. (a) Three-phase grid-side current waveforms; (b) three-phase inverter-side current waveforms; (c) three-phase capacitor voltage waveforms.

Figure 10 shows the simulation results for the estimated waveforms of three-phase grid-side currents, inverter-side currents, and capacitor voltages under the same condition of Figure 9. In these figures, the estimated three-phase variables are constructed in a DSP by using the estimated states at the SRF to demonstrate the estimating performance of the discrete-time current full-state observer.

Obviously, the estimated three-phase variables are compatible with the actual measured three-phase waveforms in Figure 9.

Figure 10. Simulation results for estimated three-phase variables with state observer under step change in *q*-axis current reference. (a) Estimated three-phase grid-side currents; (b) estimated three-phase inverter-side currents; (c) estimated three-phase capacitor voltages.

To verify the quality of injected grid currents for the proposed integral-resonant controller under a distorted grid voltage, Figure 11 shows the FFT result for grid-side *a*-phase current with the harmonic limits specified by the grid interconnection regulation IEEE Std. 1547 [29]. As can be seen clearly, the

grid-side phase current yields only small 5th, 7th, 11th, and 13th harmonics components. The resultant THD value is 3.569%, which meets the quality criteria of inverter injected current.

Figure 11. FFT result for grid-side *a*-phase current of the proposed controller under distorted grid voltages.

In order to verify the effectiveness of the proposed current control scheme, the performance of the proposed LQR-based current control is compared to PR plus harmonic compensator (PR + HC) structure presented in [3] under the same parameters as proposed in Table 1 and grid voltage condition presented in Figure 5a. The transfer function of a PI + HC controller is given in the stationary frame as:

$$G = K_P + \frac{K_{r1s}}{s^2 + \omega^2} + \frac{K_{r5s}}{s^2 + (5\omega)^2} + \frac{K_{r7s}}{s^2 + (7\omega)^2} + \frac{K_{r11s}}{s^2 + (11\omega)^2} + \frac{K_{r13s}}{s^2 + (13\omega)^2}$$
(40)

where K_P is the proportional gain and K_{ri} is the resonant gain with i = 1, 5, 7, 11, 13.

Figure 12 shows the simulation results for the control scheme in [3]. As can be observed from the grid-side current responses in Figure 12a, the PR + HC current control still can compensate effectively the harmonics caused by background voltages. However, the THD value of *a*-phase current is slightly increased to 3.69% in comparison to that obtained from the LCL filter parameters in [3] because the filter inductor values are reduced. Figure 12b presents the simulation results for the reference tracking performance of grid-side currents in the stationary frame.

Figure 12. Cont.

Figure 12. Simulation results for the proportional-resonant plus harmonic compensator (PR + HC) control scheme under step change. (a) Three-phase grid-side current waveforms; (b) current references and grid-side current responses at the stationary frame.

In spite of the control performance of the study in [3], it is worth mentioning that the main drawback of the work in [3] lies in the requirement for additional current sensing devices. As obviously shown in the control structure, the method in [3] requires the measurement of currents in inverter-side as well as in grid-side to obtain the capacitor current for active damping. Generally, since additional sensing devices cause complexity in the hardware and a high implementation cost, the purpose of this study is to implement a desired control performance by utilizing only the grid-side current sensors and grid voltage sensors.

Furthermore, the control method in [3] requires 5 resonant controllers including the fundamental component for the α -axis and additional 5 resonant controllers for the β -axis to compensate for the harmonic components in the order of the 5th, 7th, 11th, and 13th. On the contrary, since the proposed scheme is designed in the synchronous reference frame, the total of 4 resonant controllers are sufficient for the q- and d- axes to compensate the 6th and 12th order harmonic components. From the viewpoint of digital implementation burden, the proposed control scheme requires only an acceptable level of computation and complexity.

5. Experimental Results

In order to verify the feasibility of the proposed control scheme, the control algorithm is implemented on 32-bit floating-point DSP TMS320F28335 (Texas Instruments, Inc, Dallas, TX, USA) to control a 2 kVA prototype grid-connected inverter [30]. The configuration of the entire system is illustrated in Figure 13a. The sampling period is set to 100 µs, which results in the switching frequency of 10 kHz. Figure 13b depicts the photograph of the experimental test setup. The experimental setup is composed of a three-phase inverter connected to the grid through an LCL filter, a magnetic contactor for grid connecting operations, an AC power source to emulate three-phase grid voltages in the ideal as well as distorted grid conditions, and current and voltage sensors used to measure grid-side currents and grid voltages, respectively.

Figure 14a shows three-phase distorted grid voltages used for the experimental evaluation. Similar to Figure 5 in the simulation, these grid voltages contain the 5th, 7th, 11th, and 13th harmonics with the magnitude of 5% of the fundamental component. Figure 14b presents the FFT results for *a*-phase grid voltage, which shows each harmonic component similar to Figure 5b.

Figure 13. Configuration of the experimental system. (a) Block diagram of the overall system; (b) photograph of the experimental test setup.

Figure 15 shows the experimental results for the proposed control scheme under the step change in *q*-axis current reference from 4 to 6 A. It can be observed from Figure 15a that the inverter output currents can track their references well and instantly reach a new steady-state value, which demonstrates a fast transient response of the proposed control scheme. Figure 15b shows three-phase grid-side current responses. It is confirmed from this figure that the proposed control scheme provides considerable sinusoidal grid-side phase currents, which coincides well with the simulation results in Figure 9a, verifying a stable and reliable operation of the inverter system.

Figure 14. Distorted three-phase grid voltages used in the experiments. (**a**) Three-phase distorted grid voltages; (**b**) FFT result of *a*-phase grid voltage.

Figure 15. Cont.

Figure 15. Experimental results for the proposed control scheme under step change in *q*-axis current reference. (a) Grid-side current responses at the SRF; (b) three-phase grid-side current responses.

Figure 16 presents the experimental results for the estimated grid-side currents, estimated inverter-side currents, and estimated capacitor voltages by using the discrete-time current full-state observer at the SRF under the step change in *q*-axis current reference. The estimated grid-side currents at the SRF in Figure 16a show similar behavior with actual states in Figure 15a. Also, the experimental estimated waveforms are very similar to the simulation results in Figure 8.

Figure 16. Cont.

Figure 16. Experimental results for the estimated states with the discrete-time full-state observer under step change in *q*-axis current reference. (**a**) Responses of estimated grid-side currents at the SRF; (**b**) responses of estimated inverter-side currents at the SRF; (**c**) responses of estimated capacitor voltages at the SRF.

Figure 17a shows the experimental results for three-phase grid-side current waveforms at steady-state with the proposed current control scheme under harmonically distorted grid conditions as in Figure 14. Generally, the harmonic distortion on grid voltages directly influence on the grid-side current control performance, reducing the power quality of distributed generation system. However, the three-phase grid-side current waveform of the proposed scheme shows quite sinusoidal phase currents in spite of such a severe harmonic distortion on grid voltages. As is shown in Figure 17b, the FFT result for *a*-phase current shows negligibly small harmonic components in output current, which successfully meets the requirements for the harmonic limits specified by IEEE Standard 519-1992.

Figure 17. Experimental results for the proposed control scheme at steady-state. (**a**) Three-phase grid-side current waveforms; (**b**) FFT result for *a*-phase current.

Figure 18 represents the experimental results for the estimating performance of the discrete-time current full-state observer. To evaluate the estimating performance of the observer by comparison, Figure 18a,b show the estimating performance for grid-side three-phase currents and the comparison of these estimated signals with measured grid-side currents. In these figures, the estimated three-phase variables \hat{i}_2^a , \hat{i}_2^b , and \hat{i}_2^c are constructed in a DSP by using the estimated states \hat{i}_2^q and \hat{i}_2^d at the SRF. The experimental results are well matched with the simulation results in Figure 10a and validate the stability and reliability of the estimated states by the current full-state observer. Similarly, Figure 18c,d show the estimating performance for inverter-side three-phase currents, and Figure 18e,f for three-phase capacitor voltages, respectively. Also, the estimated three-phase variables \hat{i}_1^a , \hat{i}_2^b , and \hat{v}_c^c from the estimated currents \hat{i}_1^a and \hat{i}_1^a at the SRF, and \hat{v}_c^a , \hat{v}_c^b , and \hat{v}_c^c from the estimated currents \hat{v}_1^q and \hat{t}_1^a at the SRF, and \hat{v}_c^a , \hat{v}_c^b , and \hat{v}_c^c from the estimated currents \hat{v}_1^q and \hat{t}_1^a at the SRF, and \hat{v}_c^a , \hat{v}_c^b , and \hat{v}_c^c from the estimated currents \hat{v}_1^q and \hat{v}_1^q at the SRF, respectively. It can be confirmed from these figures that all the estimated three-phase variables converge to actual measured three-phase variables well.

Figure 18. Cont.

Figure 18. Experimental results for the estimating performance with the discrete-time full-state observer. (**a**) Estimating performance for grid-side three-phase currents; (**b**) comparison of the estimated grid-side currents and measured grid-side currents; (**c**) estimating performance for inverter-side three-phase currents; (**d**) comparison of the estimated inverter-side currents and measured inverter-side currents; (**e**) estimating performance for three-phase capacitor voltages; (**f**) comparison of the estimated capacitor voltage and measured capacitor voltage.

6. Conclusions

This paper has presented an LQR-based current control design for an LCL-filtered grid-connected inverter. The proposed control scheme has been constructed by using the IM principle in the SRF to augment integral and resonant terms into a state feedback control. As a result, the control scheme successfully achieves control objectives such as an asymptotic reference tracking and disturbance rejection, which significantly reduces the impact of grid voltage distortion on the output current. Moreover, since the proposed scheme is implemented in the SRF, both the negative and positive sequence harmonics can be effectively compensated for by only one resonant term, which leads to a decrease in the number of regulators. This feature is usually preferred because the required THD performance can be met with further reduction in computation efforts. Furthermore, with the aim of avoiding the increase of sensing devices in an LCL-filtered grid-connected inverter system in comparison with L-filtered counterpart, a current full-state observer in the discrete-time domain has been discussed in detail to estimate all the state variables. On account of the augmentation of the resonant terms as well as the integral term into the inverter model, an increased number of feedback gains should be selected. To deal with such a limitation, an optimal LQR approach is adopted as a way of choosing the feedback gains systematically, in which the discrete cost function is minimized to satisfy the stability and robustness requirements of the system. As a result, both the system feedback and observer gains can be selected based on the LQR method in an effective and straightforward way. In addition to that, the control objectives such as the reference tracking and harmonic compensation capability can be effectively achieved without increasing the required number of sensing devices.

In order to evaluate the feasibility and validity of the proposed control scheme, the whole control algorithm is implemented on 32-bit floating-point DSP TMS320F28335 to control 2 kVA prototype grid-connected inverter. Comprehensive simulation and experimental results are presented under distorted grid voltage conditions to demonstrate the usefulness of the proposed current control scheme.

Author Contributions: T.V.T., S.-J.Y., and K.-H.K. conceived the main concept of the control structure and developed the entire system. T.V.T. and S.-J.Y. carried out the research and analyzed the numerical data with guidance from K.-H.K. T.V.T., S.-J.Y., and K.-H.K. collaborated in the preparation of the manuscript.

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